



1 CHANNEL BIDIRECTIONAL TVS

Product Summary

V _{SB} Min	IPP Max	Ст тур
2.8V	5A	11pF

Description

Designed to replace multilayer varistors (MLVs) in portable applications where low operating voltage is vital, DIODES™ D3V3L1B2WS offers superior electrical characteristics such as lower clamping voltage and no device degradation when compared to MLVs. D3V3L1B2WS is designed to protect sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD), lightning, electrical fast transients (EFT), and cable discharge events (CDE).

Applications

- Cellular handsets & accessories
- Notebooks & handhelds
- Portable instrumentation
- Digital cameras
- Peripherals
- MP3 players

SOD323



Top View

Features

- Provides ESD Protection per IEC 61000-4-2 Standard: Air ±26kV, Contact ±26kV
- 1 Channel of ESD Protection
- Protects One Power or I/O Line
- Max. Peak Pulse Power: Ppp = 90W at tp = 8/20µs
- Low Clamping Voltage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part.
 A listing can be found at

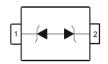
https://www.diodes.com/products/automotive/automotive-products/.

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: SOD323
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead-Free Plating). Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.004 grams (Approximate)



Device Schematic

Ordering Information (Note 4)

Ī	Part Number	Dookogo	ckage Marking Reel Size (inche		Tape Width (mm)	Packing	
	Part Number	Package	Warking	Reel Size (inches)	rape widin (ililii)	Qty.	Carrier
	D3V3L1B2WS-7	SOD323	V/Λ	7	8	3,000	Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



V / Λ = Product Type Marking Code

D3V3L1B2WS 1 of 5
Document number: DS44535 Rev. 1 - 2 www.diodes.com



Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Power Dissipation	P _{PP}	90	W	8/20µs, per Figure 3
Peak Pulse Current	IPP	5	Α	8/20µs, per Figure 3
ESD Protection – Contact Discharge	VESD_Contact	±26	kV	Standard IEC 61000-4-2
ESD Protection – Air Discharge	V _{ESD_Air}	±26	kV	Standard IEC 61000-4-2

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Package Power Dissipation (Note 5)	PD	250	mW
Thermal Resistance, Junction to Ambient (Note 5)	R _{OJA}	500	°C/W
Operating Temperature Range	TJ	-55 to +125	°C
Storage Temperature Range	T _{STG}	-55 to +150	°C
Soldering Temperature, t max = 10s	TL	+260	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Working Voltage	V_{RWM}	_	_	3.3	V	_
Reverse Current (Note 5)	I _R	_	0.05	0.5	μΑ	$V_R = V_{RWM} = 3.3V$
Punch Through Voltage	V _{PT}	3.5	_	_	V	I _R = 2μA
Snap-Back Voltage	V _{SB}	2.8	_	_		I _R = 50mA
Reverse Clamping Voltage	V	_	6.0	8.0	V	$I_{PP} = 1A, t_p = 8/20 \mu s$
	V _{CL}	_	8.5	18		$I_{PP} = 5A, t_p = 8/20 \mu s$
Capacitance	Ст	_	11	15	pF	$V_R = 0V$, $f = 1MHz$

Note: 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown in Diodes Incorporated's package outline PDFs, which can be found on our website at http://www.diodes.com/package-outlines.html.



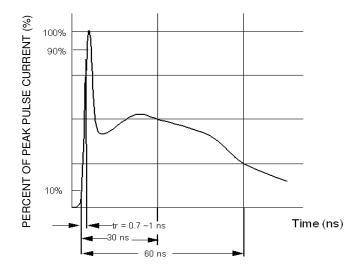


Figure 1. ESD Pulse Waveform According to IEC 61000-4-2

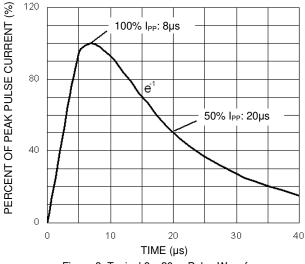


Figure 3. Typical 8 x $20\mu s$ Pulse Waveform

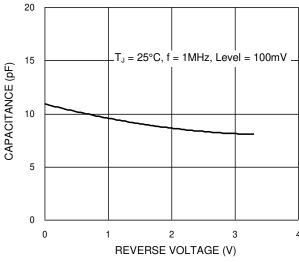


Figure 5. Typical Junction Capacitance

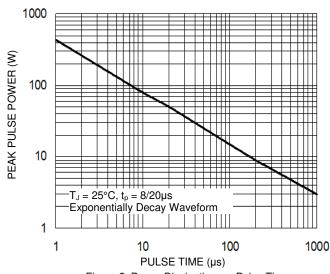
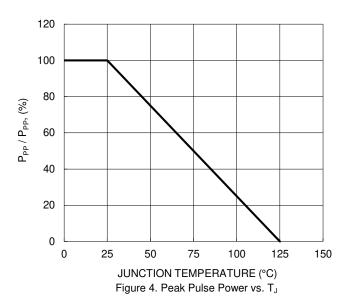


Figure 2. Power Dissipation vs. Pulse Time



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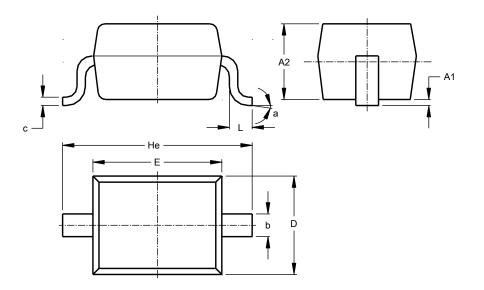
Figure 6. Reverse Leakage Current vs. T_J



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOD323

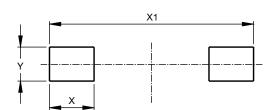


1	COL	1222				
SOD323						
Dim	Min	Max	Тур			
A 1		0.10	0.05			
A2	1.00	1.10	1.05			
b	0.25	0.35	0.30			
С	0.10	0.15	0.11			
D	1.20	1.40	1.30			
Е	1.60	1.80	1.70			
He	2.30	2.70	2.50			
L	0.20	0.40	0.30			
а	0₀	8º				
All Dimensions in mm						

Suggested Pad Layout

 $Please \ see \ http://www.diodes.com/package-outlines.html \ for \ the \ latest \ version.$

SOD323



Dimensions	Value (in mm)
Х	0.590
X1	2.700
V	0.450



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D3V3L1B2WS
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