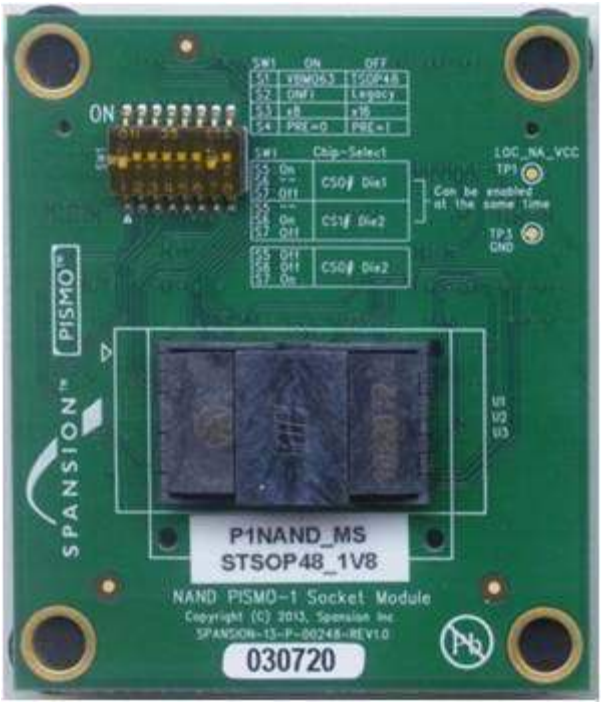

CYFL248-002 PISMO1 Development Module – S34MS NAND x8 and x16 on TSOP48 Package

Last Updated:
Oct 21, 2015

PISMO™ (Platform Independent Storage Module) is an industry-standard memory interface designed specifically for development platforms. PISMO enables a streamlined method for memory validation by providing development platforms with a single common interface for multiple Flash memory devices.

Cypress offers comprehensive support of the PISMO standard with its own family of PISMO standard-compliant storage modules. Governed by an independent standards body known as the PISMO Advisory Council, the PISMO specification defines a single, common Flash memory interface for development platforms that eliminates the need for multiple, proprietary memory interfaces. As a result, the standard enables faster development cycles, quicker memory qualification, and lower product costs with investment protection.

PISMO standard-compliant products allow engineering teams to qualify multiple Flash memory devices from a single development board, and to reuse the PISMO connector for qualifying new process geometries, enabling future cost-reduced Flash memory devices for the market.

Features of the CYFL248-002 board:

- Supports 1.8V x8 and x16 NAND Flash Memory on socket – TSOP48 package.
- Up to 16 IO signal lines.
- 10 control signal lines.
- Debug support simplifies the design of the evaluation systems without the added complexity of costly logic analyzer sockets on every board

HARDWARE REQUIREMENTS

- Requires FSK-PUP Flash Programming Tool (CYFL135-001) for operation

CYFL248-002 - SOFTWARE EXAMPLES

The following software is for demonstration purposes only. It is not fully tested, nor validated in order to fulfill its task under all circumstances. Therefore, this software or any part of it must only be used in an evaluation laboratory environment.

Please check the Disclaimer.