onsemi

Dual Switching Diode BAV70T, NSVBAV70T

Features

- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (T_A = 25° C)

Rating	Symbol	Max	Unit
Reverse Voltage	V _R	100	Vdc
Forward Current	١ _F	200	mAdc
Peak Forward Surge Current	I _{FM(surge)}	500	mAdc

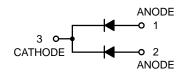
THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation, FR-4 Board (Note 1) $T_A = 25^{\circ}C$	P _D	225	mW
Derated above 25°C		1.8	m₩/°C
Thermal Resistance, Junction to Ambient (Note 1)	R_{\thetaJA}	555	°C/W
Total Device Dissipation, FR-4 Board (Note 2) $T_A = 25^{\circ}C$	PD	360	mW
Derated above 25°C		2.9	mW/°C
Thermal Resistance, Junction-to-Ambient (Note 2)	R_{\thetaJA}	345	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	–55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. FR-4 @ Minimum Pad

2. FR-4 @ 1.0 × 1.0 Inch Pad



MARKING DIAGRAM



A4 = Specific Device Code

ORDERING INFORMATION

Device	Package	Shipping [†]
BAV70TT1G	SOT–416 (Pb-Free)	3000 / Tape & Reel
NSVBAV70TT1G	SOT-416 (Pb-Free)	3000 / Tape & Reel
NSVBAV70TT3G	SOT-416 (Pb-Free)	10000 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

M = Date Code

⁼ Pb-Free Package

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ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit	
OFF CHARACTERISTICS					
Reverse Breakdown Voltage (I _(BR) = 100 μAdc)	V _(BR)	100	-	Vdc	
Reverse Voltage Leakage Current (Note 3) ($V_R = 100 \text{ Vdc}$) ($V_R = 50 \text{ Vdc}$)	I _R I _R		1.0 100	μAdc nAdc	
Diode Capacitance $(V_R = 0, f = 1.0 \text{ MHz})$	CD	-	1.5	pF	
Forward Voltage $(I_F = 1.0 \text{ mAdc})$ $(I_F = 10 \text{ mAdc})$ $(I_F = 50 \text{ mAdc})$ $(I_F = 150 \text{ mAdc})$	VF	- - - -	715 855 1000 1250	mVdc	
Reverse Recovery Time ($I_F = I_R = 10$ mAdc, $R_L = 100 \Omega$, $I_{R(REC)} = 1.0$ mAdc) (Figure 1)	t _{rr}	-	6.0	ns	
Forward Recovery Voltage $(I_F = 10 \text{ mAdc}, t_r = 20 \text{ ns})$ (Figure 2)	V _{RF}	-	1.75	V	

3. For each individual diode while the second diode is unbiased.

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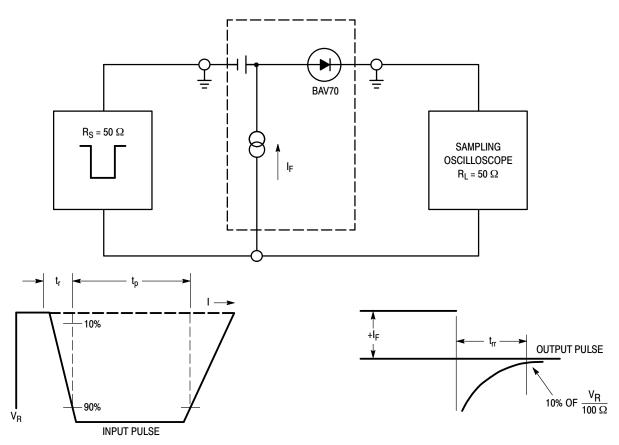
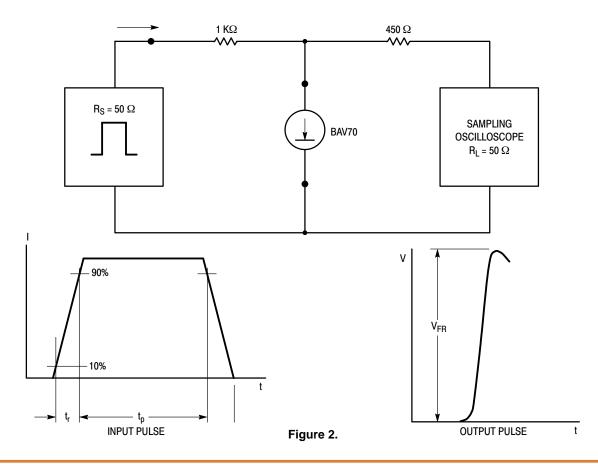
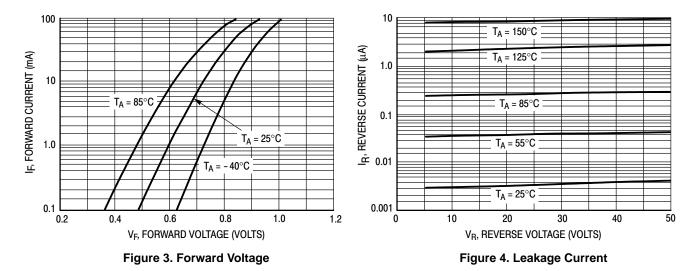
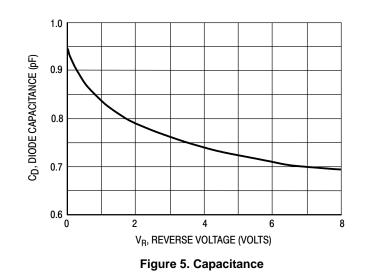


Figure 1. Recovery Time Equivalent Test Circuit



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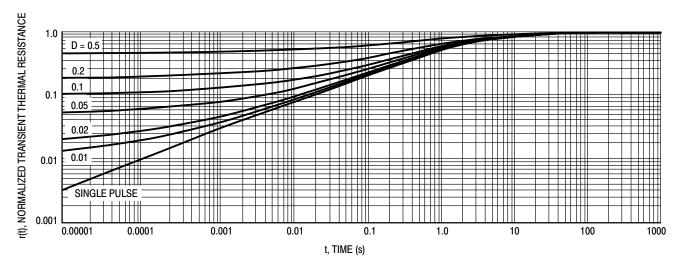
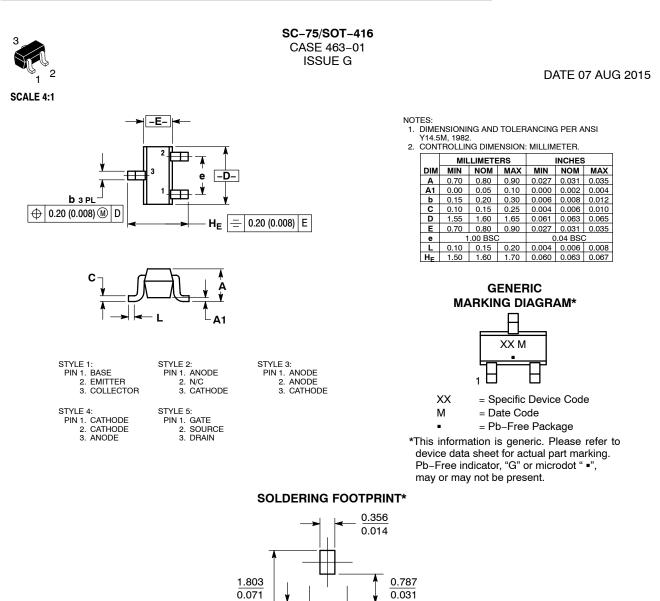


Figure 6. Normalized Thermal Response





*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

1.000

0.039

SCALE 10:1

mm

inches

0.508

0.020

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