



N-CHANNEL ENHANCEMENT MODE MOSFET

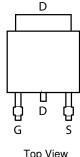
Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

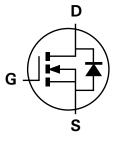
Mechanical Data

- Case: TO252 (DPAK)
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Matte Tin Finish annealed over Copper leadframe.
 Solderable per MIL-STD-202, Method 208 63
- Weight: 0.33 grams (approximate)









Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMG4800LK3-13	TO252	2500 / Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com.

Marking Information



Oll = Manufacturer's Marking
N4800L = Product Type Marking Code
YYWW = Date Code Marking
YY = Year (ex: 09 = 2009)
WW = Week (01 - 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Char	Symbol	Value	Unit		
Drain-Source Voltage	V_{DSS}	30	V		
Gate-Source Voltage			V_{GSS}	±25	V
Continuous Drain Current (Note 5)	Steady State	$T_A = +25^{\circ}C$ $T_A = +85^{\circ}C$	I _D	10.0 6.5	А
Pulsed Drain Current (Note 6)			I _{DM}	48	Α

Thermal Characteristics

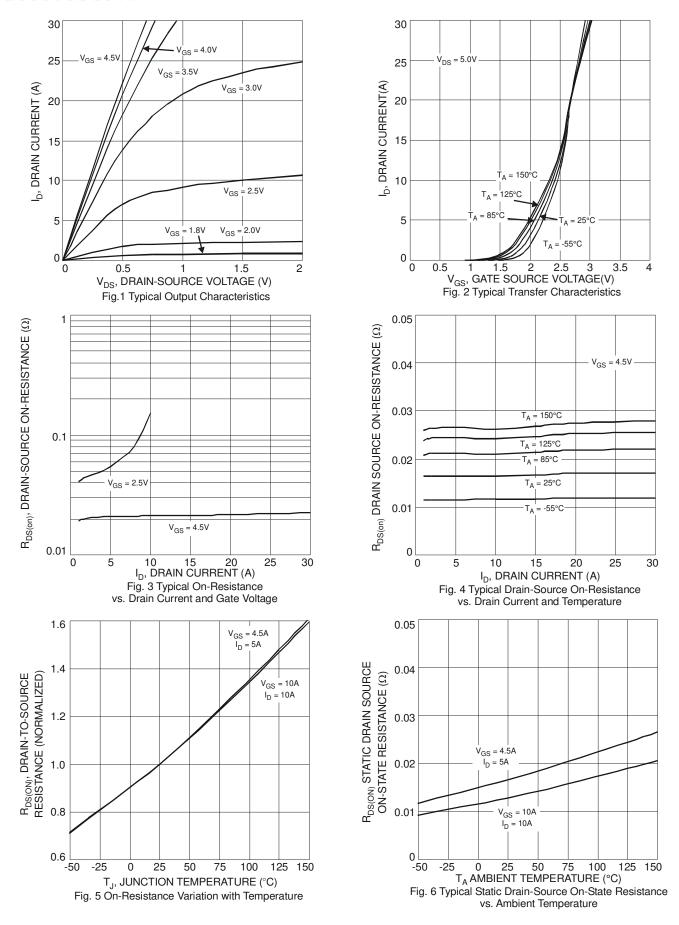
Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P_{D}	1.71	W
Thermal Resistance, Junction to Ambient @T _A = +25°C	$R_{\theta JA}$	72.9	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

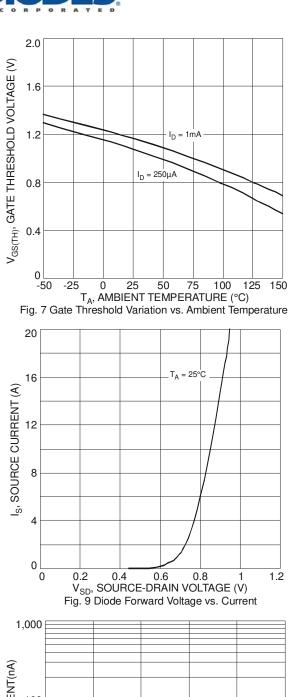
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I_{DSS}	-	-	1.0	μA	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(th)}	0.8	-	1.6	٧	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance			12	17	mΩ	$V_{GS} = 10V, I_D = 9A$	
Static Dialii-Source Off-Nesistance	R _{DS (ON)}	-	16	24	11122	$V_{GS} = 4.5V, I_D = 7A$	
Forward Transfer Admittance	Y _{fs}	-	10	-	S	$V_{DS} = 10V, I_{D} = 9A$	
Diode Forward Voltage	V_{SD}	-	0.7	1.0	V	$V_{GS} = 0V, I_{S} = 1A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}	-	798	-	рF	V 40V V 0V	
Output Capacitance	Coss	-	128	-	рF	$V_{DS} = 10V, V_{GS} = 0V,$ -f = 1.0MHz	
Reverse Transfer Capacitance	C_{rss}	-	122	-	pF	11 = 1.0IVIH2	
Gate Resistance	R_g	-	1.37	-	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Q_{g}	-	8.7	-	nC	V _{GS} = 5V, V _{DS} = 15V, I _D = 9A	
Gate-Source Charge	Q_{gs}	-	1.7	-	nC		
Gate-Drain Charge	Q_{gd}	-	2.4	-	nC		
Turn-On Delay Time	t _{D(on)}	-	5.03	-	ns		
Turn-On Rise Time	t _r	-	4.50	-	ns	$V_{DD} = 15V, V_{GS} = 10V,$ $R_{L} = 15\Omega, R_{G} = 6\Omega, I_{D} = 1A$	
Turn-Off Delay Time	t _{D(off)}	-	26.33	-	ns		
Turn-Off Fall Time	t _f	-	8.55	-	ns		

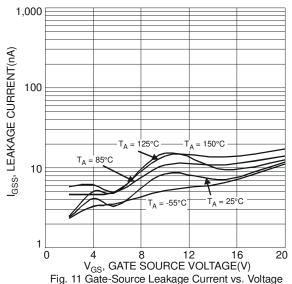
- 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
- Repetitive rating, pulse width limited by junction temperature.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing.

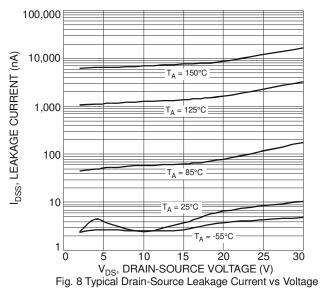


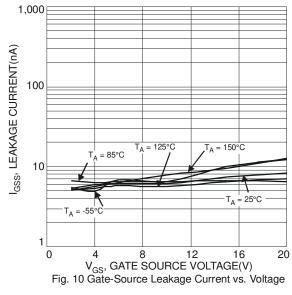












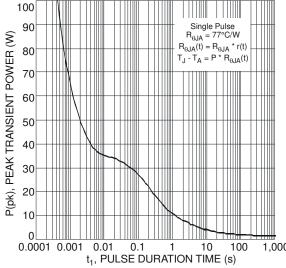
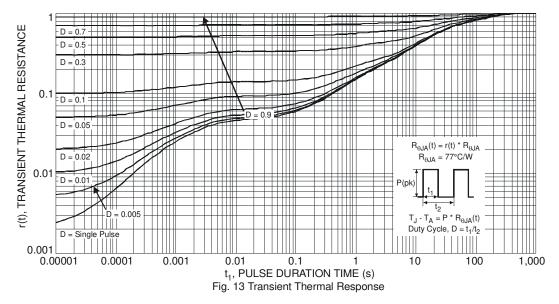


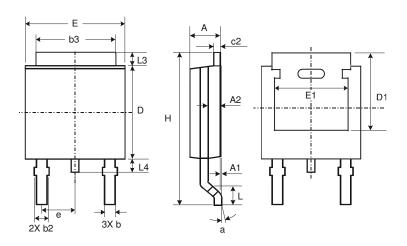
Fig. 12 Single Pulse Maximum Power Dissipation





Package Outline Dimensions

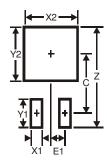
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



TO252						
Dim	Min	Max Ty				
Α	2.19	2.39	2.29			
A 1	0.00	0.13	0.08			
A2	0.97	1.17	1.07			
b	0.64	0.88	0.783			
b2	0.76	1.14	0.95			
b3	5.21	5.46	5.33			
c2	0.45	0.58	0.531			
D	6.00	6.20	6.10			
D1	5.21	_	_			
е	-	_	2.286			
Е	6.45	6.70	6.58			
E1	4.32	_	_			
Н	9.40	10.41	9.91			
L	1.40	1.78	1.59			
L3	0.88	1.27	1.08			
L4	0.64	1.02	0.83			
а	0°	10°				
All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	11.6
X1	1.5
X2	7.0
Y1	2.5
Y2	7.0
С	6.9
E1	2.3



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