

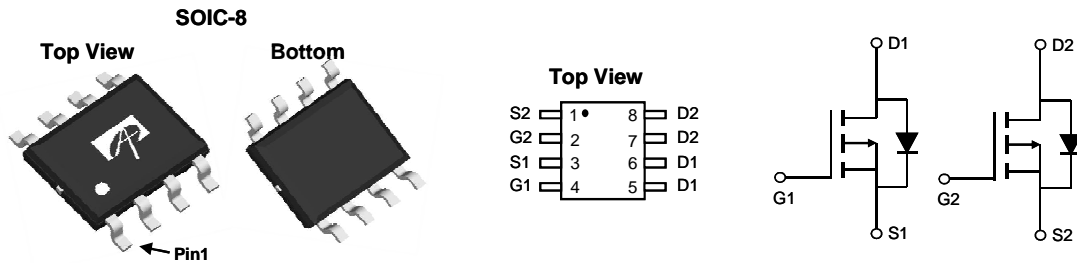
**General Description**

The AO4807 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , and ultra-low low gate charge. This device is suitable for use as a load switch or in PWM applications.

**Product Summary**

$V_{DS}$	-30V
$I_D$ (at $V_{GS}=-10V$ )	-6A
$R_{DS(ON)}$ (at $V_{GS}=-10V$ )	< 35m $\Omega$
$R_{DS(ON)}$ (at $V_{GS} = -4.5V$ )	< 58m $\Omega$

100% UIS Tested  
 100%  $R_g$  Tested


**Absolute Maximum Ratings  $T_A=25^\circ\text{C}$  unless otherwise noted**

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D$	$T_A=25^\circ\text{C}$	-6
		$T_A=70^\circ\text{C}$	-5
Pulsed Drain Current <sup>C</sup>	$I_{DM}$	-30	A
Avalanche Current <sup>C</sup>	$I_{AS}, I_{AR}$	23	A
Avalanche energy $L=0.1\text{mH}$ <sup>C</sup>	$E_{AS}, E_{AR}$	26	mJ
Power Dissipation <sup>B</sup>	$P_D$	$T_A=25^\circ\text{C}$	2
		$T_A=70^\circ\text{C}$	1.3
Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	$^\circ\text{C}$

**Thermal Characteristics**

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient <sup>A</sup>	$R_{\theta JA}$	48	62.5	$^\circ\text{C}/\text{W}$
Maximum Junction-to-Ambient <sup>A D</sup>		74	90	$^\circ\text{C}/\text{W}$
Maximum Junction-to-Lead	$R_{\theta JL}$	32	40	$^\circ\text{C}/\text{W}$

**Electrical Characteristics (T<sub>J</sub>=25°C unless otherwise noted)**

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>STATIC PARAMETERS</b>						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	I <sub>D</sub> =-250μA, V <sub>GS</sub> =0V	-30			V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V T <sub>J</sub> =55°C			-1 -5	μA
I <sub>GSS</sub>	Gate-Body leakage current	V <sub>DS</sub> =0V, V <sub>GS</sub> = ±20V			±100	nA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1.3	-1.85	-2.4	V
I <sub>D(ON)</sub>	On state drain current	V <sub>GS</sub> =-10V, V <sub>DS</sub> =-5V	-30			A
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =-10V, I <sub>D</sub> =-6A T <sub>J</sub> =125°C		21	35	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-5A		33	58	
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> =-5V, I <sub>D</sub> =-6A		19		S
V <sub>SD</sub>	Diode Forward Voltage	I <sub>S</sub> =-1A, V <sub>GS</sub> =0V		-0.8	-1	V
I <sub>S</sub>	Maximum Body-Diode Continuous Current				-3.5	A
<b>DYNAMIC PARAMETERS</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V, V <sub>DS</sub> =-15V, f=1MHz		760		pF
C <sub>oss</sub>	Output Capacitance			140		pF
C <sub>riss</sub>	Reverse Transfer Capacitance			95		pF
R <sub>g</sub>	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz	1.5	3.2	5.0	Ω
<b>SWITCHING PARAMETERS</b>						
Q <sub>g(10V)</sub>	Total Gate Charge	V <sub>GS</sub> =-10V, V <sub>DS</sub> =-15V, I <sub>D</sub> =-6A		13.6	16	nC
Q <sub>g(4.5V)</sub>	Total Gate Charge			6.7	8	nC
Q <sub>gs</sub>	Gate Source Charge			2.5		nC
Q <sub>gd</sub>	Gate Drain Charge			3.2		nC
t <sub>D(on)</sub>	Turn-On DelayTime	V <sub>GS</sub> =-10V, V <sub>DS</sub> =-15V, R <sub>L</sub> =2.7Ω, R <sub>GEN</sub> =3Ω		8		ns
t <sub>r</sub>	Turn-On Rise Time			6		ns
t <sub>D(off)</sub>	Turn-Off DelayTime			17		ns
t <sub>f</sub>	Turn-Off Fall Time			5		ns
t <sub>rr</sub>	Body Diode Reverse Recovery Time	I <sub>F</sub> =-6A, di/dt=100A/μs		15		ns
Q <sub>rr</sub>	Body Diode Reverse Recovery Charge	I <sub>F</sub> =-6A, di/dt=100A/μs		9.7		nC

A. The value of R<sub>θJA</sub> is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with T<sub>A</sub>=25° C. The value in any given application depends on the user's specific board design.

B. The power dissipation P<sub>D</sub> is based on T<sub>J(MAX)</sub>=150° C, using ≤ 10s junction-to-ambient thermal resistance.

C. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150° C. Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub>=25° C.

D. The R<sub>θJA</sub> is the sum of the thermal impedance from junction to lead R<sub>θJL</sub> and lead to ambient.

E. The static characteristics in Figures 1 to 6 are obtained using <300μs pulses, duty cycle 0.5% max.

F. These curves are based on the junction-to-ambient thermal impedance which is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, assuming a maximum junction temperature of T<sub>J(MAX)</sub>=150° C. The SOA curve provides a single pulse rating.

THIS PRODUCT HAS BEEN DESIGNED AND QUALIFIED FOR THE CONSUMER MARKET. APPLICATIONS OR USES AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS ARE NOT AUTHORIZED. AOS DOES NOT ASSUME ANY LIABILITY ARISING OUT OF SUCH APPLICATIONS OR USES OF ITS PRODUCTS. AOS RESERVES THE RIGHT TO IMPROVE PRODUCT DESIGN, FUNCTIONS AND RELIABILITY WITHOUT NOTICE.

**TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**

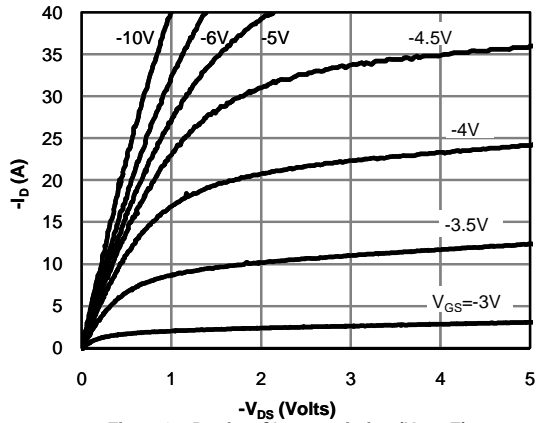


Fig 1: On-Region Characteristics (Note E)

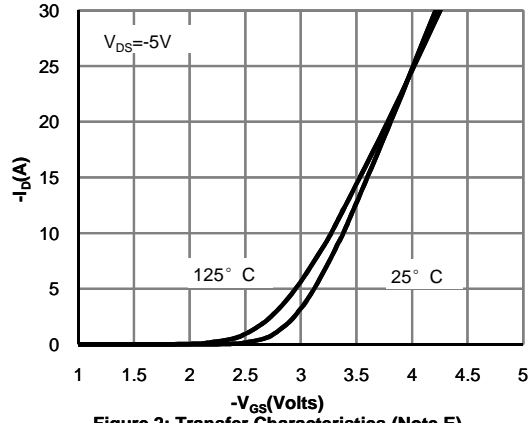


Figure 2: Transfer Characteristics (Note E)

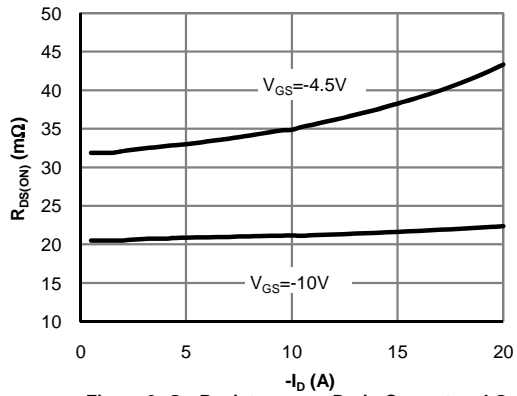


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

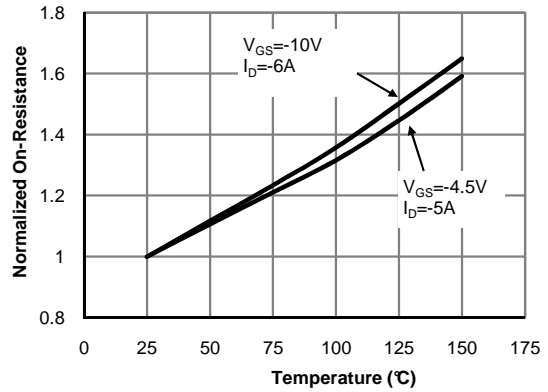


Figure 4: On-Resistance vs. Junction Temperature (Note E)

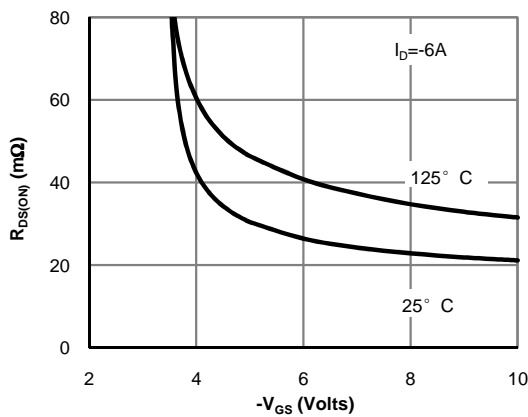


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

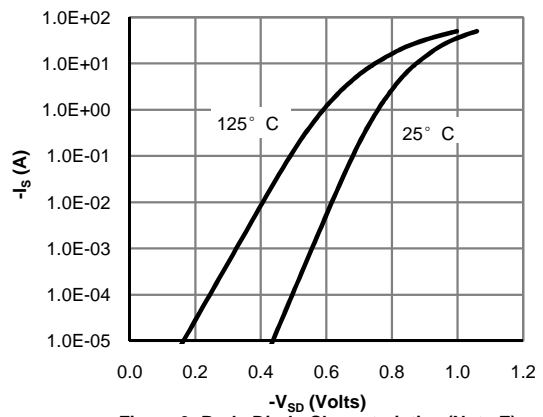
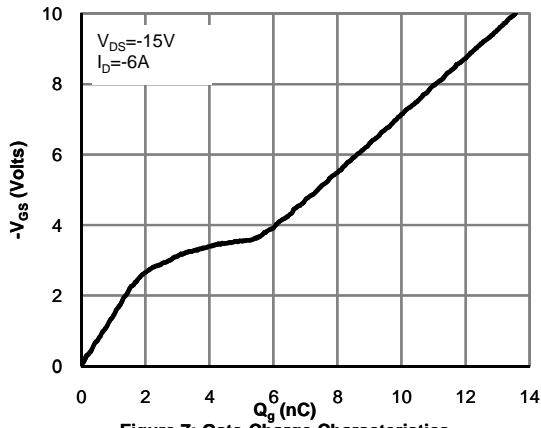
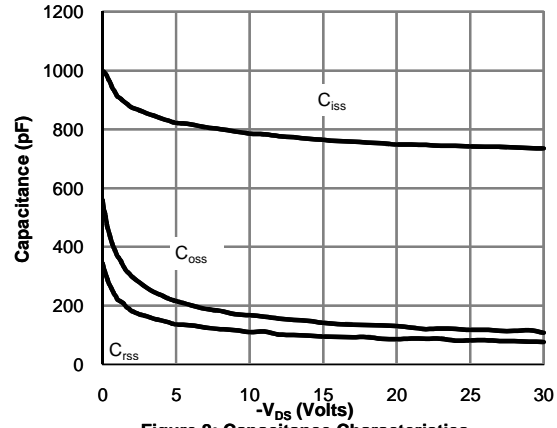


Figure 6: Body-Diode Characteristics (Note E)

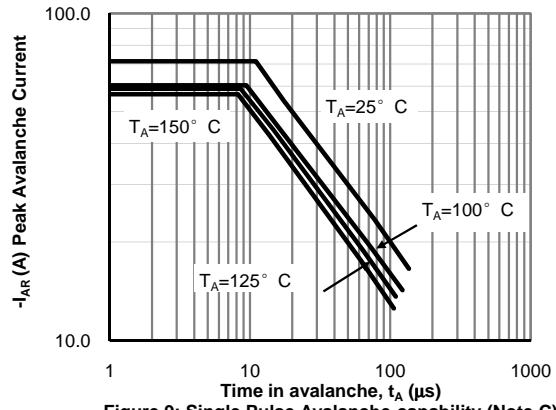
**TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**



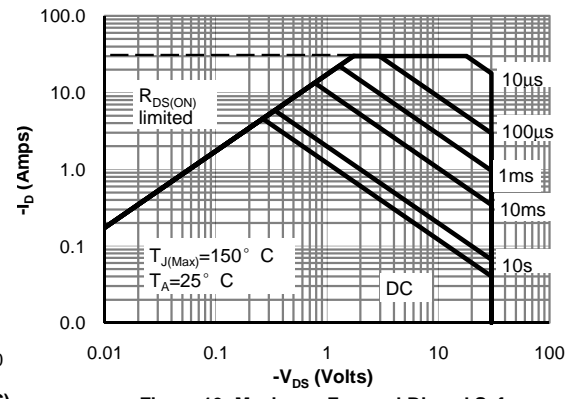
**Figure 7: Gate-Charge Characteristics**



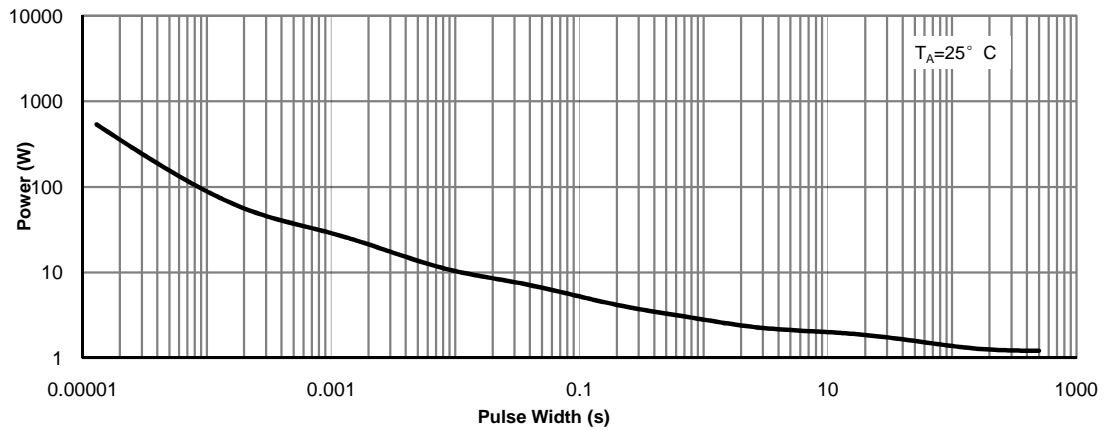
**Figure 8: Capacitance Characteristics**



**Figure 9: Single Pulse Avalanche capability (Note C)**

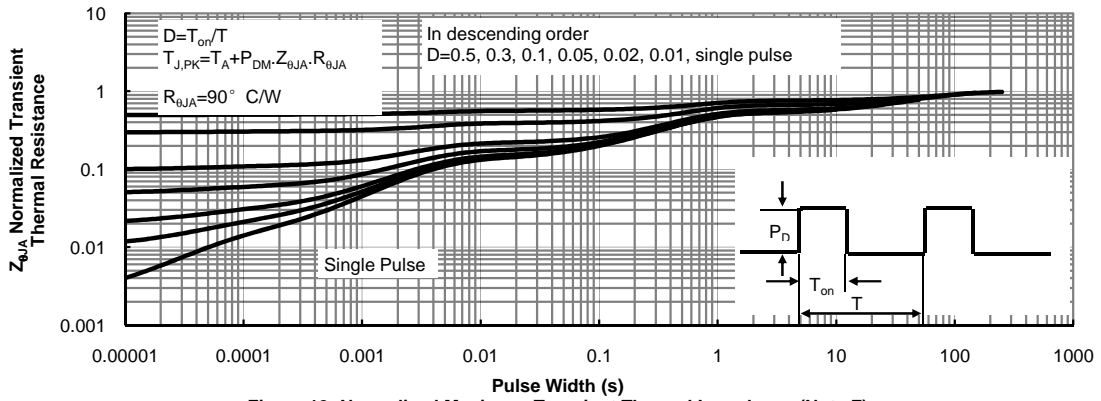


**Figure 10: Maximum Forward Biased Safe Operating Area (Note F)**



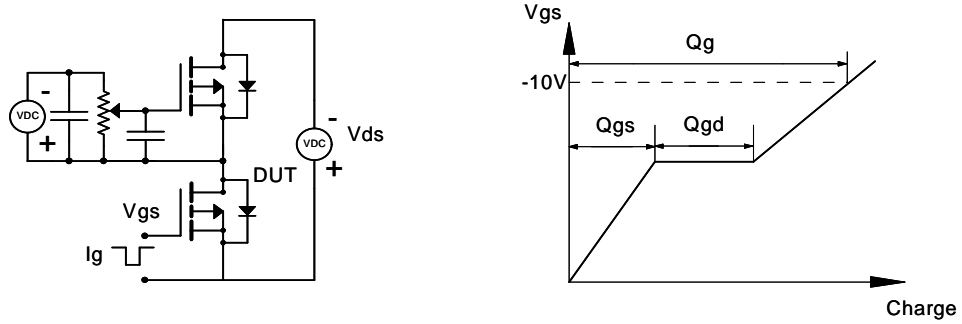
**Figure 11: Single Pulse Power Rating Junction-to-Ambient (Note F)**

**TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS**

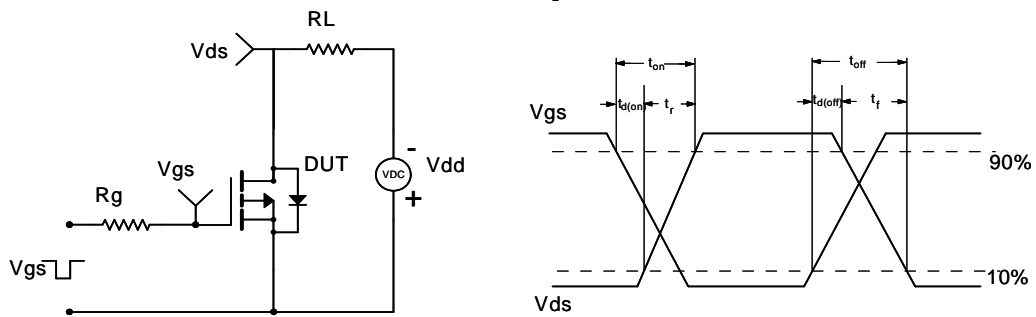


**Figure 12: Normalized Maximum Transient Thermal Impedance (Note F)**

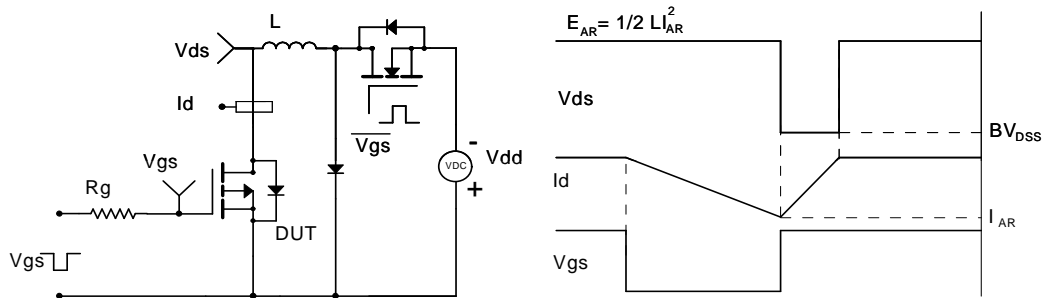
**Gate Charge Test Circuit & Waveform**



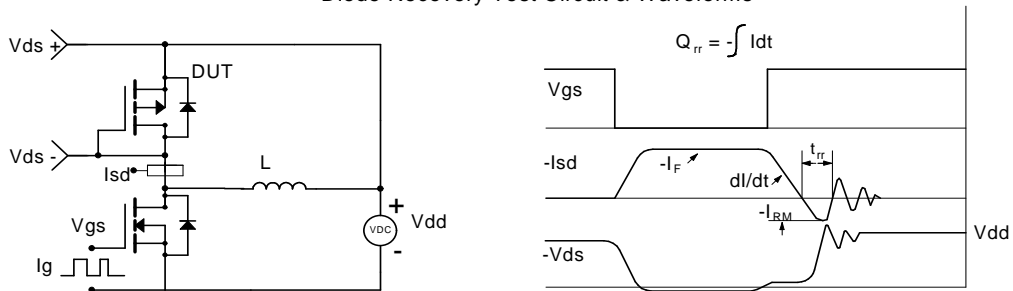
**Resistive Switching Test Circuit & Waveforms**



**Unclamped Inductive Switching (UIS) Test Circuit & Waveforms**



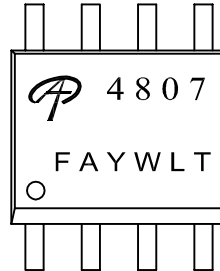
**Diode Recovery Test Circuit & Waveforms**





Document No.	PD-00035
Version	F
Title	AO4807 Marking Description

SO-8 PACKAGE MARKING DESCRIPTION



Green product

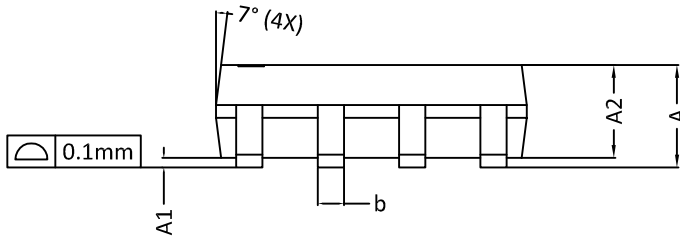
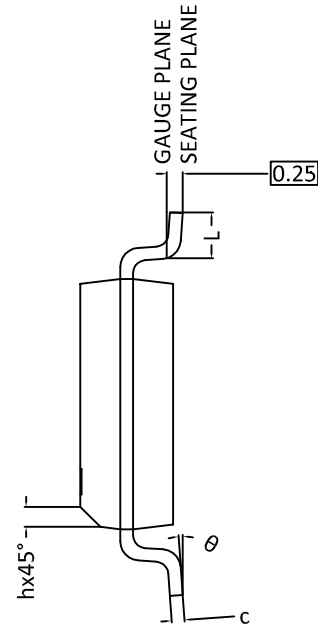
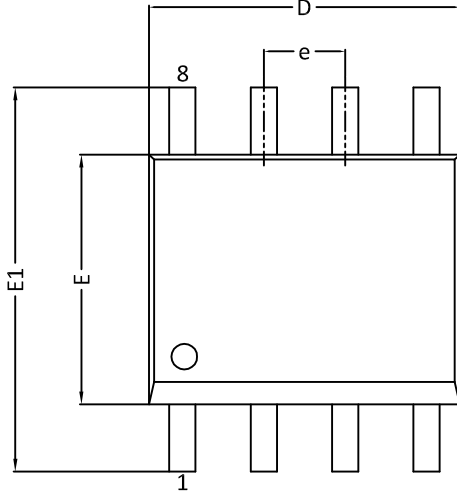
NOTE:

- LOGO - AOS Logo
- 4807 - Part number code
- F - Fab code
- A - Assembly location code
- Y - Year code
- W - Week code
- L&T - Assembly lot code

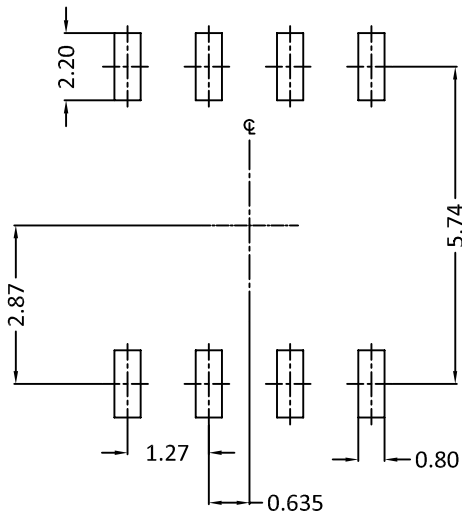
PART NO.	DESCRIPTION	CODE
AO4807	Green product	4807
AO4807L	Green product	4807



### SO8(SOP-8L) PACKAGE OUTLINE



### RECOMMENDED LAND PATTERN



SYMBOLS	DIMENSION IN MM			DIMENSION IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.35	1.65	1.75	0.053	0.065	0.069
A1	0.10	0.15	0.25	0.004	0.006	0.010
A2	1.25	1.50	1.65	0.049	0.059	0.065
b	0.31	0.41	0.51	0.012	0.016	0.020
c	0.17	0.20	0.25	0.007	0.008	0.010
D	4.80	4.90	5.00	0.189	0.193	0.197
E	3.80	3.90	4.00	0.150	0.154	0.157
E1	5.80	6.00	6.20	0.228	0.236	0.244
e	1.27 BSC			0.050 BSC		
h	0.25	0.30	0.50	0.010	0.012	0.020
L	0.40	0.69	1.27	0.016	0.027	0.050
θ	0°	4°	8°	0°	4°	8°

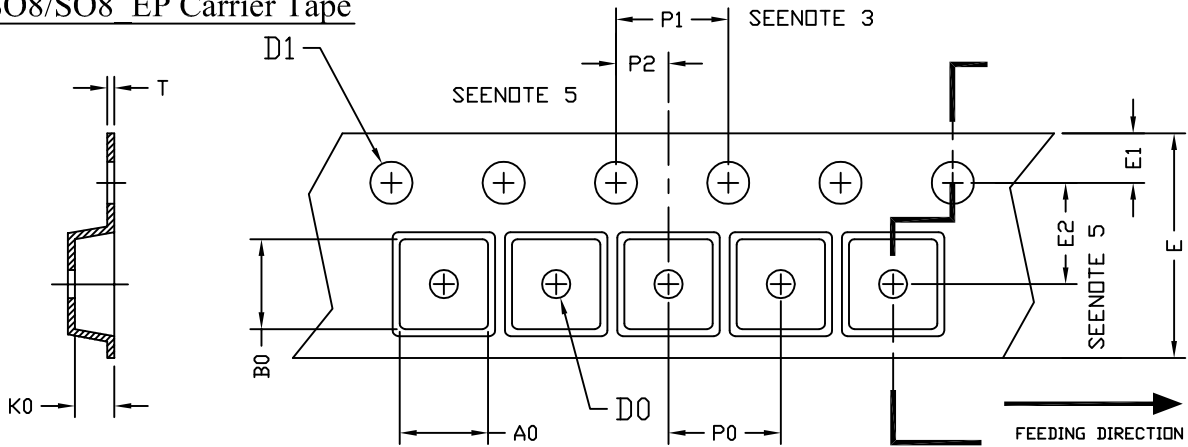
UNIT: mm

**NOTE**

1. ALL DIMENSIONS ARE IN MILLIMETERS.
2. DIMENSIONS ARE INCLUSIVE OF PLATING.
3. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.  
MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH.
4. DIMENSION L IS MEASURED IN GAUGE PLANE.
5. CONTROLLING DIMENSION IS MILLIMETER.
6. CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.



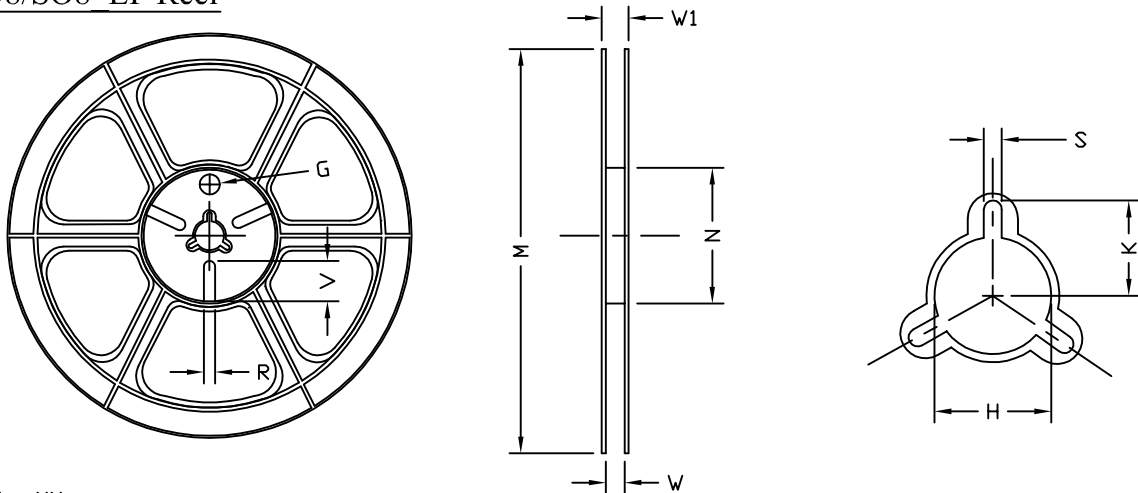
## SO7/SO8/SO8 EP Carrier Tape



UNIT: MM

PACKAGE	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	T
SO7/SO-8 (12 mm)	6.40 $\pm 0.10$	5.20 $\pm 0.10$	2.10 $\pm 0.10$	1.60 $\pm 0.10$	1.50 $+0.10$	12.00 $\pm 0.30$	1.75 $\pm 0.10$	5.50 $\pm 0.05$	8.00 $\pm 0.10$	4.00 $\pm 0.10$	2.00 $\pm 0.05$	0.25 $\pm 0.05$

## SO7/SO8/SO8 EP Reel



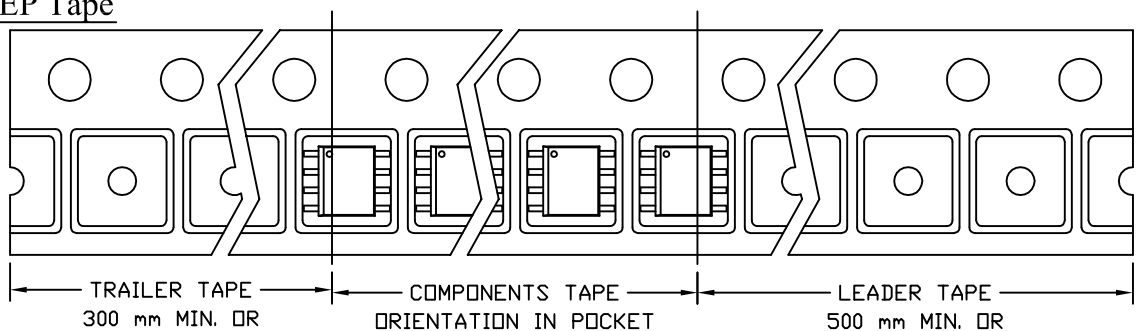
UNIT: MM

TAPE SIZE	REEL SIZE	M	N	W	W1	H	K	S	G	R	V
12 mm	$\phi 330$	$\phi 330.00$ $\pm 0.50$	$\phi 97.00$ $\pm 0.10$	13.00 $\pm 0.30$	17.40 $\pm 1.00$	$\phi 13.00$ $+0.50$ $-0.20$	10.60	2.00 $\pm 0.50$	---	---	---

## SO7/SO8/SO8 EP Tape

Leader / Trailer  
& Orientation

Unit Per Reel:  
3000pcs





# ***AOS Semiconductor Product Reliability Report***

**AO4807/AO4807L, rev A**

**Plastic Encapsulated Device**

**ALPHA & OMEGA Semiconductor, Inc**

**495 Mercury Drive  
Sunnyvale, CA 94085  
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**May 15, 2006**

This AOS product reliability report summarizes the qualification result for AO4807. Accelerated environmental tests are performed on a specific sample size, and then followed by electrical test at end point. Review of final electrical test result confirms that AO4807 passes AOS quality and reliability requirements. The released product will be categorized by the process family and be monitored on a quarterly basis for continuously improving the product quality.

## Table of Contents:

- I. Product Description
- II. Package and Die information
- III. Environmental Stress Test Summary and Result
- IV. Reliability Evaluation
- V. Quality Assurance Information

### I. Product Description:

The AO4807 uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , and ultra-low low gate charge. This device is suitable for use as a load switch or in PWM applications. Standard Product AO4807 is Pb-free (meets ROHS & Sony 259 specifications). AO4807L is a Green Product ordering option. AO4807 and AO4807L are electrically identical.

Absolute Maximum Ratings $T_A=25^\circ\text{C}$ unless otherwise noted			
Parameter	Symbol	Maximum	Units
Drain-Source Voltage	$V_{DS}$	-30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$T_A=25^\circ\text{C}$	-6	A
	$T_A=70^\circ\text{C}$	-5	
Pulsed Drain Current	$I_{DM}$	-30	
Power Dissipation	$T_A=25^\circ\text{C}$	2	W
	$T_A=70^\circ\text{C}$	1.44	
Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	$^\circ\text{C}$

Thermal Characteristics					
Parameter		Symbol	Typ	Max	Units
Maximum Junction-to-Ambient	$T \leq 10\text{s}$	$R_{\theta JA}$	48	62.5	$^\circ\text{C/W}$
Maximum Junction-to-Ambient	Steady-State		74	110	$^\circ\text{C/W}$
Maximum Junction-to-Lead	Steady-State	$R_{\theta JL}$	35	40	$^\circ\text{C/W}$

## II. Die / Package Information:

	<b>AO4807</b>	<b>AO4807L (Green Compound)</b>
<b>Process</b>	Standard sub-micron low voltage P channel process	Standard sub-micron low voltage P channel process
<b>Package Type</b>	8 leads SOIC	8 leads SOIC
<b>Lead Frame</b>	Copper with Solder Plate	Copper with Solder Plate
<b>Die Attach</b>	Ag epoxy	Ag epoxy
<b>Bond wire</b>	Au 2mils	Au 2 mils
<b>Mold Material</b>	Epoxy resin with silica filler	Epoxy resin with silica filler
<b>Filler % (Spherical/Flake)</b>	90/10	100/0
<b>Flammability Rating</b>	UL-94 V-0	UL-94 V-0
<b>Backside Metallization</b>	Ti / Ni / Ag	Ti / Ni / Ag
<b>Moisture Level</b>	Up to Level 1 *	Up to Level 1 *

**Note \*** based on info provided by assembler and mold compound supplier

## III. Result of Reliability Stress for AO4807 (Standard) & AO4807L (Green)

Test Item	Test Condition	Time Point	Lot Attribution	Total Sample size	Number of Failures
<b>Solder Reflow Precondition</b>	Standard: 1hr PCT+3 cycle reflow@260°C Green: 168hr 85°C /85%RH +3 cycle reflow@260°C	0hr	Standard: 83 lots Green: 29 lots	17380 pcs	0
<b>HTGB</b>	Temp = 150°C , Vgs=100% of Vgsmax	168 / 500 hrs  1000 hrs	12 lots  (Note A*)	984 pcs  77+5 pcs / lot	0
<b>HTRB</b>	Temp = 150°C , Vds=80% of Vdsmax	168 / 500 hrs  1000 hrs	12 lots  (Note A*)	984 pcs  77+5 pcs / lot	0
<b>HAST</b>	130 +/- 2°C , 85%RH, 33.3 psi, Vgs = 80% of Vgs max	100 hrs	Standard: 81 lots Green: 16 lots  (Note B**)	5335 pcs  50+5 pcs / lot	0
<b>Pressure Pot</b>	121°C , 15+/-1 PSIG, RH=100%	96 hrs	Standard: 83 lots Green: 20 lots  (Note B**)	5665 pcs  50+5 pcs / lot	0
<b>Temperature Cycle</b>	-65°C to 150°C , air to air	250 / 500 cycles	Standard: 87 lots Green: 29 lots  (Note B**)	6380 pcs  50+5 pcs / lot	0

### III. Result of Reliability Stress for AO4807 (Standard) & AO4807L (Green) Continues

<b>DPA</b>	Internal Vision Cross-section X-ray	NA	5 5 5	5 5 5	0
<b>CSAM</b>		NA	5	5	0
<b>Bond Integrity</b>	Room Temp 150°C bake 150°C bake	0hr 250hr 500hr	40 40 40	40 wires 40 wires 40 wires	0
<b>Solderability</b>	230°C	5 sec	15	15 leads	0
<b>Die shear</b>	150°C	0hr	10	10	0

**Note A:** The HTGB and HTRB reliability data presents total of available AO4807 and AO4807L burn-in data up to the published date.

**Note B:** The pressure pot, temperature cycle and HAST reliability data for AO4807 and AO4807L comes from the AOS generic package qualification data.

### IV. Reliability Evaluation

**FIT rate (per billion):4.3**

**MTTF = 26547years**

In general, 500 hrs of HTGB, 150 deg C accelerated stress testing is equivalent to 15 years of lifetime at 55 deg C operating conditions (by applying the Arrhenius equation with an activation energy of 0.7eV and 60% of upper confidence level on the failure rate calculation). AOS reliability group also routinely monitors the product reliability up to 1000 hr at and performs the necessary failure analysis on the units failed for reliability test(s).

The presentation of FIT rate for the individual product reliability is restricted by the actual burn-in sample size of the selected product (AO4807). Failure Rate Determination is based on JEDEC Standard JESD 85. FIT means one failure per billion hours.

$$\text{Failure Rate} = \text{Chi}^2 \times 10^9 / [2 (N) (H) (Af)]$$

$$= 1.83 \times 10^9 / [2 (6 \times 164) (168) (258) + 2 (4 \times 164) (500) (258) + 2 (2 \times 164) (1000) (258)] = 4.3$$

$$\text{MTTF} = 10^9 / \text{FIT} = 2.32 \times 10^8 \text{hrs} = 26547 \text{years}$$

**Chi<sup>2</sup>** = Chi Squared Distribution, determined by the number of failures and confidence interval

**N** = Total Number of units from HTRB and HTGB tests

**H** = Duration of HTRB/HTGB testing

**Af** = Acceleration Factor from Test to Use Conditions (Ea = 0.7eV and Tuse = 55°C)

$$\text{Acceleration Factor [Af]} = \text{Exp} [Ea / k (1/Tj u - 1/Tj s)]$$

**Acceleration Factor ratio list:**

	55 deg C	70 deg C	85 deg C	100 deg C	115 deg C	130 deg C	150 deg C
<b>Af</b>	<b>258</b>	<b>87</b>	<b>32</b>	<b>13</b>	<b>5.64</b>	<b>2.59</b>	<b>1</b>

**Tj s** = Stressed junction temperature in degree (Kelvin), K = C+273.16

**Tj u** = The use junction temperature in degree (Kelvin), K = C+273.16

**k** = Boltzmann's constant, 8.617164 X 10<sup>-5</sup>eV / K



## V. Quality Assurance Information

Acceptable Quality Level for outgoing inspection: **0.1%** for electrical and visual.

Guaranteed Outgoing Defect Rate: **< 25 ppm**

Quality Sample Plan: conform to **Mil-Std-105D**