



#### P-CHANNEL ENHANCEMENT MODE MOSFET

### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(on) max</sub>	I <sub>D</sub> T <sub>A</sub> = 25°C
-20V	1.0Ω @ V <sub>GS</sub> = -4.5V	-700mA
	1.5Ω @ V <sub>GS</sub> = -2.5V	-600mA
	2.0Ω @ V <sub>GS</sub> = -1.8V	-500mA
	3.0Ω @ V <sub>GS</sub> = -1.5V	-380mA

### **Description and Applications**

This new generation MOSFET has been designed to minimize the onstate resistance ( $R_{DS(on)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- DC-DC Converters
- Power management functions

#### **Features and Benefits**

- Low On-Resistance
- Very Low Gate Threshold Voltage V<sub>GS(TH)</sub>, 1.0V max
- Low Input Capacitance
- Fast Switching Speed
- Ultra-Small Surfaced Mount Package
- Ultra-low package profile, 0.4mm maximum package height
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

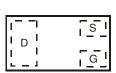
- Case: X2-DFN1006-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish NiPdAu over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Weight: 0.001 grams (approximate)



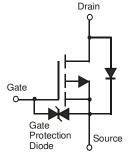




**Bottom View** 



Top View Internal Schematic



**Equivalent Circuit** 

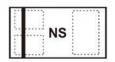
#### **Ordering Information** (Note 4)

Part Number	Case	Packaging
DMP21D5UFB4-7B	X2-DFN1006-3	10,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com.

# **Marking Information**



Top View Bar Denotes Gate and Source Side

NS = Product Type Marking Code



# Maximum Ratings @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	$V_{\mathrm{DSS}}$	-20	V		
Gate-Source Voltage	$V_{GSS}$	±8	V		
Continuous Drain Courset (Note C) V	Steady State	T <sub>A</sub> = 25°C T <sub>A</sub> = 70°C	I <sub>D</sub>	-700 -600	mA
Continuous Drain Current (Note 6) V <sub>GS</sub> = -4.5V	t<10s	$T_A = 25$ °C $T_A = 70$ °C	I <sub>D</sub>	-850 -670	mA
Steady $T_A = 25^{\circ}C$ State $T_A = 70^{\circ}C$			I <sub>D</sub>	-500 -400	mA
Continuous Drain Current (Note 6) V <sub>GS</sub> = -1.8V	I <sub>D</sub>	-600 -550	mA		
Pulsed Drain Current (10µs pulse, duty cycle = 1%)	I <sub>DM</sub>	-2	Α		
Maximum Body Diode continuous Current	I <sub>S</sub>	-800	mA		

# Thermal Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)	$P_{D}$	0.46	W	
Thermal Peciatones, Junction to Ambient (Note 5)	Steady state		279	°C/W
Thermal Resistance, Junction to Ambient (Note 5) t<10s		$R_{ hetaJA}$	210	°C/W
Total Power Dissipation (Note 6)		$P_{D}$	0.95	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	Б	134	°C/W
t<10s		$R_{ hetaJA}$	100	°C/W
Operating and Storage Temperature Range		$T_{J_1}T_{STG}$	-55 to +150	°C

# Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20	-	-	V	$V_{GS} = 0V$ , $I_D = -1mA$	
Zero Gate Voltage Drain Current T <sub>J</sub> = 25°C	I <sub>DSS</sub>	1	1	-100	nA	$V_{DS} = -20V, V_{GS} = 0V$	
Gate-Source Leakage	1	ı	1	±1.0	μА	$V_{GS} = \pm 5V$ , $V_{DS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	-	-	±5.0		$V_{GS} = \pm 8V$ , $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	$V_{GS(th)}$	-0.5	-	-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
		-	0.67	0.97		$V_{GS} = -5V, I_D = -100mA$	
			0.7	1.0		$V_{GS} = -4.5V, I_D = -100mA$	
Static Drain-Source On-Resistance	D	ı	0.9	1.5	Ω	$V_{GS} = -2.5V, I_D = -80mA$	
Static Drain-Source On-nesistance	R <sub>DS</sub> (ON)	ı	1.2	2.0	2.2	$V_{GS} = -1.8V, I_D = -40mA$	
		-	1.5	3.0		$V_{GS} = -1.5V, I_D = -30mA$	
		-	5	-		$V_{GS} = -1.2V, I_D = -1mA$	
Forward Transfer Admittance	Y <sub>fs</sub>	-	0.7	-	S	$V_{DS} = -3V, I_{D} = -100mA$	
Diode Forward Voltage	$V_{SD}$	-	-0.75	-1.2	V	$V_{GS} = 0V$ , $I_{S} = -330mA$ ,	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C <sub>iss</sub>	-	46.1	-		V 10V V 0V	
Output Capacitance	Coss	1	7.2	-	pF	$V_{DS} = 10V, V_{GS} = 0V,$ f = 1.0MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	ı	4.9	-		1 – 1.01011 12	
Gate Resistance	$R_g$	1	14.3	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge V <sub>GS</sub> = -4.5V	$Q_g$	1	0.5	-			
Gate-Source Charge	$Q_{gs}$	ı	0.09	-	nC	$V_{DS} = -10V, I_D = -250mA$	
Gate-Drain Charge	$Q_{gd}$	1	0.09	-			
Turn-On Delay Time	t <sub>D(on)</sub>	-	8.5	-		V 0V V 0.5V	
Turn-On Rise Time	t <sub>r</sub>	-	4.3	-	no	$V_{DD} = -3V, V_{GS} = -2.5V,$	
Turn-Off Delay Time	t <sub>D(off)</sub>	-	20.2	-	ns	$R_L = 300\Omega$ , $R_G = 25\Omega$ , $I_D = -100$ mA	
Turn-Off Fall Time	t <sub>f</sub>	-	19.2	-		D = 100HIA	

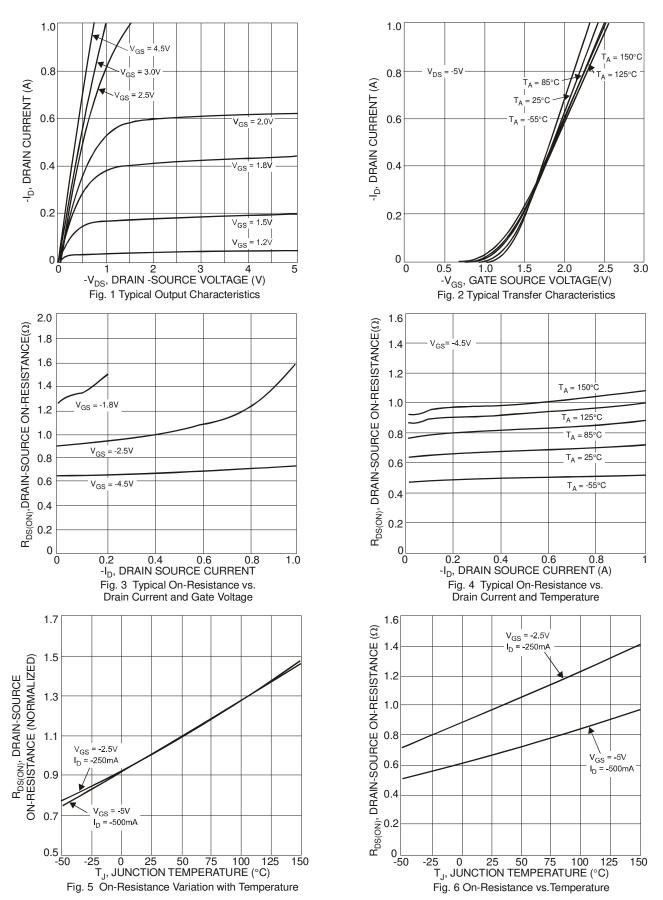
Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

<sup>6.</sup> Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

<sup>7.</sup> Short duration pulse test used to minimize self-heating effect.

<sup>8.</sup> Guaranteed by design. Not subject to product testing.







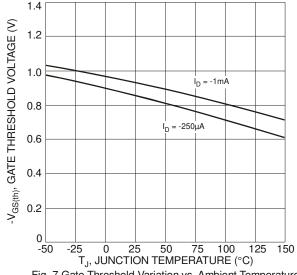
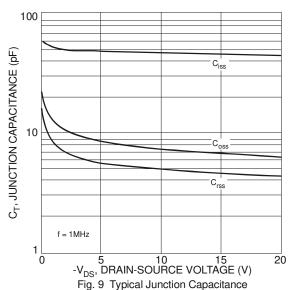
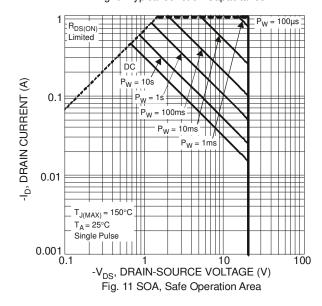
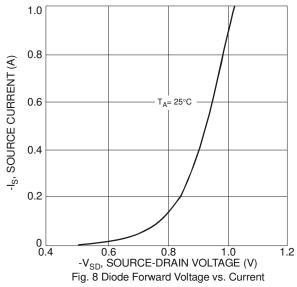


Fig. 7 Gate Threshold Variation vs. Ambient Temperature







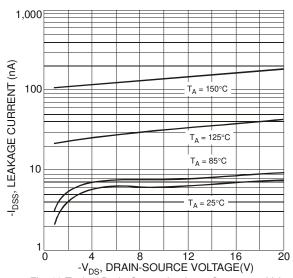
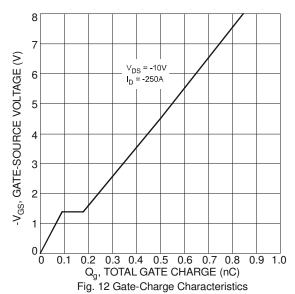
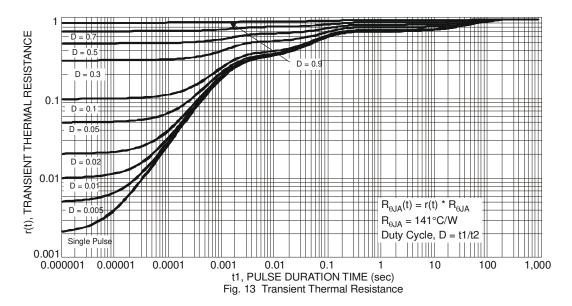


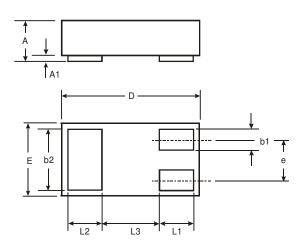
Fig. 10 Typical Drain-Source Leakage Current vs. Voltage





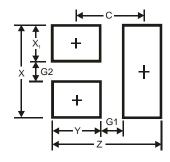


# **Package Outline Dimensions**



X2-DFN1006-3					
Dim	Min	Max	Тур		
Α	_	0.40			
A1	0	0.05	0.02		
b1	0.10	0.20	0.15		
b2	0.45	0.55	0.50		
D	0.95	1.05	1.00		
Е	0.55	0.65	0.60		
е	_	—	0.35		
L1	0.20	0.30	0.25		
L2	0.20	0.30	0.25		
L3	_	_	0.40		
All Dimensions in mm					

# **Suggested Pad Layout**



Dimensions	Value (in mm)		
Z	1.1		
G1	0.3		
G2	0.2		
Х	0.7		
X1	0.25		
Υ	0.4		
С	0.7		



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