Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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2SK1620(L), 2SK1620(S)

Silicon N Channel MOS FET

REJ03G0957-0200

(Previous: ADE-208-1298)

Rev.2.00 Sep 07, 2005

Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator, DC-DC converter and motor driver

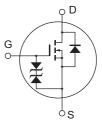
EOL anno

Outline

RENESAS Package code: PRSS0004AE-A (Package name: LDPAK(L))

RENESAS Package code: PRSS0004AE-B (Package name: LDPAK(S)-(1))





- 1. Gate
- Drain
 Source
- 4. Drain

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	150	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I _D	10	Α
Drain peak current	I _{D(pulse)} *1	40	Α
Body to drain diode reverse drain current	I _{DR}	10	Α
Channel dissipation	Pch ^{*2}	50	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	−55 to +150	°C

Notes: 1. PW ≤ 10 ∞s, duty cycle ≤ 1%

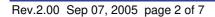
2. Value at $T_C = 25^{\circ}C$

Electrical Characteristics

 $(Ta = 25^{\circ}C)$

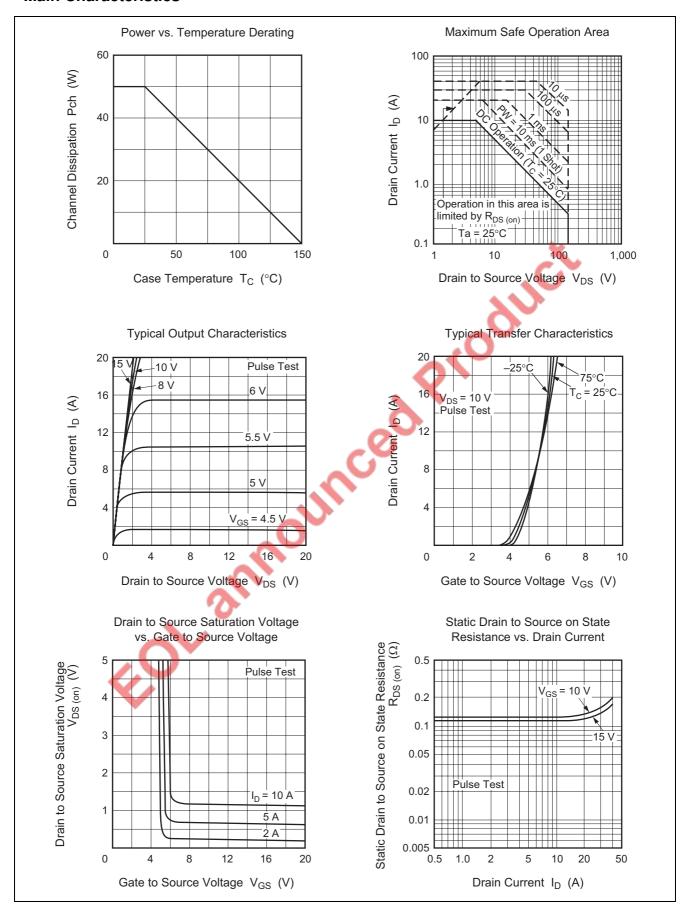
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	150			V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20			V	$I_G = \pm 100 \propto A, V_{DS} = 0$
Gate to source leak current	I _{GSS}	_		±10	∞A	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	1		250	∞A	$V_{DS} = 120 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	2.0		4.0	٧	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on state resistance	R _{DS(on)}	_	0.12	0.15	Ω	$I_D = 5 \text{ A}, V_{GS} = 10 \text{ V}^{*3}$
Forward transfer admittance	y _{fs}	4.0	7.0	<u> </u>	S	$I_D = 5 \text{ A}, V_{DS} = 10 \text{ V}^{*3}$
Input capacitance	Ciss	_	1200	_	рF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$
Output capacitance	Coss	— «	550	_	рF	f = 1 MHz
Reverse transfer capacitance	Crss		85	_	рF	
Turn-on delay time	t _{d(on)}	7	20	_	ns	$I_D = 5 A, V_{GS} = 10 V,$
Rise time	tr	O-	50	_	ns	$R_L = 6 \Omega$
Turn-off delay time	t _{d(off)}	_	70	_	ns	
Fall time	ti	_	40	_	ns	
Body to drain diode forward voltage	V_{DF}		1.2		V	$I_F = 10 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery	T _{rr}	_	220	_	ns	$I_F = 10 \text{ A}, V_{GS} = 0,$
time						$di_F/dt = 50 \text{ A/} \propto \text{s}$

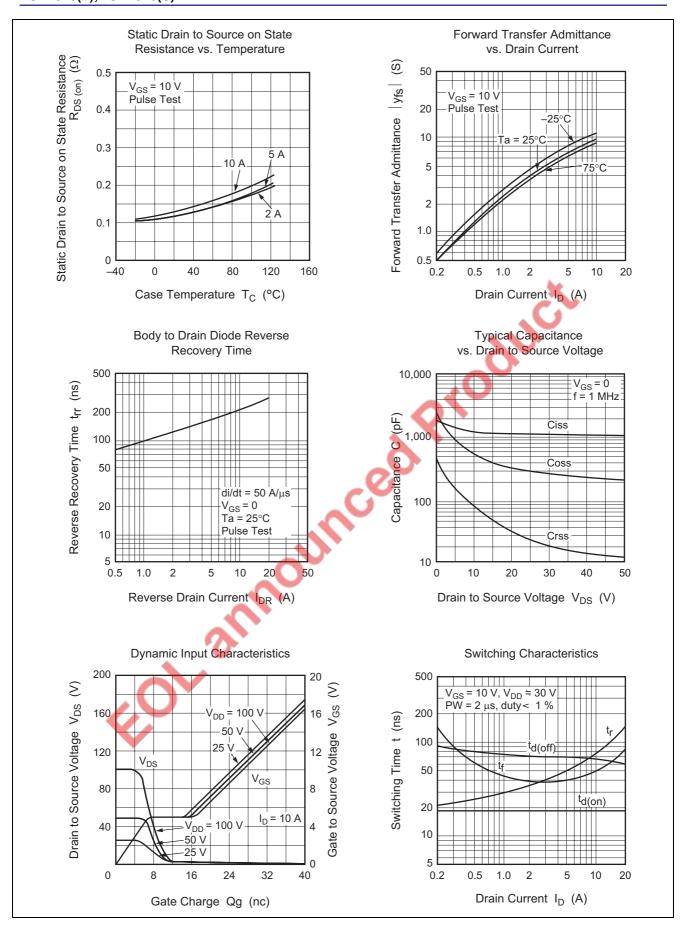
Note: 3. Pulse test

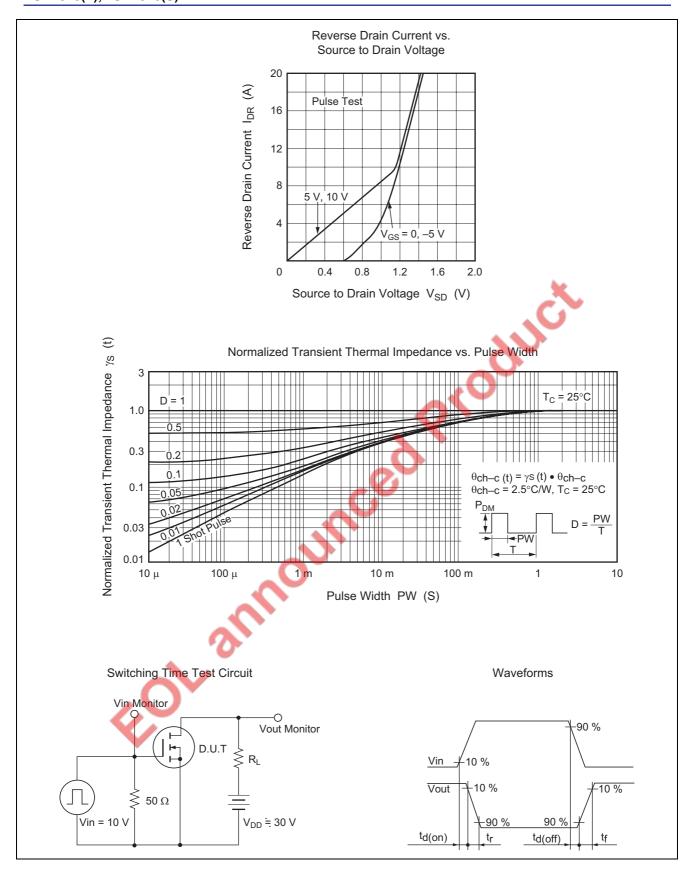




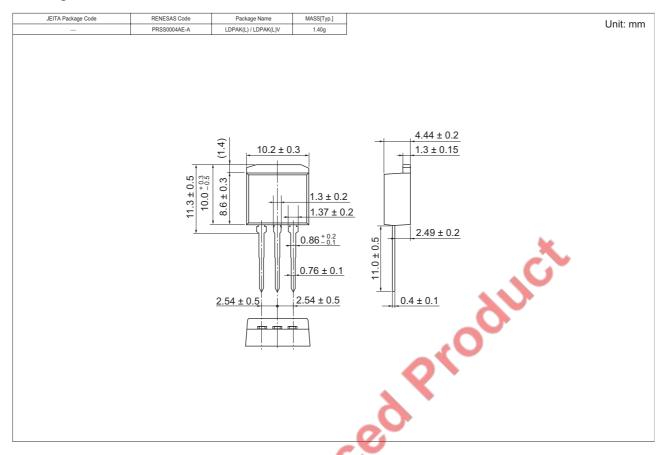
Main Characteristics

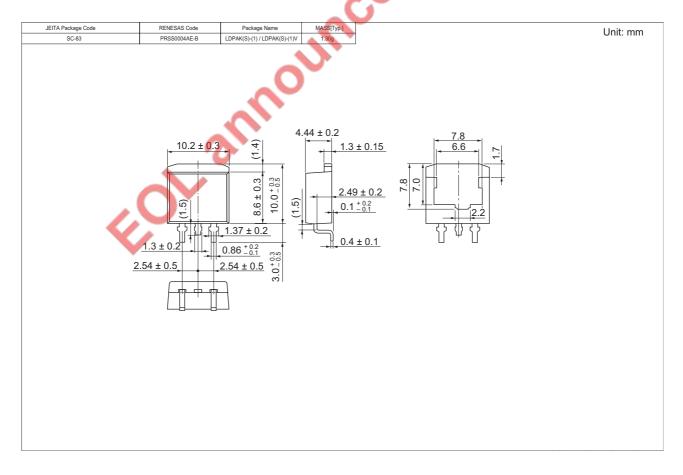






Package Dimensions





Ordering Information

Part Name	Quantity	Shipping Container
2SK1620L-E	500 pcs	Box (Sack)
2SK1620STL-E	1000 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.



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