

RQ7E110AJ

## Nch 30V 11A Middle Power MOSFET

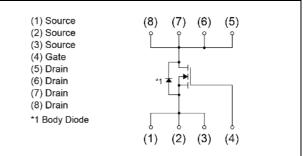
V <sub>DSS</sub>	30V
R <sub>DS(on)</sub> (Max.)	9.0mΩ
Ι <sub>D</sub>	±11A
P <sub>D</sub>	1.5W

2) Small Surface Mount Package (TSMT8).

3) Pb-free lead plating ; RoHS compliant

# • Outline TSMT8

## Inner circuit



## Packaging specifications

	Packing	Embossed Tape
	Reel size (mm)	180
Туре	Tape width (mm)	8
	Quantity (pcs)	3000
	Taping code	TCR
	Marking	HR

## • Δhsolute maximum ratings (T<sub>2</sub> = 25°C, unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V <sub>DSS</sub>	30	V
Continuous drain current	I <sub>D</sub>	±11	А
Pulsed drain current	I <sub>DP</sub> *1	±44	А
Gate - Source voltage	V <sub>GSS</sub>	±12	V
Avalanche current, single pulse	I <sub>AS</sub> *2	11	А
Avalanche energy, single pulse	E <sub>AS</sub> *2	18	mJ
	P <sub>D</sub> <sup>*3</sup>	1.5	W
Power dissipation	P <sub>D</sub> *4	1.1	W
Junction temperature	Tj	150	°C
Operating junction and storage temperature range	T <sub>stg</sub>	-55 to +150	C°

Application

Features

1) Low on - resistance.

Switching

## Thermal resistance

Deremeter	Currence of	Values			Linit
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal registeres junction embient	$R_{thJA}^{*3}$	-	-	83.3	°C/W
Thermal resistance, junction - ambient	$R_{thJA}^{*4}$	-	-	113	°C/W

## • Electrical characteristics (T<sub>a</sub> = 25°C)

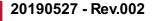
Deremeter	Sumbol	Conditions		Values		
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit
Drain - Source breakdown voltage	$V_{(BR)DSS}$ $V_{GS} = 0V, I_D = 1mA$		30	-	-	V
Breakdown voltage temperature coefficient	$\frac{\Delta V_{(BR)DSS}}{\Delta T_j}$	I <sub>D</sub> = 1mA referenced to 25°C	-	18	-	mV/°C
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V	-	-	1	μA
Gate - Source leakage current	I <sub>GSS</sub>	$V_{GS}$ = ±12V, $V_{DS}$ = 0V	-	-	±100	nA
Gate threshold voltage	$V_{GS(th)}$	$V_{DS}$ = $V_{GS}$ , $I_D$ = 10mA	0.5	-	1.5	V
Gate threshold voltage temperature coefficient	$\frac{\Delta V_{GS(th)}}{\Delta T_j}$	I <sub>D</sub> = 1mA referenced to 25°C	-	-1.8	-	mV/°C
Static drain - source	D *5	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 11A	-	6.8	9.0	m0
on - state resistance	${\sf R}_{\sf DS(on)}^{*5}$	V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 5.5A	-	9.1	12.4	mΩ
Gate resistance	R <sub>G</sub>	f = 1MHz, open drain	-	1.5	-	Ω
Forward Transfer Admittance	Y <sub>fs</sub>  * <sup>5</sup>	V <sub>DS</sub> = 5V, I <sub>D</sub> = 11A	13	-	-	S

\*1 Pw  $\leq$  10µs, Duty cycle  $\leq$  1%

\*2 L  $\simeq$  0.2mH, V \_DD = 15V, R \_G = 25 \Omega, STARTING T \_j = 25 ^C Fig.3-1,3-2

\*3 Mounted on a ceramic boad (30×30×0.8mm)

- \*4 Mounted on a FR4 (25×25×0.8mm)
- \*5 Pulsed



## • Electrical characteristics ( $T_a = 25^{\circ}C$ )

Deremeter	Sumphal	Conditions	Values			Unit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0V	-	2410	-		
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 15V	-	275	-	pF	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1MHz	-	185	-		
Turn - on delay time	t <sub>d(on)</sub> *5	$V_{DD} \simeq 15 V, V_{GS} = 4.5 V$	-	22	-		
Rise time	t <sub>r</sub> *5	I <sub>D</sub> = 5.5A	-	18	-	20	
Turn - off delay time	t <sub>d(off)</sub> *5	R <sub>L</sub> ≃ 2.72Ω	-	87	-	ns	
Fall time	t <sub>f</sub> *5	R <sub>G</sub> = 10Ω	-	79	-		

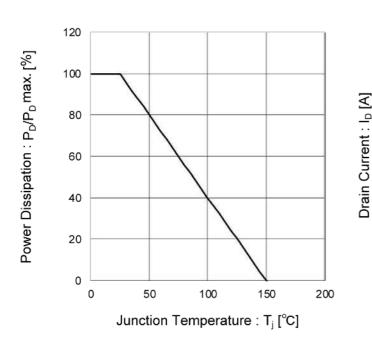
## • Gate charge characteristics ( $T_a = 25^{\circ}C$ )

Parameter	Symbol	Conditions	Values			Unit
	Symbol	Conditions	Min.	Тур.	Max.	Unit
Total gate charge	$Q_g^{*5}$	V <sub>DD</sub> ≃ 15V,	-	22.0	-	
Gate - Source charge	$Q_{gs}^{*5}$	I <sub>D</sub> = 11A,	-	5.6	-	nC
Gate - Drain charge	${\sf Q}_{\sf gd}{}^{*5}$	V <sub>GS</sub> = 4.5V	-	5.4	-	

## •Body diode electrical characteristics (Source-Drain) ( $T_a = 25^{\circ}C$ )

Parameter	Symbol	Conditions	Values			Unit	
	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Continuous forward current	۱ <sub>s</sub>	$T = 25^{\circ}$	-	-	1.25	А	
Pulse forward current	$I_{SP}^{*1}$	T <sub>a</sub> = 25°C	-	-	44	А	
Forward voltage	$V_{SD}^{*5}$	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1.25A	-	-	1.2	V	





## Fig.1 Power Dissipation Derating Curve

Fig.2 Maximum Safe Operating Area

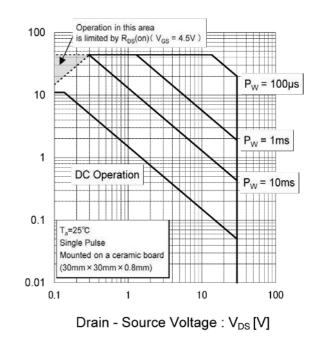
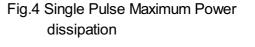
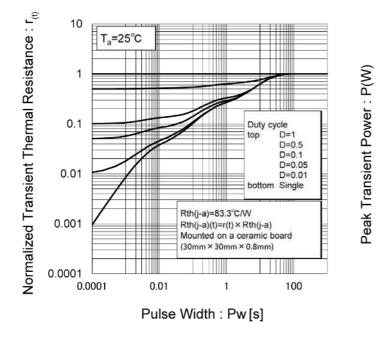
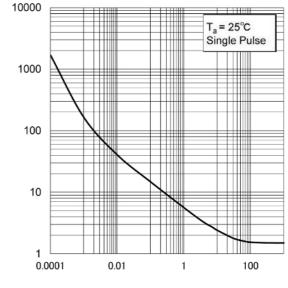


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width







Pulse Width : Pw [s]



11

10

9

8

7

6

5

4 3

2

1

0

0

## • Electrical characteristic curves



## Fig.5 Typical Output Characteristics(I)

V<sub>GS</sub>= 4.5V

V<sub>GS</sub>= 2.5V

V<sub>GS</sub>= 1.8V

V<sub>GS</sub>= 1.5V

0.1 0.2 0.3 0.4 0.5 0.6 0.7 0.8 0.9

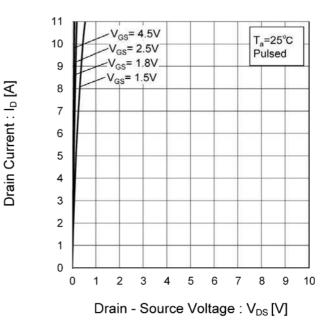
Drain - Source Voltage : V<sub>DS</sub> [V]

T<sub>a</sub>=25°C

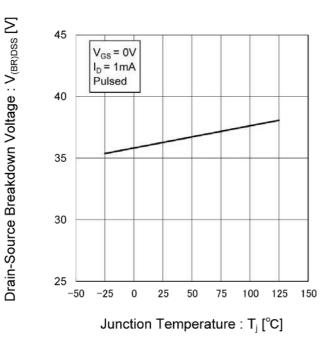
1

Pulsed

## Fig.6 Typical Output Characteristics(II)

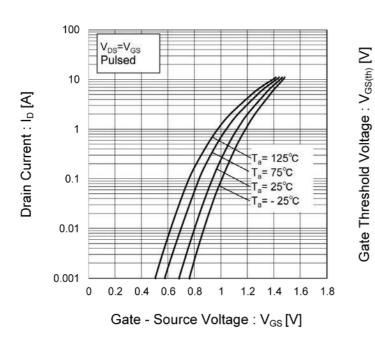


## Fig.7 Breakdown Voltage vs. Junction Temperature



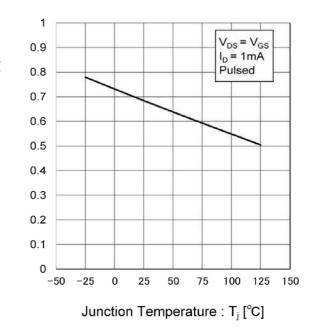
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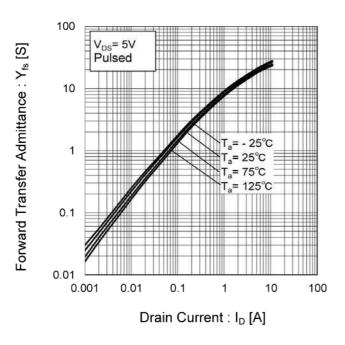


## Fig.8 Typical Transfer Characteristics

## Fig.9 Gate Threshold Voltage vs. Junction Temperature



# Fig.10 Forward Transfer Admittance vs. Drain Current





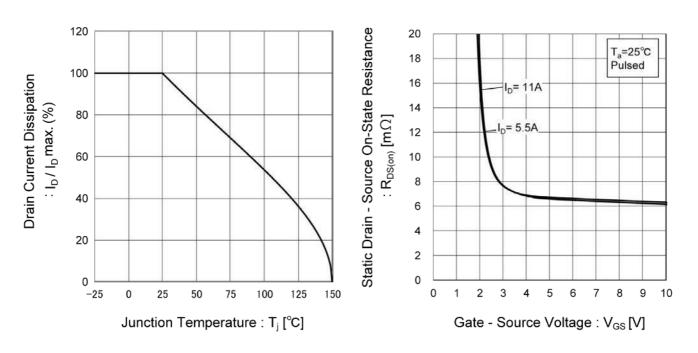
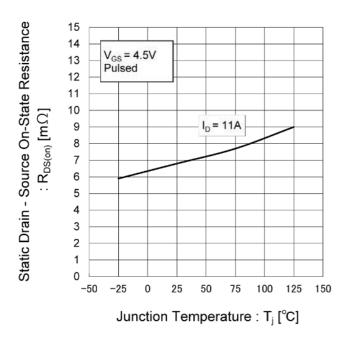


Fig.11 Drain Current Derating Curve

## Fig.12 Static Drain - Source On - State Resistance vs. Gate Source Voltage

Fig.13 Static Drain - Source On - State Resistance vs. Junction Temperature





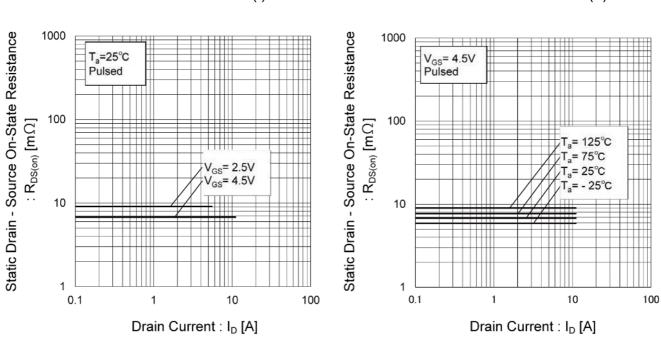
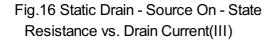
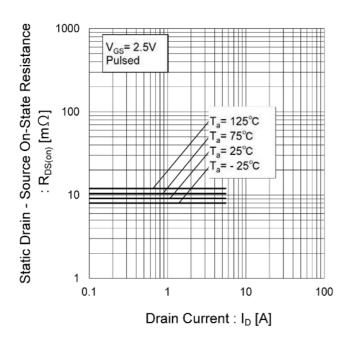
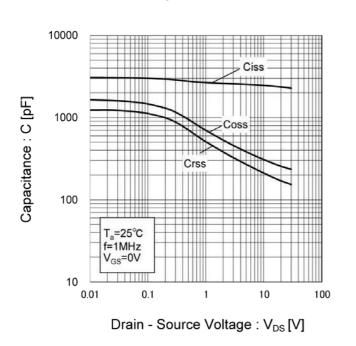


Fig.14 Static Drain - Source On - State Resistance vs. Drain Current(I) Fig.15 Static Drain - Source On - State Resistance vs. Drain Current(II)









## Fig.17 Typical Capacitance vs. Drain -Source Voltage

Fig.18 Switching Characteristics

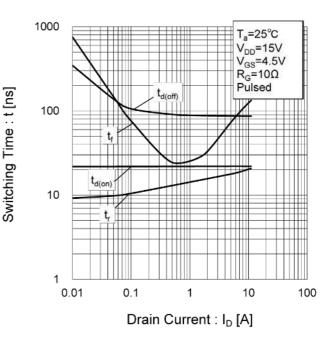


Fig.19 Dynamic Input Characteristics

Gate - Source Voltage : V<sub>GS</sub> [V]

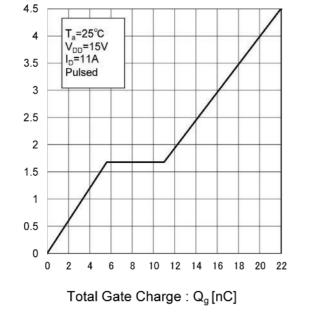
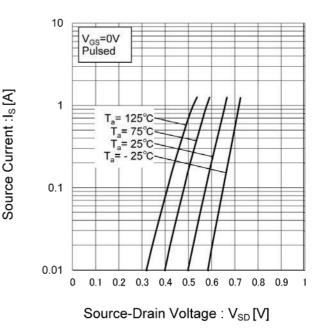


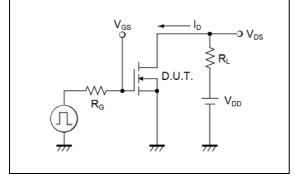
Fig.20 Source Current vs. Source Drain Voltage



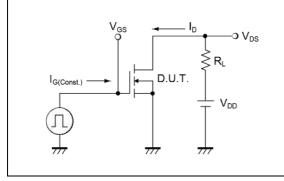


## Measurement circuits

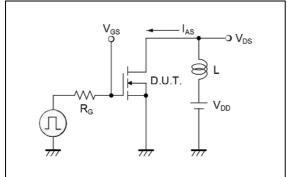
Fig.1-1 Switching Time Measurement Circuit



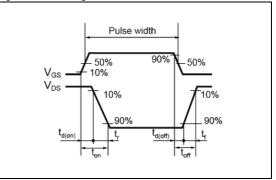
### Fig.2-1 Gate Charge Measurement Circuit



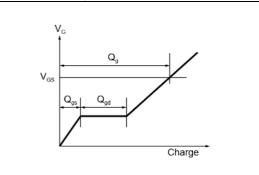
### Fig.3-1 Avalanche Measurement Circuit



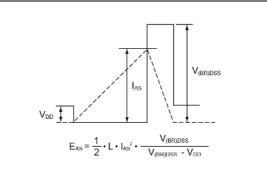
#### Fig.1-2 Switching Waveforms



### Fig.2-2 Gate Charge Waveform



### Fig.3-2 Avalanche Waveform

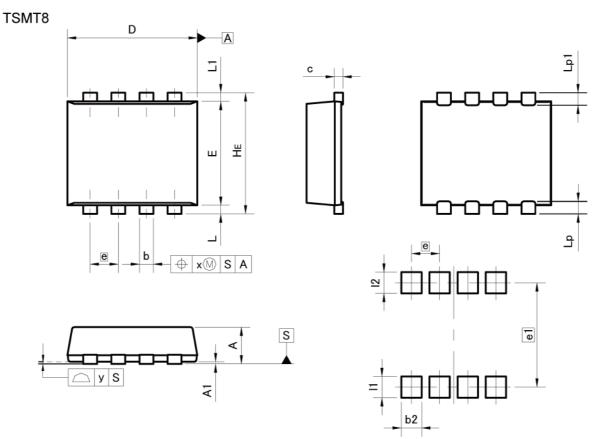


## Notice

This product might cause chip aging and breakdown under the large electrified environment. Please consider to design ESD protection circuit.



## Dimensions



Pattern of terminal position areas [Not a pattern of soldering pads]

	MILIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.75	0.85	0.030	0.033
A1	0.00	0.05	0.000	0.002
b	0.27	0.37	0.011	0.015
с	0.12	0.22	0.005	0.009
D	2.90	3.10	0.114	0.122
E	2.30	2.50	0.091	0.098
е	0.65		0.026	
HE	2.70	2.90	0.106	0.114
L	0.10	0.30	0.004	0.012
L1	0.10	0.30	0.004	0.012
Lp	0.19	0.39	0.007	0.015
Lp1	0.19	0.39	0.007	0.015
x	275	0.10	8 <del>77</del> 3	0.004
У	1977	0.10	-	0.004

DIM	MILIMETERS		INC	HES
	MIN	MAX	MIN	MAX
b2	20 <del>14</del>	0.47	(H)	0.019
e1	2.	2.41		095
11	3 <del>71</del>	0.49	3 <del></del> )	0.019
12	8000	0.49		0.019

Dimension in mm/inches



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JAPAN	USA	EU	CHINA
CLASSⅢ	CLASSⅢ	CLASS II b	CLASSII
CLASSⅣ	CLASSIII	CLASSⅢ	CLASSI

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  - [f] Sealing or coating our Products with resin or other coating materials
  - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
  - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

#### Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

#### Precautions Regarding Application Examples and External Circuits

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

#### Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
  - [a] the Products are exposed to sea winds or corrosive gases, including Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>2</sub>, and NO<sub>2</sub>
  - [b] the temperature or humidity exceeds those recommended by ROHM
  - [c] the Products are exposed to direct sunshine or condensation
  - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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