**MICROCHIP**

# Digital Power
# Development Board
# User's Guide

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# DIGITAL POWER DEVELOPMENT BOARD USER'S GUIDE

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# Preface

## NOTICE TO CUSTOMERS

**All documentation becomes dated, and this manual is no exception. Microchip tools and documentation are constantly evolving to meet customer needs, so some actual dialogs and/or tool descriptions may differ from those in this document. Please refer to our website (www.microchip.com) to obtain the latest documentation available.**

**Documents are identified with a "DS" number. This number is located on the bottom of each page, in front of the page number. The numbering convention for the DS number is "DSXXXXXXXXA", where "XXXXXXXX" is the document number and "A" is the revision level of the document.**

**For the most up-to-date information on development tools, see the MPLAB® IDE online help. Select the Help menu, and then Topics to open a list of available online help files.**

## INTRODUCTION

This chapter contains general information that will be useful to know before using the Digital Power Development Board. Items discussed in this chapter include:

- Document Layout
- Conventions Used in this Guide
- Recommended Reading
- The Microchip Website
- Product Change Notification Service
- Customer Support
- Document Revision History

## DOCUMENT LAYOUT

This document describes how to use the Digital Power Development Board User's Guide as a demonstration tool to provide a measurement platform for the Microchip dsPIC33 devices' Digital Power Plug-In Modules. The document is organized as follows:

- Chapter 1. "Overview" — This chapter introduces the Digital Power Development Board and provides a brief overview of its various features.
- Appendix A. "Board Layout and Schematics" — This appendix presents the schematics and the board layouts for the Digital Power Development Board.
- Appendix B. "Bill of Materials (BOM)" — This appendix presents the Bill of Materials for the Digital Power Development Board.

# Digital Power Development Board User's Guide

## CONVENTIONS USED IN THIS GUIDE

This manual uses the following documentation conventions:

### DOCUMENTATION CONVENTIONS

| Description | Represents | Examples |
|---|---|---|
| **Arial font:** | | |
| Italic characters | Referenced books | *MPLAB® IDE User's Guide* |
| | Emphasized text | ...is the *only* compiler... |
| Initial caps | A window | the Output window |
| | A dialog | the Settings dialog |
| | A menu selection | select Enable Programmer |
| Quotes | A field name in a window or dialog | "Save project before build" |
| Underlined, italic text with right angle bracket | A menu path | *File>Save* |
| Bold characters | A dialog button | Click **OK** |
| | A tab | Click the **Power** tab |
| N'Rnnnn | A number in verilog format, where N is the total number of digits, R is the radix and n is a digit. | 4'b0010, 2'hF1 |
| Text in angle brackets < > | A key on the keyboard | Press <Enter>, <F1> |
| **Courier New font:** | | |
| Plain Courier New | Sample source code | `#define START` |
| | Filenames | `autoexec.bat` |
| | File paths | `c:\mcc18\h` |
| | Keywords | `_asm, _endasm, static` |
| | Command-line options | `-Opa+, -Opa-` |
| | Bit values | `0, 1` |
| | Constants | `0xFF, 'A'` |
| Italic Courier New | A variable argument | `file`.o, where `file` can be any valid filename |
| Square brackets [ ] | Optional arguments | `mcc18 [options] file [options]` |
| Curly brackets and pipe character: { \| } | Choice of mutually exclusive arguments; an OR selection | `errorlevel {0\|1}` |
| Ellipses... | Replaces repeated text | `var_name [, var_name...]` |
| | Represents code supplied by user | `void main (void) { ... }` |

## RECOMMENDED READING

This user's guide describes how to use the Digital Power Development Board. Other useful document(s) are listed below. The following Microchip document(s) are recommended as supplemental reference resources.

- **dsPIC33 Family Digital Power PIM User's Guides** are available for download from the Microchip website (www.microchip.com)

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Microchip provides online support via our website at www.microchip.com. This website is used as a means to make files and information easily available to customers. Accessible by using your favorite Internet browser, the website contains the following information:

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Customers should contact their distributor, representative or Embedded Solutions Engineer (ESE) for support. Local sales offices are also available to help customers. A listing of sales offices and locations is included in the back of this document.

Technical support is available through the website at:

http://www.microchip.com/support.

## DOCUMENT REVISION HISTORY

**Revision A (October 2018)**

This is the initial version of this document.

# Digital Power Development Board User's Guide

**NOTES:**

# DIGITAL POWER DEVELOPMENT BOARD USER'S GUIDE

# Chapter 1.  Overview

## 1.1    INTRODUCTION

The Digital Power Development Board is a demonstration board that provides the user with a flexible measurement platform for all compatible Microchip dsPIC33 Digital Power Plug-In Modules (DP PIMs). DP PIMs can be inserted into the mating socket J1 in the middle of the Digital Power Development Board. All pins of the DP PIM are accessible via test loops or pin headers. The on-board micro USB connector provides a DC power input to all circuitry. In addition, a mikroBUS™ socket is provided to extend functionality.

This chapter provides an overview of the features of the Digital Power Development Board. The topics covered include:

- Features
- Electrical Characteristics

## 1.2    FEATURES

The Digital Power Development Board has the following features, as shown in Figure 1-1.

**FIGURE 1-1:       DIGITAL POWER DEVELOPMENT BOARD**
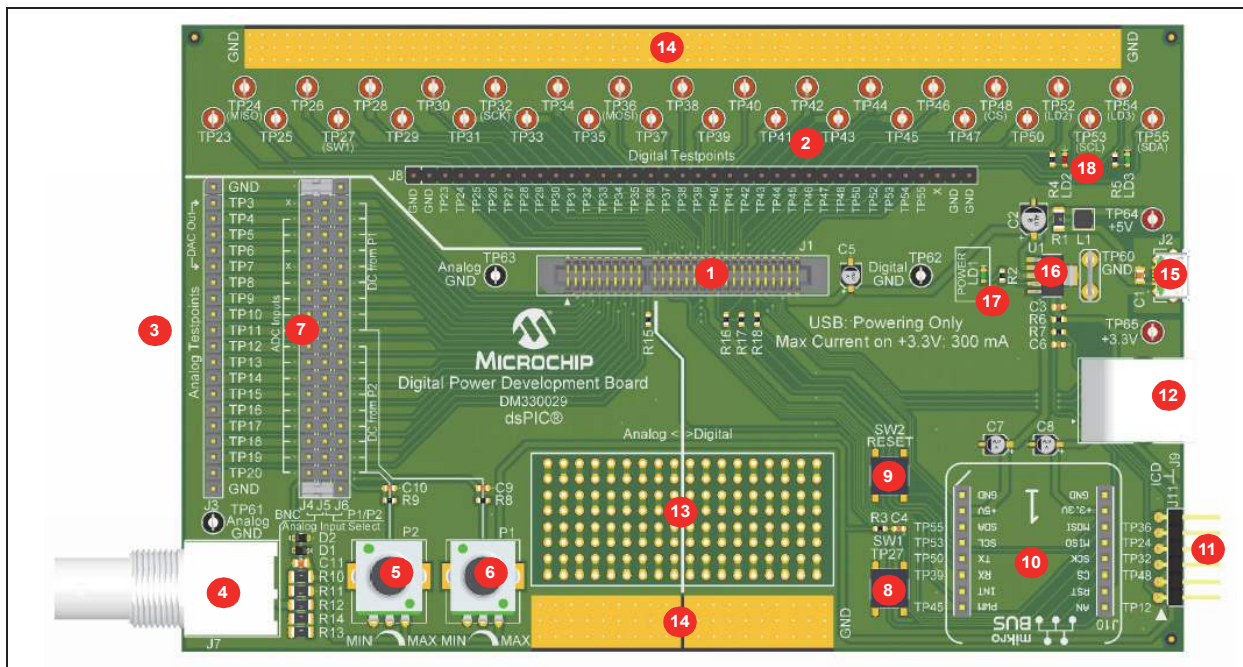


1.  Socket for DP PIM boards.
2.  Digital GPIO test points. Pin header with connection to the digital test points.
3.  ADC and DAC test points.
4.  External analog signal input.
5.  Potentiometer for DC level insertion from GND to +3.3V; it can be connected to the lower half of the ADC inputs.

6. Potentiometer for DC level insertion from GND to +3.3V; it can be connected to the upper half of the ADC inputs.
7. Analog input connection jumpers.
8. User push button.
9. Reset push button.
10. mikroBUS socket.
11. ICSP™ header to program the on-board MCU (6-pin, 2.54 mm header).
12. ICSP header to program the on-board MCU (RJ25 connector).
13. General purpose soldering pad area.
14. Solder pads for ground connection.
15. USB connector.
16. On-board LDO with Power Good (PG) function.
17. Power indicator LED (Green).
18. User LEDs (Red and Green).

Board dimensions are: 160 mm (length) x 100 mm (height).

### 1.2.1 Socket for DP PIM Boards

Insert the DP PIM board under test into the socket located in the middle of the Digital Power Development Board. Socket J1 has a slot that defines the DP PIM board direction. Be careful not to break the slot when inserting the DP PIM board into the socket.

### 1.2.2 Test Points

The Digital Power Development Board ensures good signal integrity and provides access to all pins of a DP PIM board. Each signal line is named after the DP PIM edge connector pin number, with a "TP" (Test Point) prefix on the schematic diagram, which is similarly marked on the silkscreen. These signals are divided into two main sections: Analog and Digital (see Figure 1-1). For a detailed pinout, refer to the User's Guide of the DP PIM under test.

1. Analog Section
   The analog section is located at the left connector side (smaller sector of the socket). It consists of 18 signal lines, all referenced to analog ground. These lines are split into two subsections:
   • ADC inputs
   • DAC output
2. Digital Section
   The digital section is located at the right connector side (larger sector of the socket). It consists of 31 lines, all referenced to digital ground. These lines support all the digital lines of the DP PIMs. Some of them have dedicated functions, such as:
   • High-speed PWM outputs
   • Medium speed GPIO
   • Communication lines (SPI, I$^2$C, UART)
   • User push button and LEDs

### 1.2.3    Grounding System

The entire board ground potential is connected together and forms one galvanically common domain. The labels, "Analog" and "Digital", indicate the geometrical domain. The copper pour connected to the ground potential is split into two geometrical domains, but the two geometrical domains are jointed together under the PIM socket, thus improving the signal integrity and keeping noise confined to near their sources.

### 1.2.4    External Analog Signal Input

It is possible to insert an analog signal to any ADC input from a signal generator via a standard BNC connector. The input impedance is 50Ω. Input signals between the ground and +3.3V are accepted. An overvoltage protection is implemented on this input with a series resistor, and a pair of parallel diodes between the ground and the positive supply rail.

### 1.2.5    DC Level Insertion

Two on-board potentiometers are connected between the ground and the +3.3V supply rail to provide two stable DC levels. The two DC levels can be set independently with potentiometers: P1 and P2. These DC signals can be used as an input signal to any ADC input. The DC level set by P1 can be connected to the upper half, while P2 can be connected to the lower half of the ADC inputs, at the analog input connection jumpers.

### 1.2.6    Analog Input Connection Jumpers

The ADC inputs of the DP PIM board can be accessed directly from the pin header (J3) located at the left side of the board. These inputs can also be connected to either the external analog signal input (J7) or to the DC level insertion, respectively. The three-row pin header matrix, J4, J5, J6 (No. 7 in Figure 1-1), forms a three-pin selection jumper for each ADC input. When placing a jumper over the left, or over the right and the middle pins, the corresponding ADC input can be connected to the extra input as indicated on the silkscreen. The left side pins connect the external analog signal input, while the right side pins connect the DC level insertion to the given ADC input. Two positions on the DAC outputs are not connected. The pins on those positions are just placeholders to keep the pin order. Three pins are connected to GND at both ends of the jumper row. Jumpers can be stored on those pins if not needed. Two jumpers are provided in the kit.

### 1.2.7    Push Buttons

There are two push buttons on the board: SW1 and SW2. Push button SW2, which is directly connected to the RESET pin of the DP PIM board, is for the system Reset. For dual core devices, this RESET is connected to the Master core Reset line. Push button SW1 is a general purpose user interface connected to the TP27 test point and the corresponding pin on the DP PIM.

### 1.2.8    MikroBUS™ Socket

A standard extension socket (J10) is provided on the board for mikroBUS click boards™. The functionality of the board can be easily extended by using this feature. The DP PIM board communicates with the mikroBUS socket via dedicated SPI lines and the I$^2$C bus. Both +5V and +3.3V supply rails are connected to the corresponding pins of the mikroBUS socket. The Reset button is connected to the mikroBUS socket RESET pin.

### 1.2.9 ICSP™ Programming Port

Dedicated lines for programming the MCU device on the inserted DP PIM board are accessible via an RJ-25 modular connector or on a 6-pin header.

### 1.2.10 General Purpose Soldering Pad Area

The soldering point matrix on the bottom of the board with Plated-Through-Hole (PTH) pads can be used to assemble any small extension circuitry needed for testing or prototyping.

### 1.2.11 Solder Pad for Ground Connection

A bare gold-plated copper area is located on the upper and the bottom edges of the board. These areas can either be used for soldering a solid ground connection or for attaching alligator clips to connect instrument ground.

### 1.2.12 USB Connector

The Digital Power Development Board can be powered via the micro USB connector, J2, at the right side of the board. The DP PIM board supply input and the +5V power rail for the mikroBUS click board are directly connected to the positive supply line of the USB. This connector is only for powering. The input voltage must be in the range of 3.6V to 6.3V. The mikroBUS click board can tighten this constraint; please refer to the specifications before powering. Communication is not possible via this USB connector.

### 1.2.13 On-Board LDO

The +3.3V power rail for the mikroBUS click board is supplied by the on-board LDO (Microchip's MCP1755), connected to the +5V supply rail coming from the USB. The total load for the mikroBUS click board supply current and the additional load caused by its active GPIO lines should not exceed 300 mA. The DC level insertion potentiometer and the pull-up resistor of the user push button are also connected to this line. The Power Good output drives the Reset line of the board, ensuring stable performance.

### 1.2.14 Power Indicator LED

The green LED (LD1) is on when the supply voltage is applied on the +5V supply rail.

### 1.2.15 User LEDs

The Digital Power Development Board has two user-programmable on-board LEDs. The red LED is connected to TP52, while the green LED is connected to TP54. Logic level high drives the LEDs.

## 1.3 ELECTRICAL CHARACTERISTICS

Table 1-1 shows the electrical characteristics of the Digital Power Development Board.

**TABLE 1-1: ELECTRICAL CHARACTERISTICS**

| Parameter | Value |
|---|---|
| Input Voltage Range | 3.6 V$_{DC}$ to 6.3 V$_{DC}$ |
| Current Consumption | <10 mA |
| Maximal Load Current | 1A |
| Operating Temperature Range | 0°C to +65°C |

# Appendix A. Board Layout and Schematics

This appendix contains the pinout, schematics and board layouts for the Digital Power Development Board. The topics covered in this appendix include:

- Pinout
- Board Schematic
- PCB Layout

## A.1    PINOUT

Table A-1 shows the pinout of the Digital Power Development Board.

**TABLE A-1:    PINOUT**

| Name | Edge Connector Pin | Default Function/Description |
|---|---|---|
| GND | 1, 2 | Analog Ground |
| DAC OUT | 3, 7 | DAC Output |
| ADC IN | 4..6, 8..20 | Analog Input |
| Slot | 21..22 | Aligning Slot |
| GPIO | 23 | Digital General Purpose |
| GPIO | 24 | Digital General Purpose and SPI_MISO |
| GPIO | 25..31 | Digital General Purpose |
| GPIO | 32 | Digital General Purpose and SPI_SCK |
| GPIO | 33..35 | Digital General Purpose |
| GPIO | 36 | Digital General Purpose and SPI_MOSI |
| GPIO | 37..38 | Digital General Purpose |
| GPIO | 39 | Digital General Purpose and SER0 (UART to mikroBUS™ socket) |
| GPIO | 40..44 | Digital General Purpose |
| PWM1H | 45 | PWM Output |
| GPIO | 46 | Digital General Purpose |
| PWM1L | 47 | PWM Output |
| GPIO | 48 | Digital General Purpose and SPI Chip Select |
| MCLR | 49 | Reset |
| GPIO | 50 | Digital General Purpose and SER1 (UART to mikroBUS socket) |
| PGEC | 51 | Programming/Debugging Line |
| GPIO | 52 | Digital General Purpose and Red LED |
| SCL | 53 | I$^2$C Clock Line |
| GPIO | 54 | Digital General Purpose and Green LED |
| SDA | 55 | I$^2$C Data Line |
| PGED | 56 | Programming/Debugging Line |
| 5V | 57, 59 | V$_{DD}$ Rail |
| GND | 58, 60 | Digital Ground |

# Digital Power Development Board User's Guide

## A.2    BOARD SCHEMATIC

Figure A-1 shows the board schematic.

## FIGURE A-1:    DIGITAL POWER DEVELOPMENT BOARD SCHEMATIC

## A.3  PCB LAYOUT

The Digital Power Development Board is a two-layer FR4, 1.55 mm, Plated-Through-Hole PCB construction. Figure A-2 through Figure A-5 illustrate the PCB layers. Figure A-6 and Figure A-7 show the assembly drawings of the Digital Power Development Board.

**FIGURE A-2:**    **DIGITAL POWER DEVELOPMENT BOARD TOP SILKSCREEN**
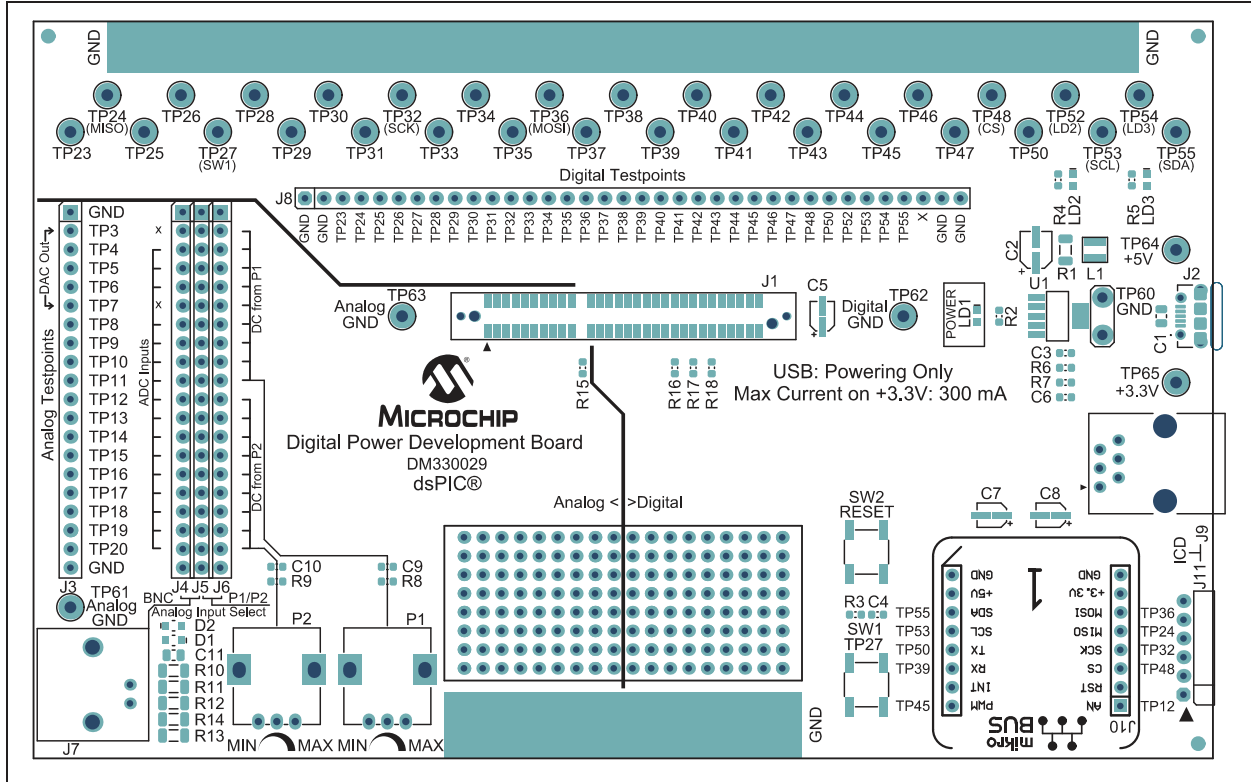


**FIGURE A-3:**    **DIGITAL POWER DEVELOPMENT BOARD TOP COPPER**
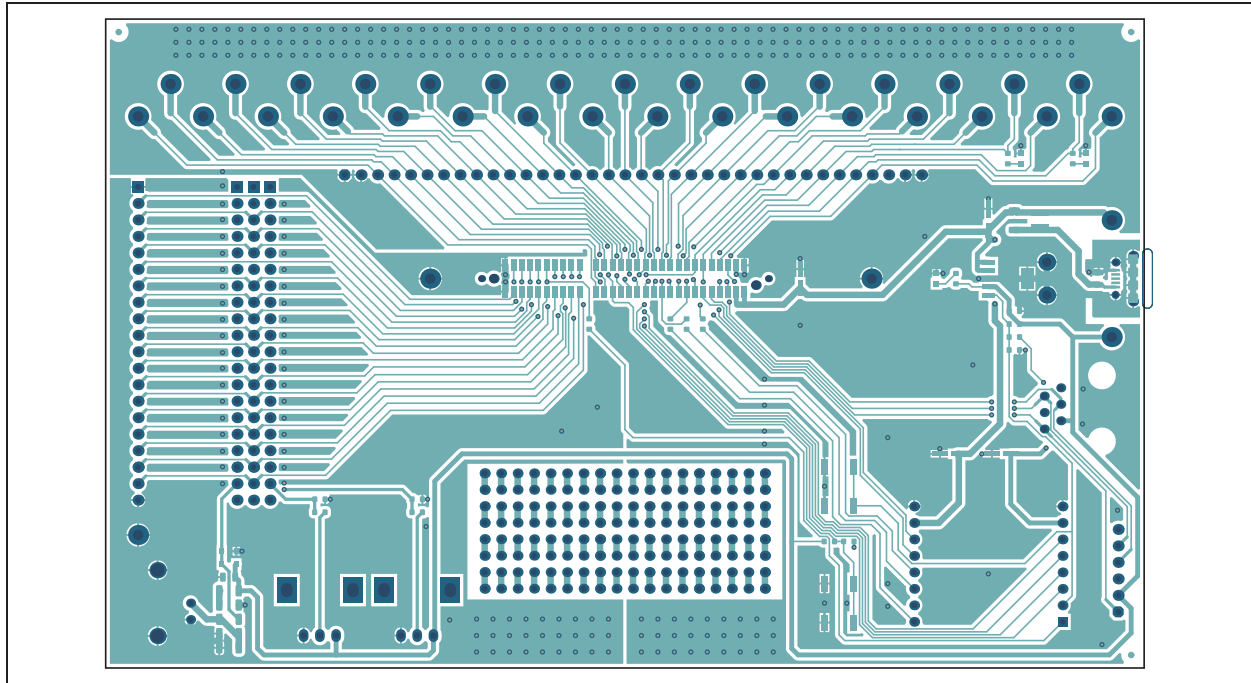
# Digital Power Development Board User's Guide

**FIGURE A-4:**      **DIGITAL POWER DEVELOPMENT BOARD BOTTOM COPPER (BOTTOM VIEW)**



**FIGURE A-5:**      **DIGITAL POWER DEVELOPMENT BOARD BOTTOM SILKSCREEN (BOTTOM VIEW)**

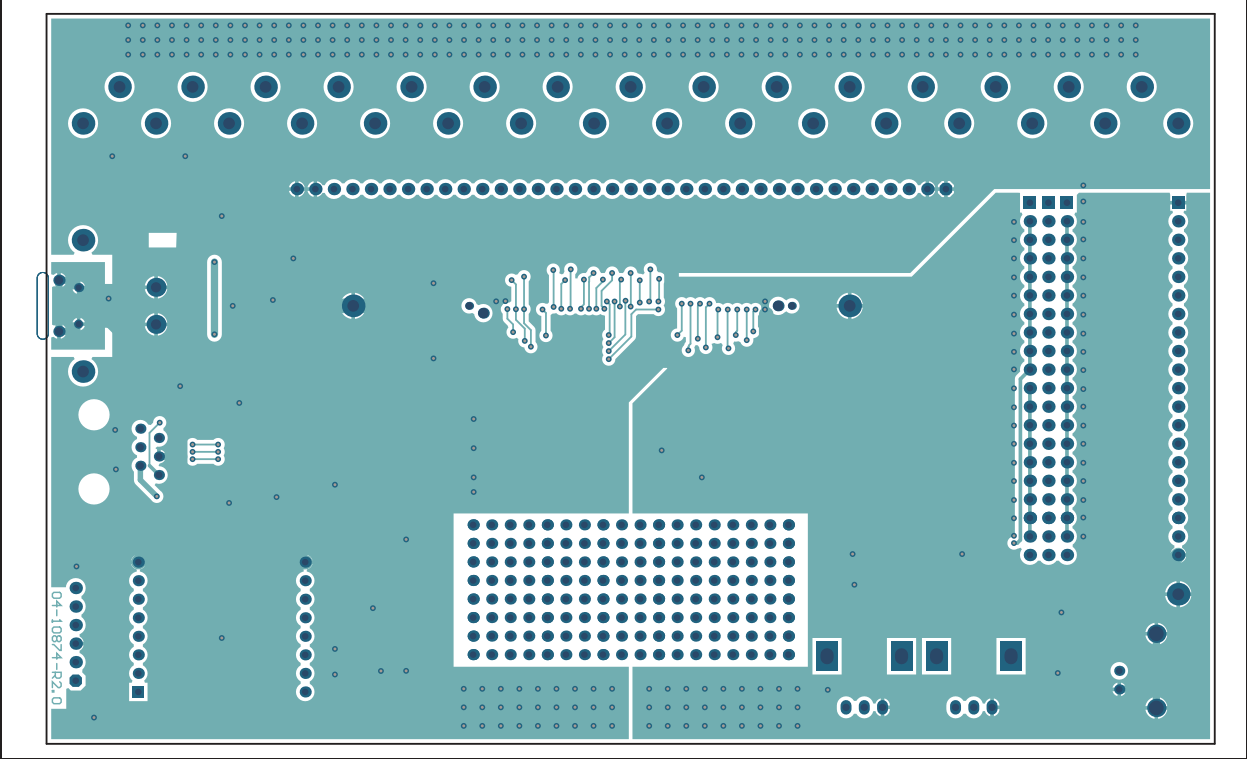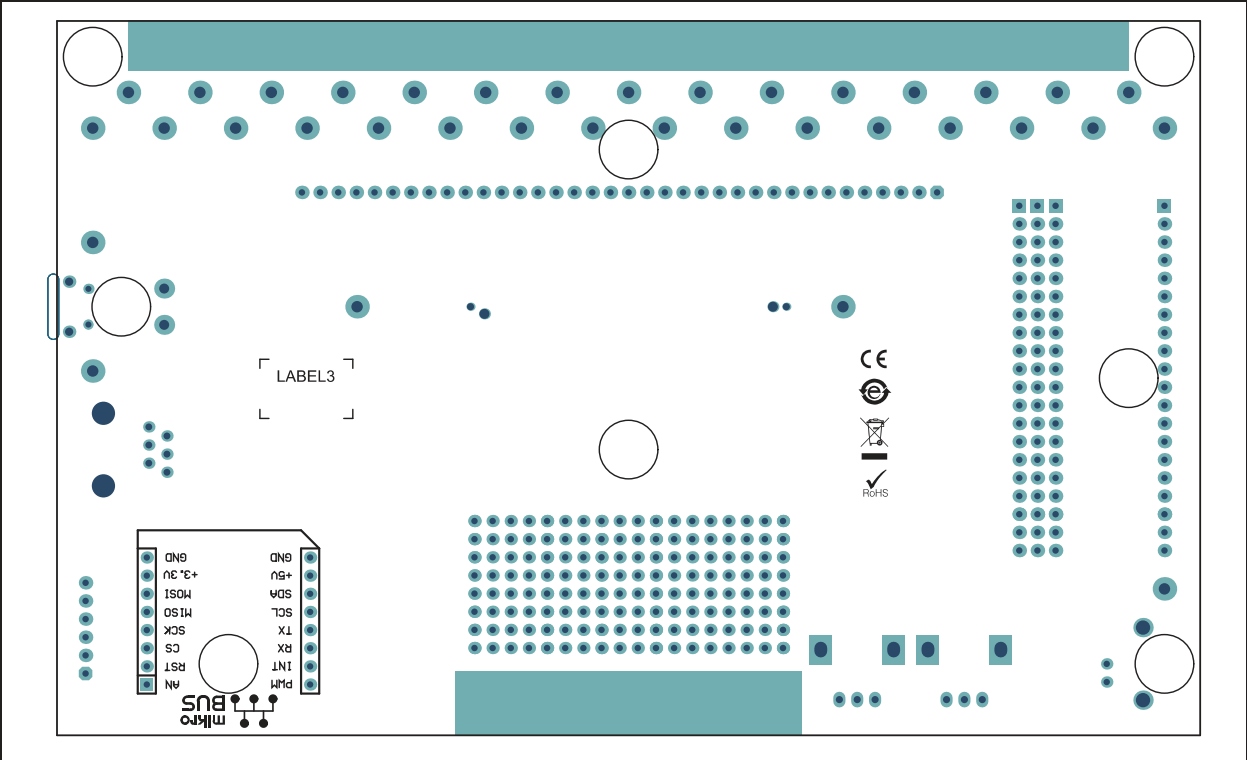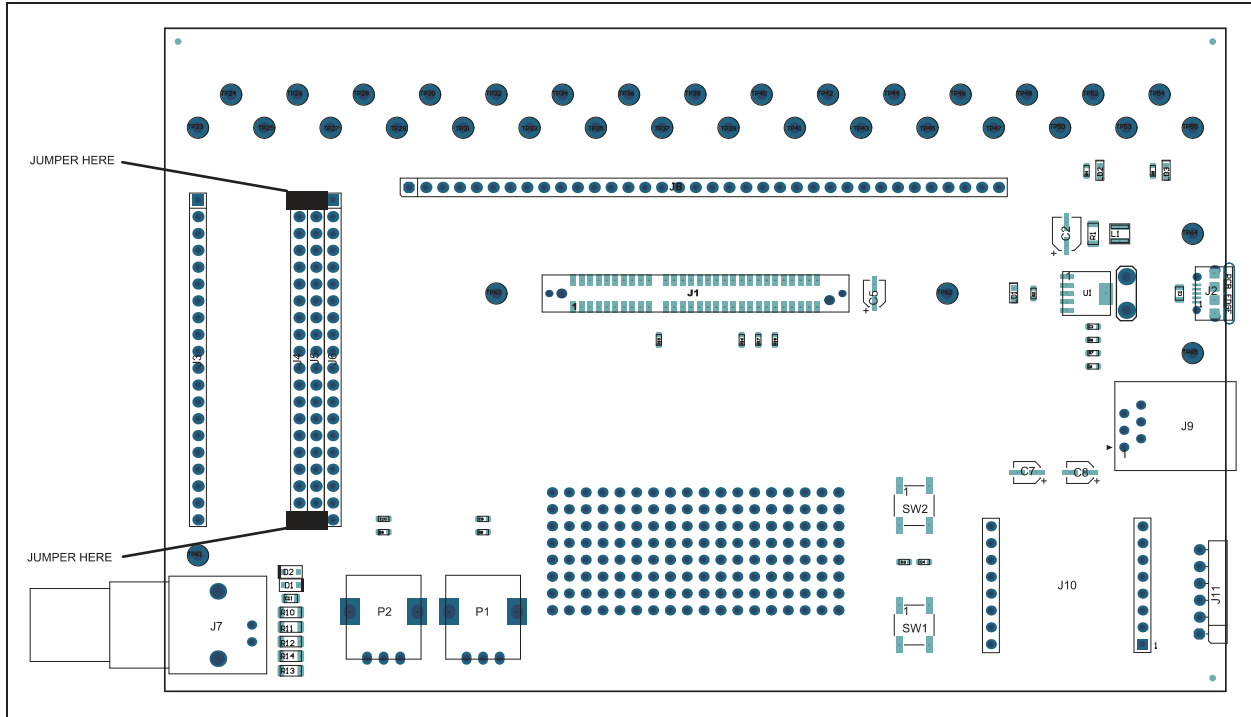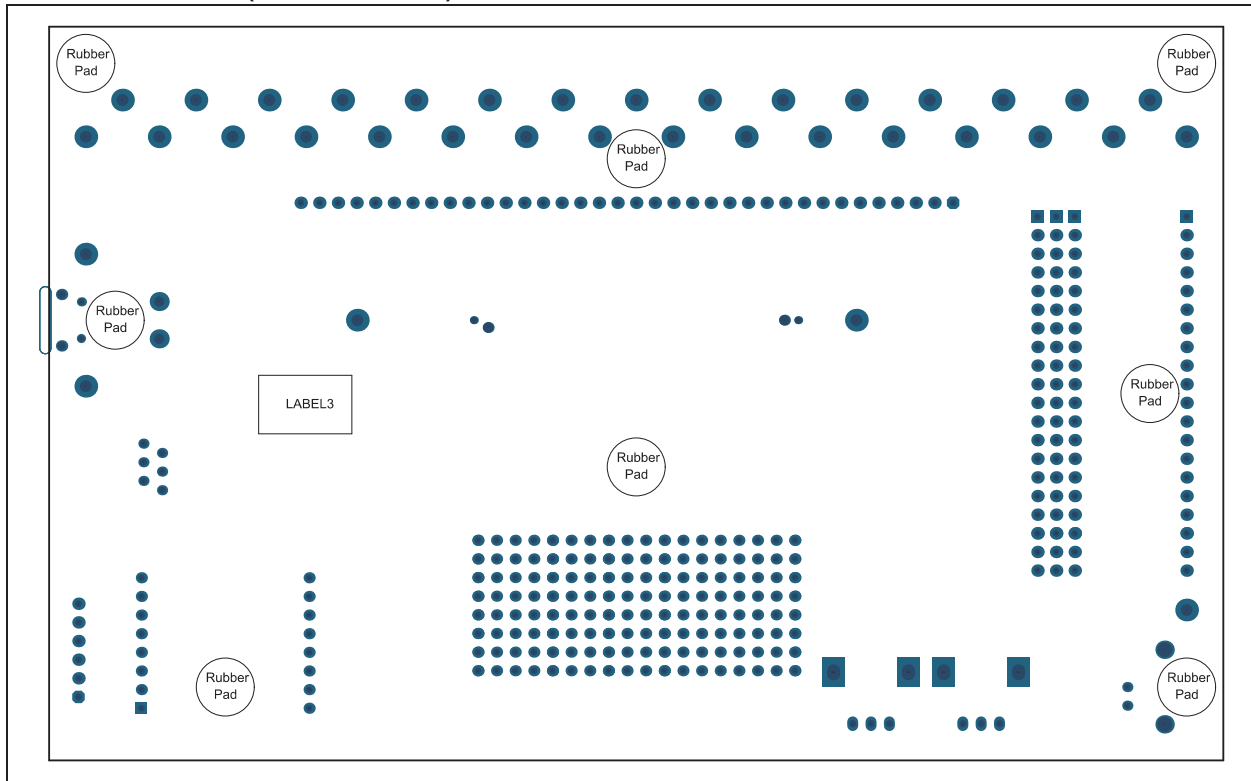**FIGURE A-6:     DIGITAL POWER DEVELOPMENT BOARD TOP ASSEMBLY**



**FIGURE A-7:     DIGITAL POWER DEVELOPMENT BOARD BOTTOM ASSEMBLY
(BOTTOM VIEW)**

**NOTES:**

# Appendix B. Bill of Materials (BOM)

This appendix contains the Bill of Materials (BOM) for the Digital Power Development Board.

• Bill of Materials

## B.1    BILL OF MATERIALS

Table B-1 shows the Bill of Materials for the Digital Power Development Board.

**TABLE B-1:    DIGITAL POWER DEV. BOARD BILL OF MATERIALS (BOM)**

| Qty. | Designator | Description | Manufacturer | Manufacturer Part Number |
|---|---|---|---|---|
| 2 | C1, C11 | Capacitor, Ceramic, 10 µF, 16V, 20%, X7R, SMD, 0805 | Taiyo Yuden Co., Ltd. | EMK212BB7106MG-T |
| 1 | C2 | Capacitor Aluminum, 33 µF, 10V, 20%, SMD, B | Panasonic® - ECG | EEE-1AA330WR |
| 3 | C3, C9, C10 | Capacitor Ceramic, 1 µF, 50V, 10%, X7R, SMD, 0603 | Taiyo Yuden Co., Ltd. | UMK107AB7105KA-T |
| 2 | C4, C6 | Capacitor Ceramic, 0.01 µF, 100V, 10%, X7R, SMD, 0603 | TDK Corporation | C1608X7R2A103K080AA |
| 3 | C5, C7, C8 | Capacitor, Aluminum, 10 µF, 16V, 20%, SMD, A | Nichicon Corporation | UWX1C100MCL2GB |
| 2 | D1, D2 | Diode, Rectifier, 1N4148, 855 mV, 300 mA, 75V, SOD-323 | Diodes Incorporated® | 1N4148WS-7-F |
| 1 | J1 | Connector, Edge, MECF, 1.27 mm, 60P, Female, SMD, Vertical | Samtec, Inc. | MECF-30-01-L-DV-WT |
| 1 | J2 | Connector, USB 2.0, micro-B, Female, TH/SMD, R/A | FCI | 10118194-0001LF |
| 4 | J3, J4, J5, J6 | Connector Header-2.54, Male, 1x20, Gold, 5.84 MH, TH, Vertical | Samtec, Inc. | TSW-120-07-G-S |
| 1 | J7 | Connector, RF, Coaxial, BNC, Female, 2P, TH, RA | Amphenol Commercial | 31-5431-2010 |
| 1 | J8 | Connector Header-2.54, Male, 1x36, Gold, 5.84 MH, TH | Sullins Connector Solutions | PBC36SAAN |
| 1 | J9 | Connector, Modular, RJ25, TH, R/A | Tyco Electronics (TE Connectivity Ltd.) | 5555165-1 |
| 2 | J10 | Socket, mikroBUS™, Host, DIP, 16, TH | Sullins Connector Solutions | PPTC081LFBN-RC |
| 1 | J11 | Connector Header-2.54, Male, 1x6, Gold, 5.84 MH, TH, R/A | FCI | 68016-106HLF |
| 1 | L1 | Inductor, 1 µH, 2.1A, 30%, SMD, L3W3H1.5 | Taiyo Yuden Co., Ltd. | NR3015T1R0N |
| 1 | LD1 | Diode, LED, Green, 2V, 30 mA, 35 mcd, Clear, SMD, 0603 | Lite-On®, Inc. | LTST-C190KGKT |

**TABLE B-1:** **DIGITAL POWER DEV. BOARD BILL OF MATERIALS (BOM) (CONTINUED)**

| Qty. | Designator | Description | Manufacturer | Manufacturer Part Number |
|------|-----------|-------------|--------------|--------------------------|
| 1 | LD2 | Diode, LED, Red, 1.8V, 40 mA, 10 mcd, Clear, SMD, 0603 | Lite-On®, Inc. | LTST-C190KRKT |
| 1 | LD3 | Diode, LED, Green, 2.2V, 25 mA, 18 mcd, Diffuse, SMD, 0603 | Lumex® Inc. | SML-LX0603GW-TR |
| 2 | P1, P2 | Resistor, Variable, 10K, 20%, TH, P090S | TT Electronics Plc. | P090S-14T20BR10K |
| 1 | R1 | Resistor, TKF, 1R, 1%, 1/2W, SMD, 1206 | Stackpole Electronics, Inc. | CSR1206FT1R00 |
| 1 | R2 | Resistor, TKF, 1.5k, 1%, 1/10W, SMD, 0603 | Panasonic® - ECG | ERJ-3EKF1501V |
| 3 | R3, R4, R5 | Resistor, TKF, 3.3k, 1%, 1/10W, SMD, 0603 | Panasonic - ECG | ERJ-3EKF3301V |
| 1 | R6 | Resistor, TKF, 22k, 1%, 1/10W, SMD, 0603 | Panasonic - ECG | ERJ-3EKF2202V |
| 1 | R7 | Resistor, TKF, 150R, 1%, 1/10W, SMD, 0603 | Stackpole Electronics, Inc. | RMCF0603FT150R |
| 2 | R8, R9 | Resistor, TKF, 1k, 1%, 1/10W, SMD, 0603 | Panasonic - ECG | ERJ-3EKF1001V |
| 5 | R10, R11, R12, R13, R14 | Resistor, TKF, 100R, 1%, 1/4W, SMD, 1206 | Yageo Corporation | RC1206FR-07100RL |
| 4 | R15, R16, R17, R18 | Resistor, TKF, 0R, 1/10W, SMD, 0603 | NIC Components Corp. | NRC06Z0TRF |
| 2 | SW1, SW2 | Switch, Tact, SPST, 12V, 50 mA, MCLTL-613R | Multicomp Inc. | MCLTL-613R |
| 31 | TP23, TP24, TP25, TP26, TP27, TP28, TP29, TP30, TP31, TP32, TP33, TP34, TP35, TP36, TP37, TP38, TP39, TP40, TP41, TP42, TP43, TP44, TP45, TP46, TP47, TP48, TP50, TP52, TP53, TP54, TP55 | Connector, TP, Loop, Orange, TH | Keystone Electronics Corp. | 5013 |
| 1 | TP60 | Connector, TP, Tab, 0.250, TH | Tyco Electronics (TE Connectivity Ltd.) | 1217861-1 |
| 3 | TP61, TP62, TP63 | Connector, TP, Loop, Black, TH | Keystone Electronics Corp. | 5011 |
| 2 | TP64, TP65 | Connector, TP, Loop, Red, TH | Keystone Electronics Corp. | 5010 |
| 1 | U1 | Microchip Analog, LDO, 3.3V, MCP1755T-3302E/DC, SOT-223-5 | Microchip Technology Inc. | **MCP1755T-3302E/DC** |

**NOTES:**

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