DLP-USB232M-G2
LEAD FREE

USB-to-Serial UART Interface Module

The DLP-USB232M-G2 is a redesign of DLP Design's legacy DLP-USB232M-G USB-to-UART interface module based on FTDI's second-generation FT232BL silicon.

The DLP-USB232M-G2 is manufactured in a lead-free, RoHS-compliant, compact, 0.1-inch spaced standard 0.6-inch wide DIP footprint. It is electrically and mechanically identical to the original DLP-USB232M-G module with the exception of having a wider PCB body. The interface pins on the bottom side of the module have the same 0.6-inch width for mounting to a standard 24-pin DIP footprint as the legacy module.

DLP-USB232M-G   **DLP-USB232M-G2**

0.70inch
17.8mm

0.86inch
21.8mm

For all other details on how to use this module, please refer to the datasheet for the original DLP-USB232M-G module on the DLP Design website.

The electrical schematic at the end of this document points out the subtle differences between this module and the legacy module. These differences are not detectable by the host PC.

DISCLAIMER

This product and its documentation are supplied on an as-is basis, and no warranty as to their suitability for any particular purpose is either made or implied. DLP Design, Inc. will not accept any claim for damages whatsoever arising as a result of the use or failure of this product. Your statutory rights are not affected. This product or any variant of it is not intended for use in any medical appliance, device or system in which the failure of the product might reasonably be expected to result in personal injury.

This document provides preliminary information that may be subject to change without notice.

CONTACT INFORMATION

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DLP-USB232M-G2

J2
Board ID

Pin	Signal		Pin	Signal
1	3V3OUT		13	TXLED#
2	RESET#		14	PWRCTL
3	RESETO#		15	POWEREN#
4	SLEEP#		16	TXDEN
5	RXLED#		17	RI#
6	VCC-IO		18	DCD#
7	EXTVCC		19	DSR#
8	PORTVCC		20	DTR#
9			21	CTS#
10			22	RTS#
11			23	RXD
12			24	TXD

CN1
USB Conn.
1
2
3
4

C4
0.47uF

PORTVCC

EXTVCC

USBVCC

C2
.01uF

FB1
HZ0805E601R-10
1
2

C6
10/10 Tant

USBVCC

Y1
6.00MHz

C9
27pF

C8
27pF

RESETO#

R3
27R

R2
27R

R4
1.5K

3V3OUT

C10
.033uF

Board ID

R6
100K
Populate for
DLP-USB245M

R5
100K
Populate for
DLP-USB232M

USBVCC

EEDATA

R7
2.2K

U2
93LC46B
3 DI   DO 1
   VSS VCC
4 CLK CS 5
       GND 6

R8
10K

EESK
EECS
USBVCC

EESK
EEDATA

EECS

RESET#

U1
FT232BL

TEST 31
EEDATA 2
EESK 1
EECS 32
RESET# 4
XTOUT 28
XTIN 27
RESETO# 5
USBDP 7
USBDM 8
3V3OUT 6

AGND 29
GND 9
GND 17

SLEEP# 10

AVCC 30
VCC 3
VCC 26
VCC-IO 13

R1
470

USBVCC

VCC_IO

C5 0.1uF
C7 0.1uF
C11 0.1uF
C3 0.1uF

TXD 24
RXD 23
RTS# 22
CTS# 21
DTR# 20
DSR# 19
DCD# 18
RI# 16
TXDEN 15
POWEREN# 14
PWRCTL 12
TXLED# 11
RXLED#

SLEEP#