

Compound Field Effect Power Transistor

μ PA1572B

N-CHANNEL POWER MOS FET ARRAY SWITCHING INDUSTRIAL USE

DESCRIPTION

The μ PA1572B is N-channel Power MOS FET Array that built in 4 circuits designed for solenoid, motor and lamp driver.

FEATURES

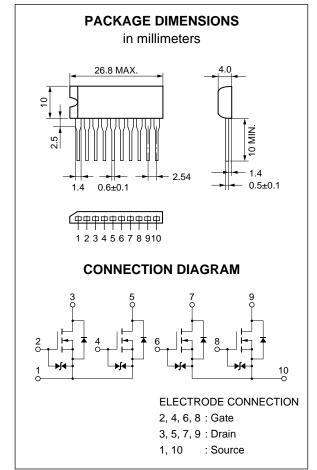
- Full Mold Package with 4 Circuits
- 4 V driving is possible
- Low On-state Resistance RDS(on) = 0.6 Ω MAX. (VGS = 10 V, ID = 1 A) RDS(on) = 0.8 Ω MAX. (VGS = 4 V, ID = 1 A)
- Low Input Capacitance Ciss = 110 pF TYP.

ORDERING INFORMATION

Type Number	Package
μPA1572BH	10Pin SIP

ABSOLUTE MAXIMUM RATINGS (TA = 25 $^{\circ}$ C)

Drain to Source Voltage (Vgs = 0)	VDSS	60	V
Gate to Source Voltage (Vps = 0)	VGSS (AC)	±20	V
Drain Current (DC)	ID (DS)	±2.0	A/unit
Drain Current (pulse)	ID (pulse) *1	±6.0	A/unit
Total Power Dissipation	P _{T1} *2	20	W
Total Power Dissipation	P _{T2} *3	3.0	W
Channel Temperature	Тсн	150	°C
Storage Tempreature	T _{stg} –	55 to +	150°C
Single Avalanche Current	las *4	5.0	Α
Single Avalanche Energy	Eas *4	0.1	mJ



Build-in Gate Diodes are for protection from static electricity in handing. In case high voltage over VGSs is applied, please append gate protection circuits.

The information in this document is subject to change without notice.

^{*1} PW \leq 10 μ s, Duty Cycle \leq 1 % *2 4 Circuits Tc = 25 °C



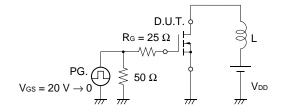
ELECTRICAL CHARACTERISTICS (TA = 25 °C)

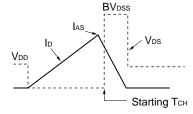
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION
Drain Leakage Current	IDSS			10	μΑ	VDS = 60 V, VGS = 0
Gate Leakage Current	Igss			±10	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Gate Cutoff Voltage	VGS (off)	1.0		2.0	V	V _{DS} = 10 V, I _D = 1.0 mA
Forward Transfer Admittance	Y _{fs}	0.5			S	Vps = 10 V, lp = 1.0 A
Drain to Source ON-Resistance	RDS (on)1		0.3	0.6	Ω	Ves = 10 V, Ip = 1.0 A
Drain to Sourse ON-Resistance	RDS (on)2		0.4	0.8	Ω	Vgs = 4.0 V, Ip = 1.0 A
Input Capacitance	Ciss		110		pF	V _{DS} = 10 V, V _{GS} = 0, f = 1.0 MHz
Output Capacitance	Coss		70		pF	
Reverse Transfer Capacitance	Crss		25		pF	
Turn-on Delay Time	td (on)		30		ns	$I_D = 1.0 \text{ A}, \text{ VGs (on)} = 10 \text{ V}, \text{ VDD} = 30 \text{ V}, \text{ RL} = 30 \Omega$
Rise Time	tr		200		ns	
Turn-off Delay Time	td (off)		100		ns	
Fall Time	tf		160		ns	
Total Gate Charge	QG		5.4		nC	Vgs = 10 V, ID = 2.0 A, VDD = 48 V
Gate to Source Charge	Qgs		0.7		nC	
Gate to Drain Charge	Q _{GD}		2.0		nC	
Body Diode Forward Voltage	VF (S-D)		1.0		V	IF = 2.0 A, VGS = 0
Reverse Recovery Time	trr		130		ns	$I_F = 2.0 \text{ A}, \text{ Vgs} = 0, \text{ di/dt} = 50 \text{ A/}\mu\text{s}$
Reverse Recovery Charge	Qrr		110		nC	

2

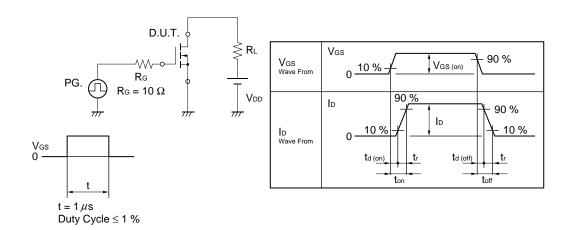


Test Circuit 1 Avalanche Capability

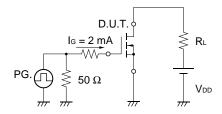




Test Circuit 2 Switching Time



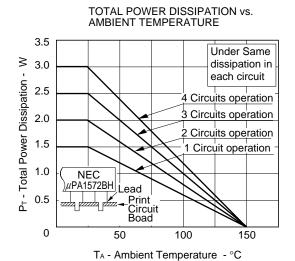
Test Circuit 3 Gate Charge

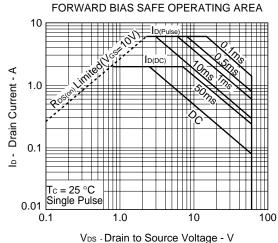


3

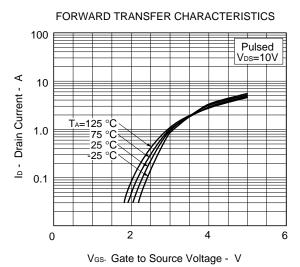


CHARACTERISTICS (TA = 25 °C)

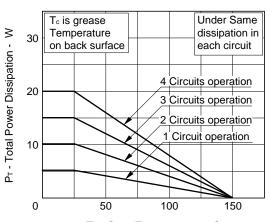




VDS - Drain to Source Voltage - V

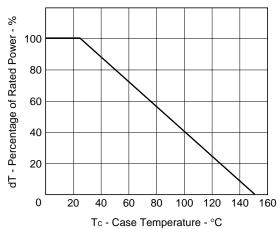


TOTAL POWER DISSIPATION vs. CASE TEMPERATURE

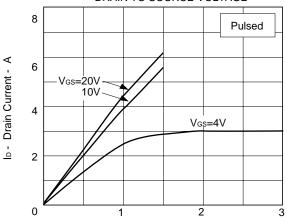


Tc - Case Temperature - °C

DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



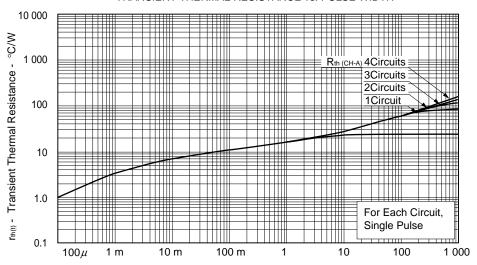
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



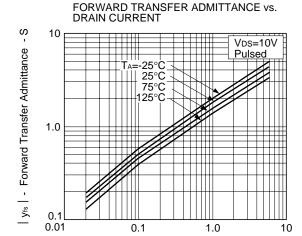
V_{DS} - Drain to Source Voltage - V



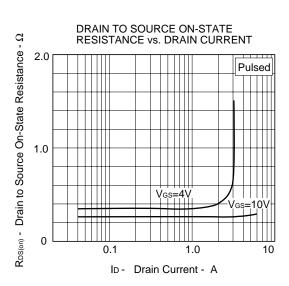
TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

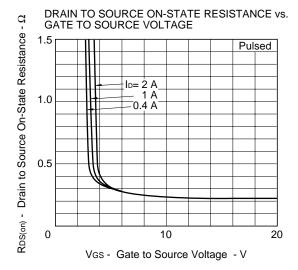


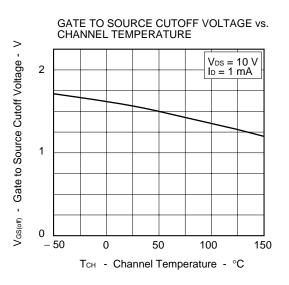
PW - Pulse Width - s

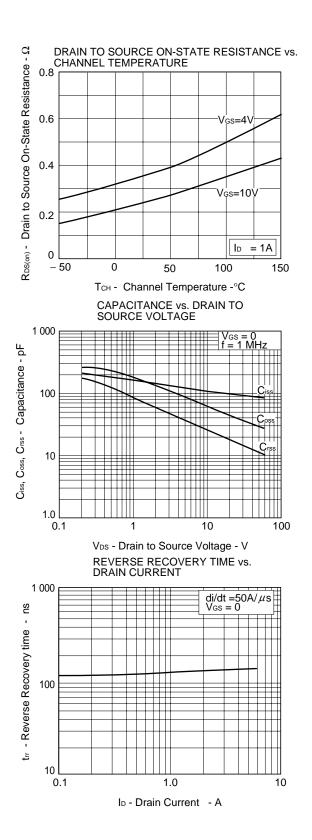


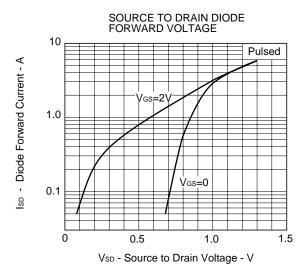
ID- Drain Current - A

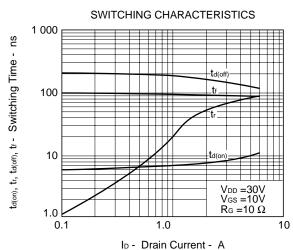


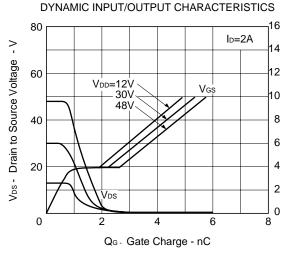




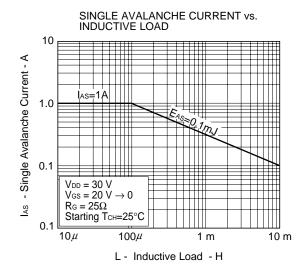


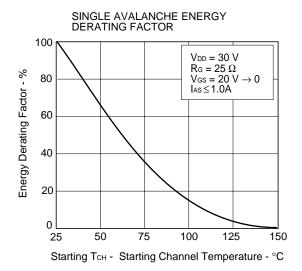












REFERENCE

Document Name	Document No.
NEC semiconductor device reliability/quality control system	TEI-1202
Quality grade on NEC semiconductor devices	IEI-1209
Semiconductor device mounting technology manual	C10535E
Semiconductor device package manual	C10943X
Guide to quality assurance for semiconductor devices	MEI-1202
Semiconductor selection guide	X10679E
Power MOS FET features and application switching power supply	TEA-1034
Application circuits using Power MOS FET	TEA-1035
Safe operating area of Power MOS FET	TEA-1037

No part of this document may be copied or reproduced in any form or by any means without the prior written consent of NEC Corporation. NEC Corporation assumes no responsibility for any errors which may appear in this document.

NEC Corporation does not assume any liability for infringement of patents, copyrights or other intellectual property rights of third parties by or arising from use of a device described herein or any other liability arising from use of such device. No license, either express, implied or otherwise, is granted under any patents, copyrights or other intellectual property rights of NEC Corporation or others.

While NEC Corporation has been making continuous effort to enhance the reliability of its semiconductor devices, the possibility of defects cannot be eliminated entirely. To minimize risks of damage or injury to persons or property arising from a defect in an NEC semiconductor device, customers must incorporate sufficient safety measures in its design, such as redundancy, fire-containment, and anti-failure features.

NEC devices are classified into the following three quality grades:

"Standard", "Special", and "Specific". The Specific quality grade applies only to devices developed based on a customer designated "quality assurance program" for a specific application. The recommended applications of a device depend on its quality grade, as indicated below. Customers must check the quality grade of each device before using it in a particular application.

Standard: Computers, office equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment and industrial robots

Special: Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support)

Specific: Aircrafts, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems or medical equipment for life support, etc.

The quality grade of NEC devices is "Standard" unless otherwise specified in NEC's Data Sheets or Data Books. If customers intend to use NEC devices for applications other than those specified for Standard quality grade, they should contact an NEC sales representative in advance.

Anti-radioactive design is not implemented in this product.