



PJQ1906

30V N-Channel Enhancement Mode MOSFET

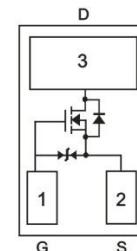
Voltage **30 V**

Current **300mA**

Features

- Advanced Trench Process Technology
- ESD Protected
- Specially Designed for Switch Load
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

DFN1006-3L



Mechanical Data

- Case : DFN1006-3L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0007 grams

Maximum Ratings and Thermal Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 10	
Continuous Drain Current ^(Note 4)	I_D	300	mA
Pulsed Drain Current ^(Note 1)	I_{DM}	600	
Power Dissipation	$T_A=25^\circ\text{C}$	700	mW
		5.6	$\text{mW}/^\circ\text{C}$
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~150	$^\circ\text{C}$
Typical Thermal Resistance - Junction to Ambient ^(Note 5)	$R_{\theta JA}$	175	$^\circ\text{C}/\text{W}$



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Electrical Characteristics ($T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.4	0.75	1.0	
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS}=4.5V, I_D=300mA$	-	0.7	1.2	Ω
		$V_{GS}=2.5V, I_D=200mA$	-	0.8	1.6	
		$V_{GS}=1.8V, I_D=100mA$	-	0.9	2.0	
		$V_{GS}=1.5V, I_D=50mA$	-	1.1	3.0	
		$V_{GS}=1.2V, I_D=20mA$		1.5	4.0	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=24V, V_{GS}=0V$	-	-	1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 8V, V_{DS}=0V$	-	-	± 10	
Dynamic ^(Note 6)						
Total Gate Charge	Q_g	$V_{DS}=10V, I_D=300mA,$ $V_{GS}=4.5V$	-	0.9	-	nC
Gate-Source Charge	Q_{gs}		-	0.3	-	
Gate-Drain Charge	Q_{gd}		-	0.2	-	
Input Capacitance	C_{iss}	$V_{DS}=10V, V_{GS}=0V,$ $f=1.0MHz$	-	45	-	pF
Output Capacitance	C_{oss}		-	14	-	
Reverse Transfer Capacitance	C_{rss}		-	0.8	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=10V, I_D=300mA,$ $V_{GS}=4V,$ $R_G=10\Omega$ ^(Note 1,2)	-	8.3	-	ns
Turn-On Rise Time	t_r		-	5.7	-	
Turn-Off Delay Time	$t_{d(off)}$		-	35	-	
Turn-Off Fall Time	t_f		-	12	-	
Drain-Source Diode						
Diode Forward Current	I_s	---	-	-	300	mA
Diode Forward Voltage	V_{SD}	$I_s=300mA, V_{GS}=0V$	-	0.9	1.3	V

Notes :

- 1.Pulse width<300us, Duty cycle<2%.
- 2.Essentially independent of operating temperature typical characteristics.
- 3.Repetitive rating, pulse width limited by junction temperature $T_J(MAX)=150^\circ C$. Ratings are based on low frequency and duty cycles to keep initial $T_J = 25^\circ C$.
4. $R_{Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.
- 5.Guaranteed by design, not subject to production testing.



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TYPICAL CHARACTERISTIC CURVES

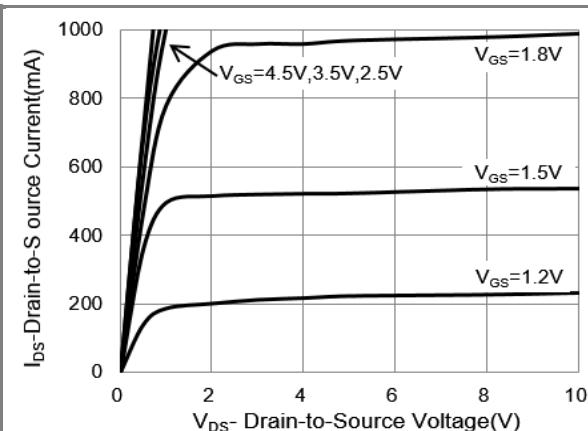


Fig.1 Output Characteristics

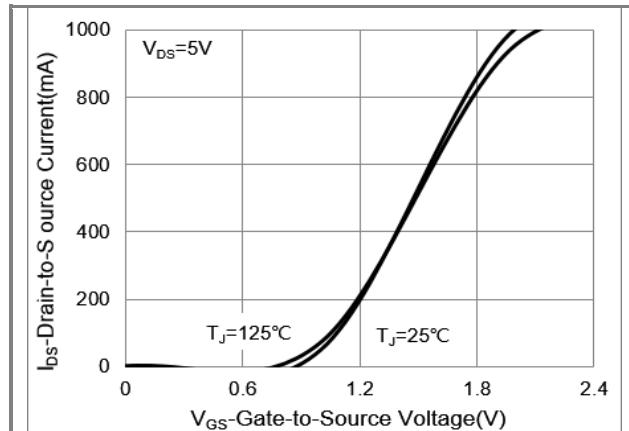


Fig.2 Transfer Characteristics

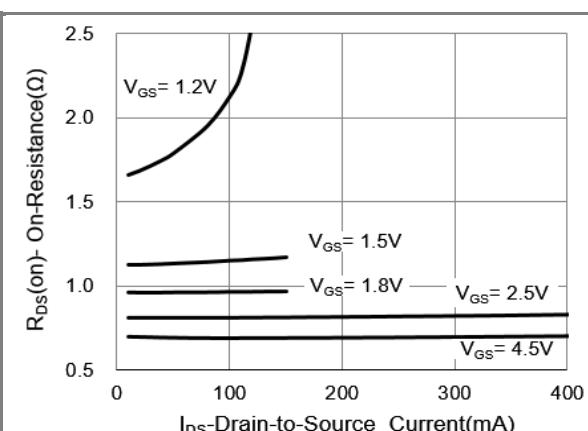


Fig.3 On-Resistance vs. Drain Current

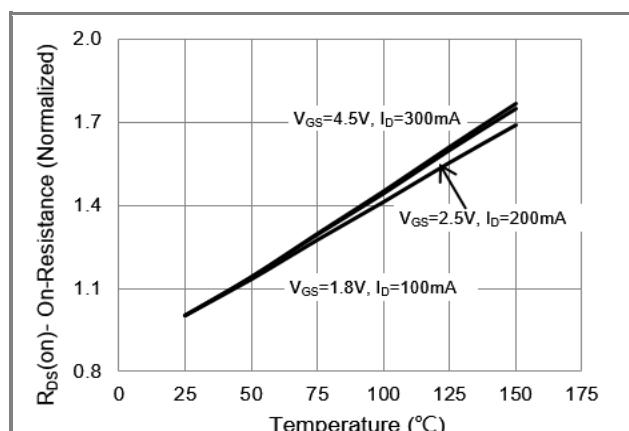


Fig.4 On-Resistance vs. Junction temperature

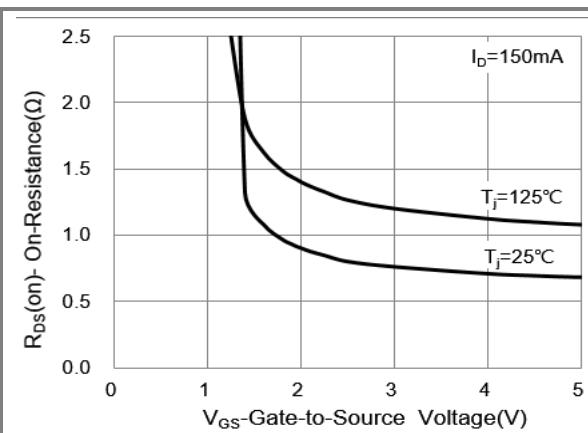


Fig.5 On-Resistance Variation with V_{GS}

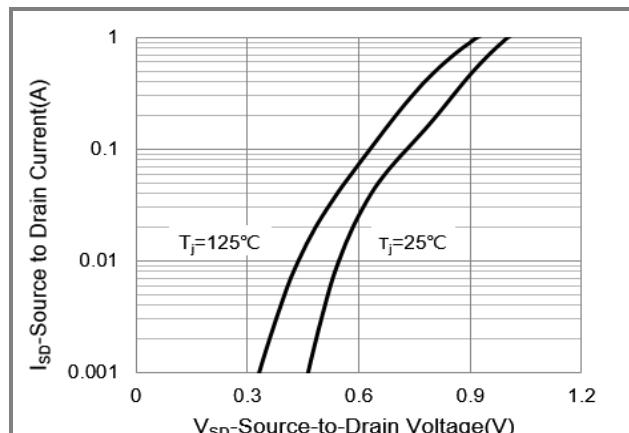


Fig.6 Source-Drain Diode Forward Voltage



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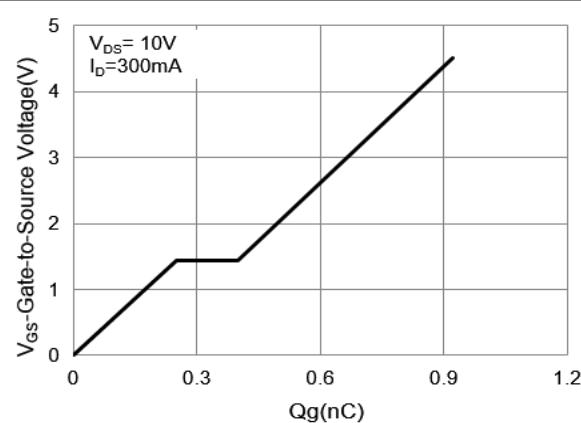


Fig.7 Gate-Charge Characteristics

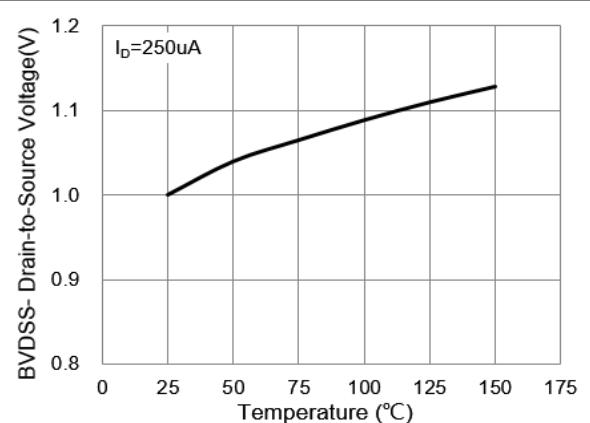


Fig.8 Breakdown Voltage Variation vs. Temperature

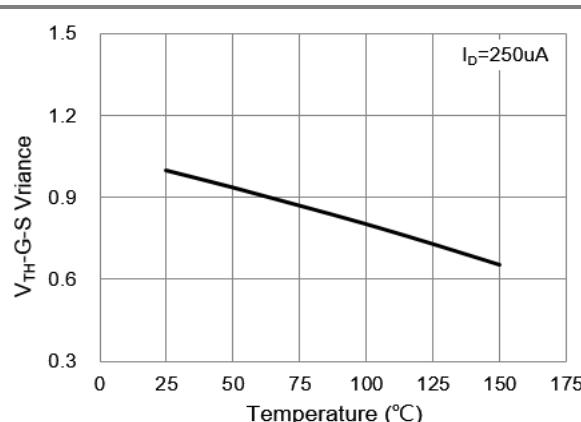


Fig.9 Threshold Voltage Variation with Temperature

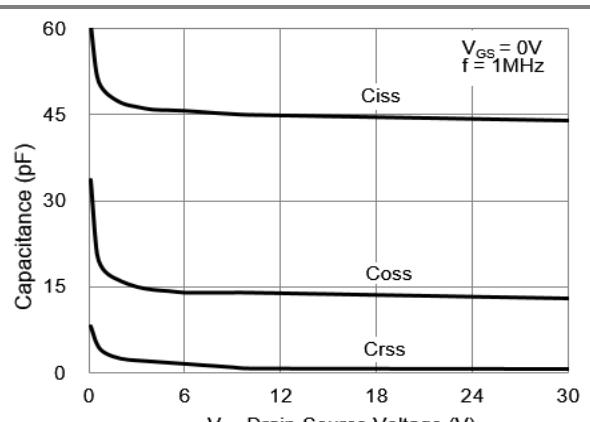


Fig.10 Capacitance vs. Drain-Source Voltage

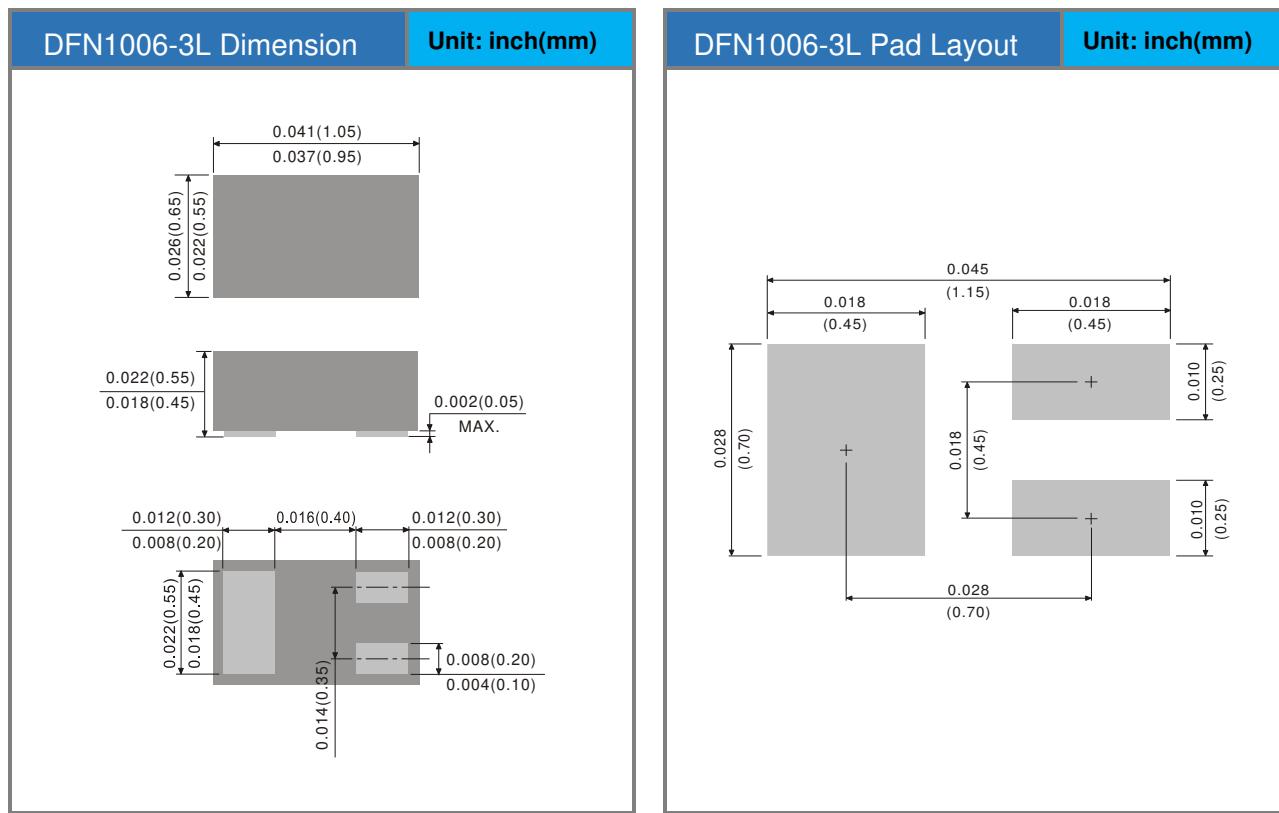


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Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJQ1906_R1_00201	DFN1006-3L	10K pcs / 7" reel	6	Halogen free RoHS compliant

Packaging Information & Mounting Pad Layout





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