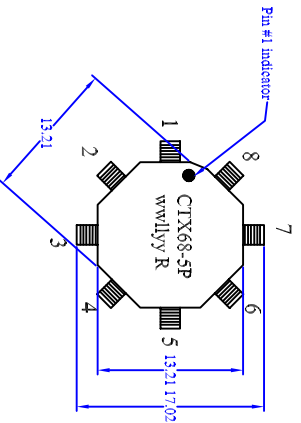
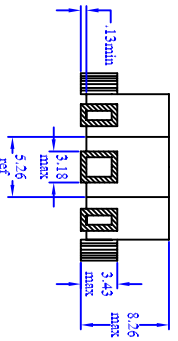


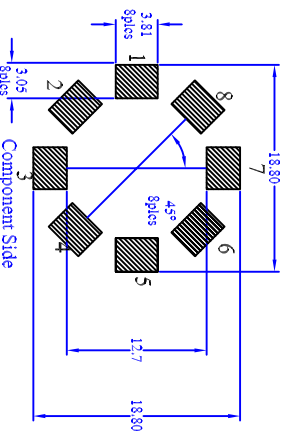
TOP VIEW



FRONT VIEW



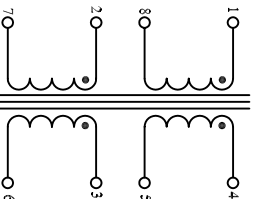
RECOMMENDED PCB LAYOUT



ELECTRICAL CHARACTERISTICS

OCL @100kHz, 0.010Vrms, 0.0Ade pins (1,2,3,4) - (8,7,6,5): 53.66 - 80.49 uH
 OCL @10kHz, 0.250Vrms, 1.72Ade pins (1,2,3,4) - (8,7,6,5): 43.32 uH min
 DCR @20°C pins (1-8): 0.450 ohms max
 DCR @20°C pins (2-7): 0.450 ohms max
 DCR @20°C pins (3-6): 0.450 ohms max
 DCR @20°C pins (4-5): 0.450 ohms max
 Hipot@300Vdc winding to winding for 1 second

SCHEMATIC



Notes:

- 1) All Dimensions are in millimeters unless otherwise specified.
- 2) Tolerances are +/- 0.25 unless stated otherwise.
- 3) All soldering surfaces to be coplanar within 0.15 millimeters.
- 4) Wwilly = (Date Code) R- (Revision Level)
- 5) PCB tolerances are +/- 0.1 unless stated otherwise.
- 6) Tie pins (1,2,3&4) and pins (8,7,6&5) together for 68uH@1.06Ade.

Cooper Electronic Technologies

3601 Quantum Boulevard, Boynton Beach, FL 33426
 Inductor/Transformer

68/1088uH @ 1.72/0.43Ade	Drawing Number	CTX68-5P	Sheet 3 of 5
SMT, Octape Size 5, 8 Pad, 8.26mm max ht	Size		
A	Revision Level	B	