# **ON Semiconductor**

## Is Now



To learn more about onsemi™, please visit our website at www.onsemi.com

onsemi and ONSEMI. and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. onsemi reserves the right to make changes at any time to any products or information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/ or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use onsemi products for any such unintended or unauthorized application,



ON Semiconductor®

## FDS3890

# 80V N-Channel Dual PowerTrench® MOSFET

## **General Description**

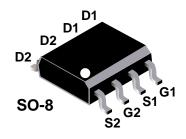
This N-Channel MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers.

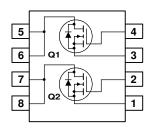
These MOSFETs feature faster switching and lower gate charge than other MOSFETs with comparable  $R_{\rm DS(ON)}$  specifications. The result is a MOSFET that is easy and safer to drive (even at very high frequencies), and DC/DC power supply designs with higher overall efficiency.

#### **Features**

4.7 A, 80 V.  $R_{DS(ON)} = 44 \ m\Omega \ @ \ V_{GS} = 10 \ V$   $R_{DS(ON)} = 50 \ m\Omega \ @ \ V_{GS} = 6 \ V$ 

- · Fast switching speed
- High performance trench technology for extremely low Research.
- · High power and current handling capability





## Absolute Maximum Ratings T<sub>A</sub>=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V <sub>DSS</sub>	Drain-Source Voltage		80	V
V <sub>GSS</sub>	Gate-Source Voltage		± 20	V
I <sub>D</sub>	Drain Current - Continuous	(Note 1a)	4.7	А
	- Pulsed		20	
P <sub>D</sub>	Power Dissipation for Dual Operation		2	W
	Power Dissipation for Single Operation	(Note 1a)	1.6	
		(Note 1b)	1.0	
		(Note 1c)	0.9	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range		−55 to +175	°C

## **Thermal Characteristics**

$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	(Note 1a)	78	°C/W
R <sub>eJC</sub>	Thermal Resistance, Junction-to-Case	(Note 1)	40	°C/W

**Package Marking and Ordering Information** 

Device Marking	Device	Reel Size	Tape width	Quantity
FDS3890	FDS3890	13"	12mm	2500 units

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Drain-Sc	ource Avalanche Ratings (Note	2)		<u> </u>		<u> </u>
W <sub>DSS</sub>	Single Pulse Drain-Source Avalanche Energy	$V_{DD} = 40 \text{ V},  I_D = 4.7 \text{ A}$			175	mJ
I <sub>AR</sub>	Maximum Drain-Source Avalanche Current				4.7	Α
Off Char	acteristics					
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	80			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	$I_D$ = 250 $\mu$ A, Referenced to 25°C		86		mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = 64 \text{ V}, \qquad V_{GS} = 0 \text{ V}$			1	μΑ
I <sub>GSSF</sub>	Gate-Body Leakage, Forward	$V_{GS} = 20 \text{ V}, \qquad V_{DS} = 0 \text{ V}$			100	nA
I <sub>GSSR</sub>	Gate-Body Leakage, Reverse	$V_{GS} = -20 \text{ V}$ $V_{DS} = 0 \text{ V}$			-100	nA
On Char	acteristics (Note 2)					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2	2.3	4	V
$\Delta V_{GS(th)} \over \Delta T_J$	Gate Threshold Voltage Temperature Coefficient	$I_D$ = 250 $\mu$ A, Referenced to 25°C		-6		mV/°C
$R_{\text{DS(on)}}$	Static Drain–Source On–Resistance	$\begin{split} &V_{GS} = 10 \ V, &I_{D} = 4.7 \ A \\ &V_{GS} = 6.0 \ V, &I_{D} = 4.4 \ A \\ &V_{GS} = 10 \ V, \ I_{D} = 4.7 \ A, \ T_{J} = 125^{\circ}C \end{split}$		34 37 60	44 50 82	mΩ
I <sub>D(on)</sub>	On-State Drain Current	$V_{GS} = 10 \text{ V}, \qquad V_{DS} = 5 \text{ V}$	20			Α
<b>g</b> <sub>FS</sub>	Forward Transconductance	$V_{DS} = 10 \text{ V}, \qquad I_{D} = 4.7 \text{ A}$		24		S
Dvnamic	Characteristics	•				
C <sub>iss</sub>	Input Capacitance	$V_{DS} = 40 \text{ V}, \qquad V_{GS} = 0 \text{ V},$		1180		pF
Coss	Output Capacitance	f = 1.0 MHz		171		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			50		pF
Switchin	g Characteristics (Note 2)				•	
t <sub>d(on)</sub>	Turn-On Delay Time	$V_{DD} = 40 \text{ V}, \qquad I_D = 1 \text{ A},$		11	20	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{GS} = 10 \text{ V}, \qquad R_{GEN} = 6 \Omega$		8	16	ns
t <sub>d(off)</sub>	Turn-Off Delay Time			26	50	ns
t <sub>f</sub>	Turn-Off Fall Time			12	25	ns
$Q_g$	Total Gate Charge	$V_{DS} = 40 \text{ V}, \qquad I_{D} = 4.7 \text{ A},$		25	35	nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>GS</sub> = 10 V		4.5		nC
$Q_{gd}$	Gate-Drain Charge			5.8		nC
Drain-So	ource Diode Characteristics a	and Maximum Ratings				
Is	Maximum Continuous Drain-Source				1.3	Α
V <sub>SD</sub>	Drain-Source Diode Forward	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 1.3 A (Note 2)		0.74	1.2	V

R<sub>BJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R<sub>BJC</sub> is guaranteed by design while R<sub>BCA</sub> is determined by the user's board design.



a) 78°C/W when mounted on a 1in<sup>2</sup> pad of 2 oz copper



b) 125°C/W when mounted on a .04 in<sup>2</sup> pad of 2 oz copper





c) 135°C/W when mounted on a minimum pad.

<sup>2.</sup> Pulse Test: Pulse Width <  $300\mu s$ , Duty Cycle < 2.0%

# **Typical Characteristics**

2.2

1.8

0.6

0.2

-50

R<sub>DS(ON)</sub>, NORMALIZED DRAIN-SOURCE ON-RESISTANCE

 $I_D = 4.7A$  $V_{GS} = 10V$ 

-25

0 25

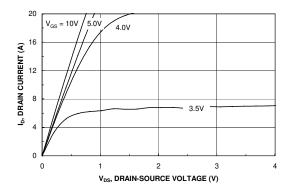
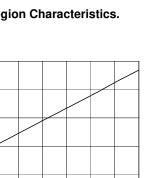


Figure 1. On-Region Characteristics.



150 175

125

Figure 3. On-Resistance Variation with Temperature.

50

T<sub>J</sub>, JUNCTION TEMPERATURE (°C)

75

100

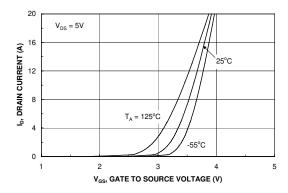


Figure 5. Transfer Characteristics.

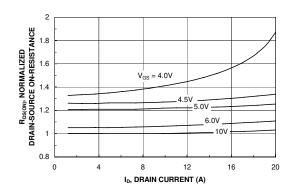


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

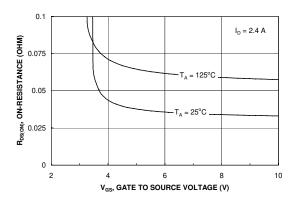


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

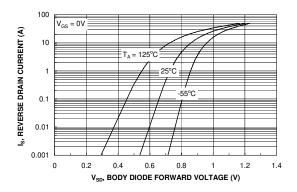
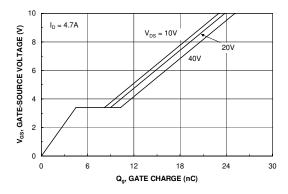


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

## **Typical Characteristics**



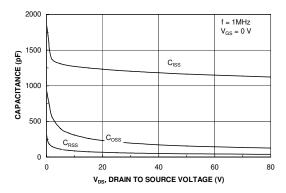
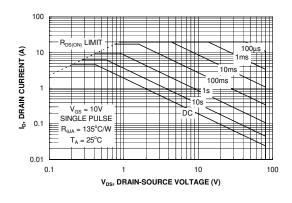


Figure 7. Gate Charge Characteristics.





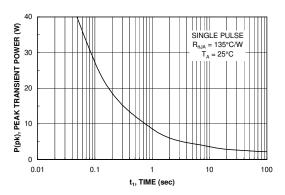


Figure 9. Maximum Safe Operating Area.



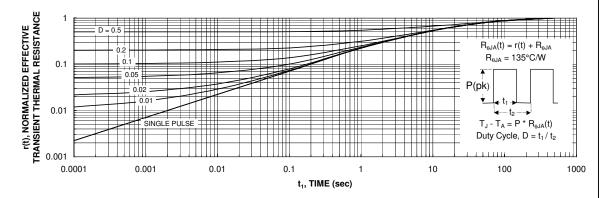


Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 1c.

Transient thermal response will change depending on the circuit board design.

ON Semiconductor and in are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at <a href="www.onsemi.com/site/pdf/Patent-Marking.pdf">www.onsemi.com/site/pdf/Patent-Marking.pdf</a>. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hol

#### **PUBLICATION ORDERING INFORMATION**

#### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800-282-9855 Toll Free USA/Canada
Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Phone: 81–3–5817–1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative