

## CMPA0060025F1

25 W, 20 MHz - 6.0 GHz, GaN MMIC, Power Amplifier

#### Description

Wolfspeed's CMPA0060025F1 is a gallium nitride (GaN) High Electron Mobility Transistor (HEMT) based monolithic microwave integrated circuit (MMIC). GaN has superior properties compared to silicon or gallium arsenide, including higher breakdown voltage, higher saturated electron drift velocity and higher thermal conductivity. GaN HEMTs also offer greater power density and wider bandwidths compared to Si and GaAs transistors. This MMIC enables extremely wide bandwidths to be achieved in a small footprint screw-down package



PN: CMPA0060025F1 Package Type: 440219

#### Typical Performance Over 20 MHz - 6.0 GHz ( $T_c = 25^{\circ}C$ )

Parameter	20 MHz	0.5 GHz	1.0 GHz	2.0 GHz	3.0 GHz	4.0 GHz	5.0 GHz	6.0 GHz	Units
Gain	21.4	20.1	19.3	16.7	16.6	16.8	15.7	15.5	dB
Output Power @ P <sub>IN</sub> = 32 dBm	26.9	30.2	26.3	23.4	24.5	24.0	20.9	18.6	W
Power Gain @ P <sub>IN</sub> = 32 dBm	12.3	12.8	12.2	11.7	11.9	11.8	11.3	10.7	dB
Efficiency @ P <sub>IN</sub> = 32 dBm	63	55	40	31	33	31	28	26	%

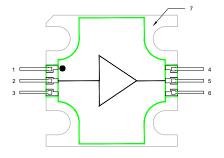
Note:  $V_{DD} = 50 \text{ V}$ ,  $I_{DQ} = 500 \text{ mA}$ 

#### **Features**

- 17 dB Small Signal Gain
- 25 W Typical P<sub>SAT</sub>
- Operation up to 50 V
- High Breakdown Voltage
- High Temperature Operation
- 0.5" x 0.5" total product size

#### Applications

- Ultra Broadband Amplifiers
- Test Instrumentation
- EMC Amplifier Drivers





#### Rev. 0.1, 2022-12-12



## Absolute Maximum Ratings (not simultaneous) at 25°C

Parameter	Symbol	Rating	Units	
Drain-Source Voltage	V <sub>DSS</sub>	84		
Gate-Source Voltage	V <sub>GS</sub>	-10, +2	V <sub>DC</sub>	
Storage Temperature	T <sub>STG</sub>	-65, +150	00	
Operating Junction Temperature	TJ	225	- °C	
Maximum Forward Gate Current	I <sub>GMAX</sub>	6.3	mA	
Soldering Temperature <sup>1</sup>	Ts	245	°C	
Screw Torque	τ	40	in-oz	
Thermal Resistance, Junction to Case	R <sub>θJC</sub>	3.3	°C/W	
Case Operating Temperature <sup>2</sup>	Tc	-40, +150	°C	

Notes:

<sup>1</sup> Refer to the Application Note on soldering at wolfspeed.com/rf/document-library

 $^{\rm 2}$  Measured for the CMPA0060025F1 at  $\rm P_{\rm IN}$  = 32 dBm

#### Electrical Characteristics (Frequency = 20 MHz to 6.0 GHz unless otherwise stated; $T_c = 25^{\circ}C$ )

Characteristics			Symbol	Ту	yp.	Max.	U	Inits	Condition	าร	
<b>DC Characteristics</b>											
Gate Threshold Voltag	ge <sup>2</sup>		$V_{\text{GS(th)}}$	-3	3.0	_		V	$V_{DS} = 20 \text{ V}, \Delta I_{D} = 20 \text{ mA}$		
Gate Quiescent Voltag	je		$V_{\text{GS}(\text{Q})}$	-2	2.7	—		V <sub>DC</sub>	$V_{DD} = 50 \text{ V}, I_{DQ} = 500 \text{ mA}, P_{IN} = 32 \text{ dBr}$		
Saturated Drain Curre	nt		$I_{DS}$	1	12	_		А	$V_{DS} = 12 V, V_{GS} = 2.0 V$		
<b>RF Characteristics</b> <sup>1</sup>	L										
Power Output at Pour	@ 4.5 G⊦	lz		42	2.8	-					
Power Output at Pour	@ 5.0 G⊦	lz	P <sub>OUT</sub>	43	3.3	—	0	dBm			
Power Output at Pour	@ 6.0 G⊦	lz		42	2.9	_				-500  m  A  D -32  d  B  m	
Drain Efficiency at $P_{ou}$	⊤@4.50	GHz		24	4.1	-			$V_{DD} = 50 \text{ V}, I_{DQ} = 500 \text{ mA}, P_{IN} = 32 \text{ dBm}$		
Drain Efficiency at Pou	⊤@5.00	GHz	η	28	8.0	_		%			
Drain Efficiency at Pou	⊤@6.00	GHz		2	7.2	—					
Output Mismatch Stre	SS		VSWR	-	_	5:1		Ψ		e at all phase angles, <sub>DQ</sub> = 500 mA, P <sub>IN</sub> = 32 dBm	
Small Signal RF Ch	aractei	ristics									
Frequency	Min.	Тур. S21 (dB)	Max.	Min.	Typ. S11 (dE	B) Ma	х.	Min.	Typ. S22 (dB)	Conditions	
0.02 GHz - 0.25 GHz	18	19.3	23.7	_	-4.1	-2.	5		-8.5		
0.25 GHz - 0.5 GHz	10	19.8		_	-6.8	-3.	5	_	-8.9		
0.5 GHz - 1.0 GHz	15.5		22			-6.	5	-	-6.7	$V_{DD} = 50 \text{ V}, I_{DO} = 500 \text{ mA}$	
1.0 GHz - 2.0 GHz	15.5	18.6		I	-15.3	11	-12.5		-0.7	$v_{DD} = 50 v$ , $v_{DQ} = 500 \text{ IIIA}$	
2.0 GHz - 3.0 GHz	13		20	-		-12		_	-6.0		
3.0 GHz - 6.0 GHz	13	16.3	20	_	-14.2	-6.	5	_	-12.0		

#### Notes:

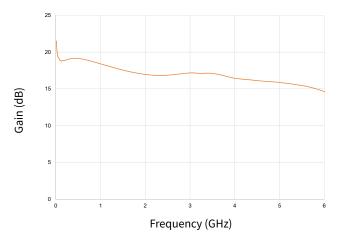
 $^{1}$  P<sub>OUT</sub> is defined as P<sub>IN</sub> = 32 dBm

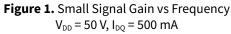
<sup>2</sup> The device will draw approximately 55-70 mA at pinch off due to the internal circuit structure

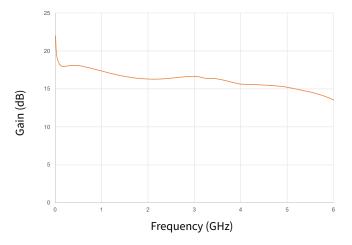
#### Rev. 0.1, 2022-12-12

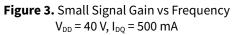
# 3

## **Typical Performance**









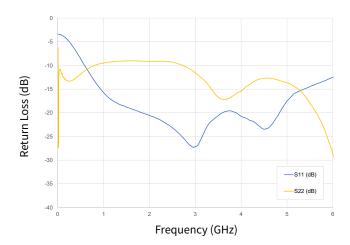


Figure 2. Input & Output Return Losses vs Frequency  $V_{\text{DD}}$  = 50 V,  $I_{\text{DQ}}$  = 500 mA

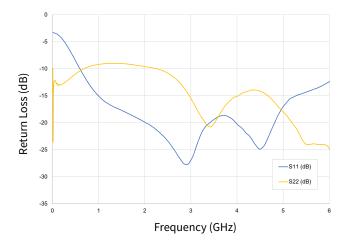
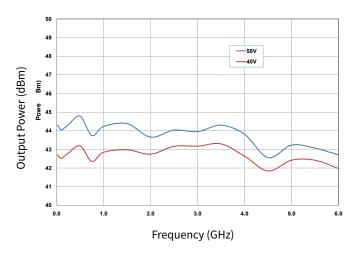


Figure 4. Small Signal Gain vs Frequency  $V_{DD}$  = 40 V,  $I_{DQ}$  = 500 mA



## **Typical Performance**



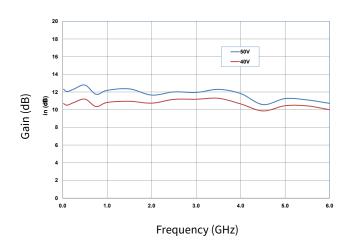
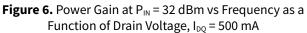
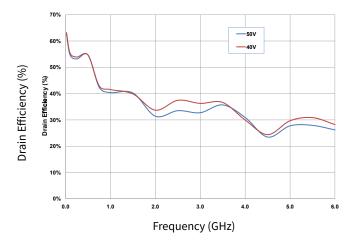
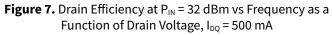


Figure 5. Output Power at  $P_{IN}$  = 32 dBm vs Frequency as aFigFunction of Drain Voltage,  $I_{DQ}$  = 500 mAFig

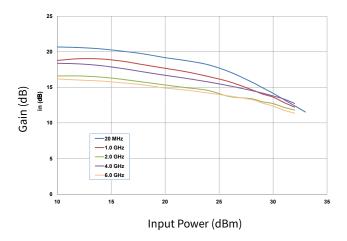


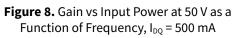


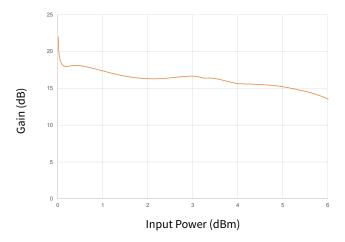


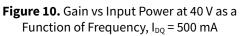


## **Typical Performance**









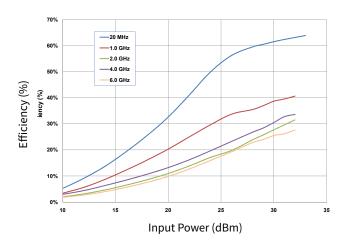


Figure 9. Efficiency vs Input Power at 50 V as a Function of Frequency,  $I_{DQ} = 500 \text{ mA}$ 

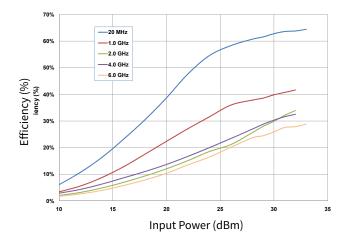


Figure 11. Efficiency vs Input Power at 40 V as a Function of Frequency,  $I_{DQ}$  = 500 mA

Rev. 0.1, 2022-12-12



### **General Device Information**

The CMPA0060025F1 is a GaN HEMT MMIC Power Amplifier, which operates between 20 MHz - 6.0 GHz. The amplifier typically provides 17 dB of small signal gain and 25 W saturated output power with an associated power added efficiency of better than 20%. The wideband amplifier's input and output are internally matched to 50 Ohm. The amplifier requires bias from appropriate Bias-T's, through the RF input and output ports.

The CMPA0060025F1-AMP1 and the device were then measured using external Bias-T's, (TECDIA: AMP1T-H06M20 or similar), as shown in Figure 2. The Bias-T's were included in the calibration of the test system. All other losses associated with the test fixture are included in the measurements.

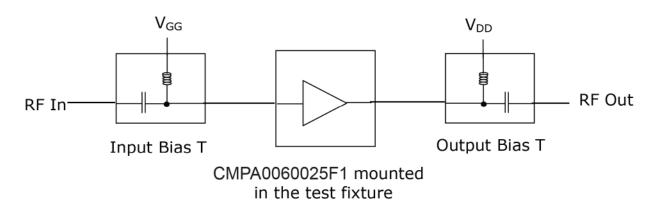
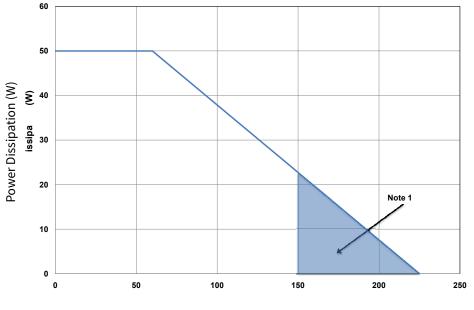


Figure 2. Typical test system setup required for measuring CMPA0060025F1-AMP1

Rev. 0.1, 2022-12-12



#### CMPA0060025F1 Power Dissipation De-rating Curve



Maximum Case Temperature (°C)

Note: Area exceeds Maximum Case Temperature (See Page 2)

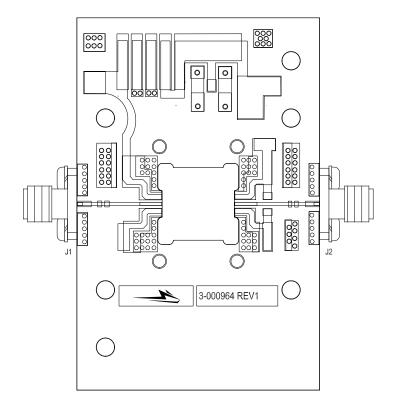
#### **Electrostatic Discharge (ESD) Classifications**

Parameter	Symbol	Class	Classification Level	Test Methodology
Human Body Model	НВМ	2	ANSI/ESDA/JEDEC JS-001 Table 3	JEDEC JESD22 A114-D
Charge Device Model	CDM	С3	ANSI/ESDA/JEDEC JS-002 Table 3	JEDEC JESD22 C101-C

Rev. 0.1, 2022-12-12



## CMPA0060025F1-AMP Demonstration Amplifier Circuit Outline



Rev. 0.1, 2022-12-12

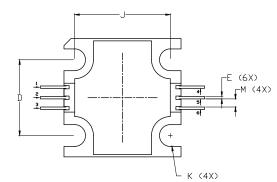


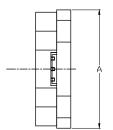
#### CMPA0060025F1-AMP Demonstration Amplifier Circuit Bill of Materials

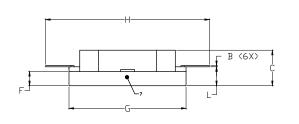
Designator	Description	Qty
J1,J2	CONNECTOR, SMA, AMP11052901-1	2
-	PCB, TACONIC, RF-35-0100-CH/CH	1
Q1	CMPA0060025F1	1

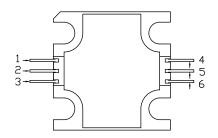
Note: An external Bias-T is required

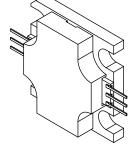
#### Product Dimensions CMPA0060025F1 (Package Type – 440219)











NOT TO SCALE

PIN	Function	
1	NC	
2	Gate	
3	NC	
4	NC	
5	Drain	
6	NC	
7	Source	

NDTES:

1. DIMENSIONING AND TOLERANICING PER ANSI Y14.5M, 1982.

2. CONTROLLING DIMENSION: INCH.

3. ADHESIVE FROM LID MAY EXTEND A MAXIMUM OF 0.020" BEYOND EDGE OF LID.

4. LID MAY BE MISALIGNED TO THE BODY OF THE PACKAGE BY A MAXIMUM OF 0.008" IN ANY DIRECTION. 5. ALL PLATED SURFACES ARE NI/AU

	INC	HES	MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
A	0.495	0.505	12.57	12.82	
В	0.003	0.005	0.076	0.127	
С	0.140	0.160	3.56	4.06	
D	0.315	0.325	8.00	8.25	
E	0.008	0.012	0.204	0.304	
F	0.055	0.065	1.40	1.65	
G	0.495	0.505	12.57	12.82	
н	0.695	0.705	17.65	17.91	
J	0.403	0.413	10.24	10.49	
к	ø.	092	2.3	34	
L	0.075	0.085	1.905	2.159	
М	0.032	0.040	0.82	1.02	

#### Rev. 0.1, 2022-12-12



## **Product Ordering Information**

Order Number	Description	Unit of Measure	Image
CMPA0060025F1	GaN HEMT	Each	Conversion of the second secon
CMPA0060025F1-AMP	Test board with GaN MMIC installed	Each	





#### For more information, please contact:

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RF Product Marketing Contact RFMarketing@wolfspeed.com

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#### Rev. 0.1, 2022-12-12