



60V DUAL N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Product Summary

BV _{DSS}	R _{DS(ON)}	I _D Tc = +25°C	
60V	14mΩ @ V _{GS} = 10V	40A	
	22mΩ @ V _{GS} = 4.5V	33A	

Features

- 100% Unclamped Inductive Switching (UIS) Test in Production -Ensures More Reliable and Robust End Application
- Thermally Efficient Package-Cooler Running Applications
- High Conversion Efficiency
- Low $R_{DS(ON)}$ Minimizes On State Losses
- Low Input Capacitance
- Fast Switching Speed
- Wettable Flank for Improved Optical Inspection
- **ESD Protected Gate**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

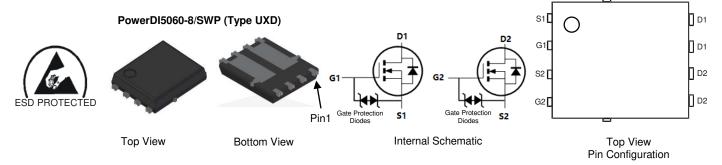
Description and Applications

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Wireless Charging
- **DC-DC Converters**
- Power Management

Mechanical Data

- Case: PowerDI®5060-8
- Case Material: Molded Plastic. "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.097 grams (Approximate)



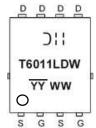
Ordering Information (Note 4)

Part Number	Case	Packaging
DMT6011LPDW-13	PowerDI5060-8/SWP (Type UXD)	2,500/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information



D H = Manufacturer's MarkingT6011LDW = Product Type Marking Code YYWW = Date Code Marking \overline{YY} = Last Two Digits of Year (ex: 21 = 2021) WW = Week Code (01 to 53)

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Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	60	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current, $V_{GS} = 10V$ (Note 6) $T_{C} = +25^{\circ}C$ $T_{C} = +70^{\circ}C$		lp	40 32	А	
Continuous Drain Current, Vos = 10V (Note 5)		$T_A = +25$ °C $T_A = +70$ °C	I _D	10.3 8.2	А
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)			I _{DM}	160	Α
Maximum Continuous Body Diode Forward Current (Note 6)			ls	40	Α
Pulsed Body Diode Forward Current (10μs Pulse, Duty Cycle = 1%)			Ism	160	Α
Avalanche Current L = 0.3mH			I _{AS}	16.2	Α
Avalanche Energy L = 0.3mH			Eas	39.4	mJ

Thermal Characteristics

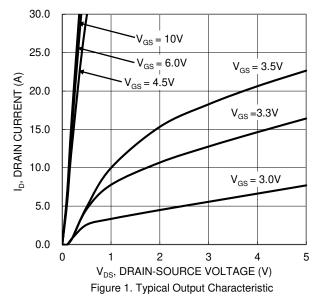
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	$T_A = +25^{\circ}C$	P_{D}	2.5	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	50	°C/W
Total Power Dissipation (Note 6)	Tc = +25°C	PD	37.9	W
Thermal Resistance, Junction to Case (Note 6)	Rejc	3.3	°C/W	
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

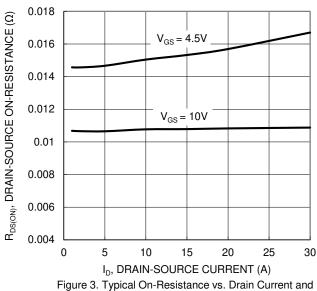
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)				•			
Drain-Source Breakdown Voltage	BV _{DSS}	60		_	V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	IDSS	_	_	1	μΑ	V _{DS} = 48V, V _{GS} = 0V	
Gate-Source Leakage	Igss	_		±10	μΑ	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	1.4	_	2.5	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance		_	10.8	14	mΩ	VGS = 10V, ID = 10A	
Static Drain-Source On-nesistance	R _{DS(ON)}	_	14.7	22	11177	VGS = 4.5V, ID = 5A	
Diode Forward Voltage	V_{SD}	_	0.7	1.2	V	V _{GS} = 0V, I _S = 1A	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	-	1072	_		$V_{DS} = 30V$, $V_{GS} = 0V$, $f = 1MHz$	
Output Capacitance	Coss	_	382	_	pF		
Reverse Transfer Capacitance	Crss	_	38	_			
Gate Resistance	Rg	_	1.4	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	11.8	_			
Total Gate Charge (VGS = 10V)	Qg	_	22.2	_	~C), 20)/ I= 10A	
Gate-Source Charge	Qgs	_	3.8	_	nC	V _{DD} = 30V, I _D = 10A	
Gate-Drain Charge	Qgd	_	5.0	_			
Turn-On Delay Time	tD(ON)	_	8.2	_		V _G S = 10V, V _{DD} = 30V,	
Turn-On Rise Time	t _R		3.9	_			
Turn-Off Delay Time	tD(OFF)		21.2	_	ns	$R_G = 6\Omega$, $I_D = 10A$	
Turn-Off Fall Time	t _F		15.7	_			
Reverse Recovery Time	t _{RR}	_	30.6	_	ns	100 11/-14 1000/	
Reverse Recovery Charge	Qrr	-	21.9	_	nC	IF = 10A, di/dt = 100A/μs	

 Device mounted on FR-4 substrate PC board, 2oz. copper, with thermal bias to bottom layer 1inch square copper plate.
 Thermal resistance from junction to soldering point (on the exposed drain pad).
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing. Notes:







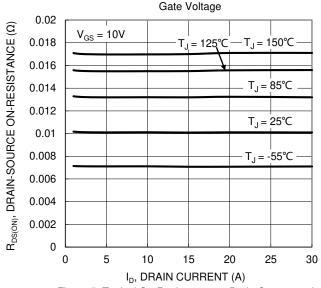


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

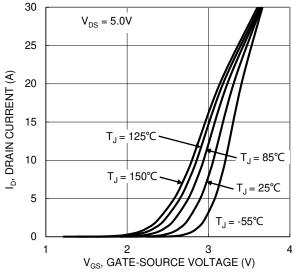


Figure 2. Typical Transfer Characteristic

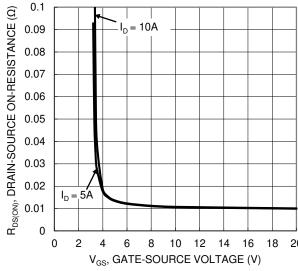


Figure 4. Typical Transfer Characteristic

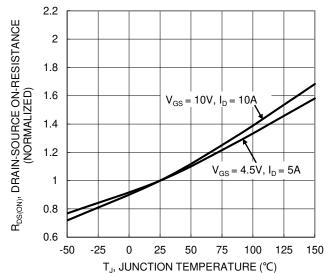


Figure 6. On-Resistance Variation with Junction Temperature



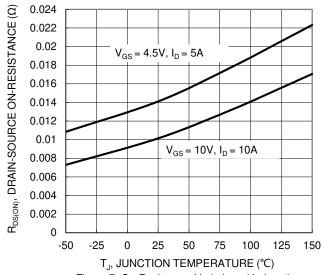


Figure 7. On-Resistance Variation with Junction Temperature

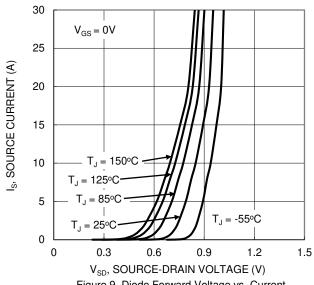
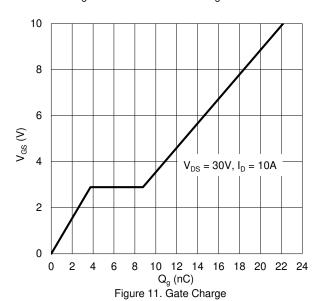
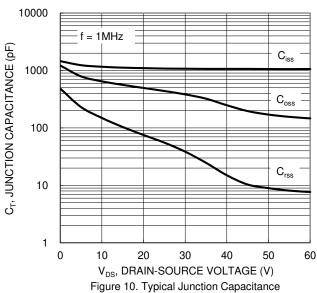


Figure 9. Diode Forward Voltage vs. Current



3 $V_{GS(TH)},$ GATE THRESHOLD VOLTAGE (V) 2.5 $I_D = 1mA$ 2 1.5 $I_D = 250 \mu A$ 1 0.5 0 -25 0 125 -50 25 50 75 100 150 T_J, JUNCTION TEMPERATURE (°C)

Figure 8. Gate Threshold Variation vs. Junction Temperature



1000 100 ID, DRAIN CURRENT (A) 10 $T_{J(Max)} = 150$ °C T_C = 25°C 0.1 Single Pulse DUT on Infinite Heatsink $V_{GS} = 10V$ 0.01 0.1 10 100 V_{DS} , DRAIN-SOURCE VOLTAGE (V) Figure 12. SOA, Safe Operation Area



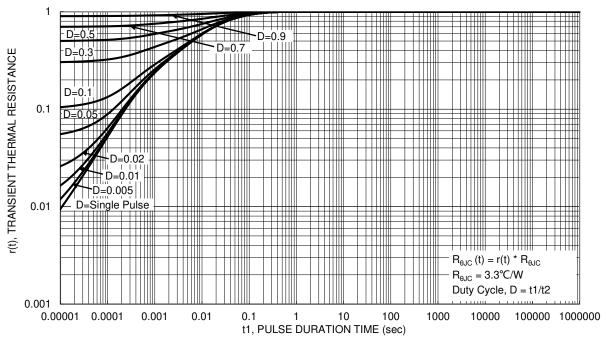
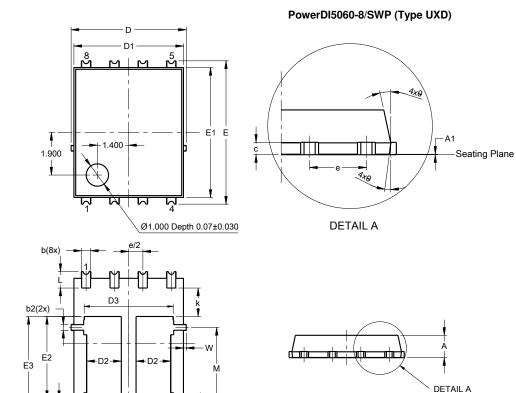


Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

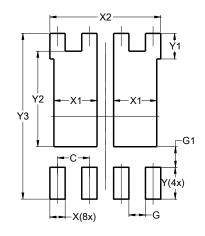


PowerDI5060-8/SWP					
(Type UXD)					
Dim	Min	Max	Тур		
Α	0.90	1.10	1.00		
A 1	0.00	0.05			
b	0.30	0.50	0.41		
b2	0.20	0.35	0.25		
b4	().25REF			
С	0.230	0.330	0.277		
D		5.15 BSC			
D1	4.70	5.10	4.90		
D2	1.46	1.66	1.55		
D3	3.78	4.18	3.98		
Е	6	.40 BS0			
E1	5.60	6.00	5.80		
E2	3.46	3.86	3.66		
E2a	4.195	4.595	4.395		
е	1.27BSC				
k	1.05				
L	0.635	0.835	0.735		
La	0.635	0.835	0.735		
L1	0.200	0.400	0.300		
М	3.205	4.005	3.605		
W	0.025	0.225	0.125		
θ	10°	12°	11°		
θ1	6°	8°	7°		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

PowerDI5060-8/SWP (Type UXD)



Dimensions	Value		
Dimensions	(in mm)		
С	1.270		
G	0.660		
G1	0.820		
Х	0.610		
X1	1.720		
X2	4.420		
Υ	1.270		
Y1	1.020		
Y2	3.810		
Y3	6.610		



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