

Features

- Advanced Trench Cell Design
- Excellent Package for Heat Dissipation
- High Density Cell Design for Low $R_{DS(on)}$
- Epoxy Meets UL 94 V-0 Flammability Rating
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

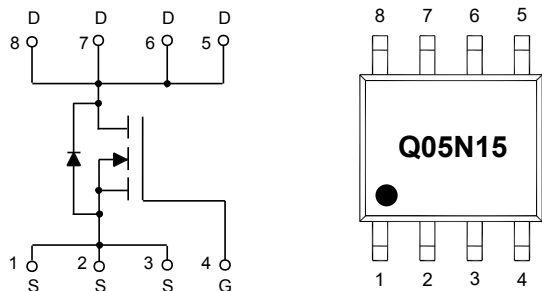
Maximum Ratings

- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 62.5°C/W Junction to Ambient (Note 2)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	150	V
Gate-Source Voltage	V_{GS}	±25	V
Continuous Drain Current	I_D	5	A
Pulsed Drain Current	I_{DM}	15.6	A
Total Power Dissipation	P_D	2	W

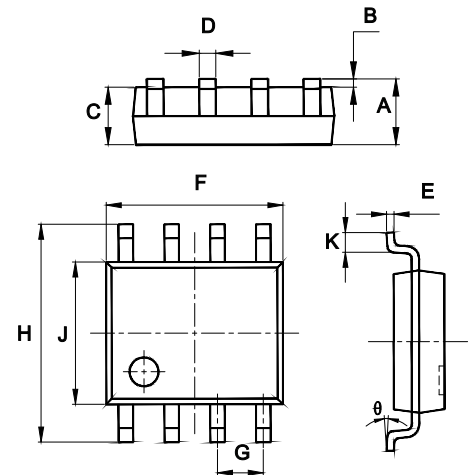
Note: 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. Surface Mounted on 1 in² Pad Area, t ≤ 10 sec.

Internal Structure and Marking Code



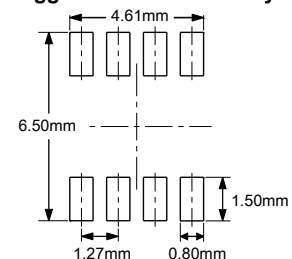
N-CHANNEL MOSFET

SOP-8



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.053	0.069	1.35	1.75	
B	0.004	0.010	0.10	0.25	
C	0.053	0.061	1.35	1.55	
D	0.013	0.020	0.33	0.51	
E	0.007	0.010	0.17	0.25	
F	0.185	0.200	4.70	5.10	
G	0.050		1.270		TYP.
H	0.228	0.244	5.80	6.20	
J	0.150	0.157	3.80	4.00	
K	0.016	0.050	0.40	1.27	
θ	0°	8°	0°	8°	

Suggested Solder Pad Layout



Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	150			V
Gate-Source Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 25V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=120V, V_{GS}=0V$			1	μA
		$V_{DS}=120V, V_{GS}=0V, T_J=85^\circ C$			30	
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2		4	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=3A$		51	63	m Ω
		$V_{GS}=6V, I_D=2A$		53	67	
Diode Characteristics						
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=3A$			1.3	V
Reverse Recovery Time	t_{rr}	$I_{DS}=3A, V_{GS}=0V$ $di_{SD}/dt=100A/\mu s$		63		ns
Reverse Recovery Charge	Q_{rr}			138		nC
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=75V, V_{GS}=0V, f=1MHz$		1179		pF
Output Capacitance	C_{oss}			66		
Reverse Transfer Capacitance	C_{rss}			36		
Total Gate Charge	Q_g	$V_{DS}=75V, V_{GS}=10V, I_D=3A$		22		nC
Gate-Source Charge	Q_{gs}			6.2		
Gate-Drain Charge	Q_{gd}			5.3		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS}=75V, V_{GEN}=10V,$ $R_G=4.5\Omega, R_L=25\Omega,$ $I_{DS}=3A$		7.4		ns
Turn-On Rise Time	t_r			19		
Turn-Off Delay Time	$t_{d(off)}$			15		
Turn-Off Fall Time	t_f			18		

Curve Characteristics

Fig. 1 - Typical Output Characteristics

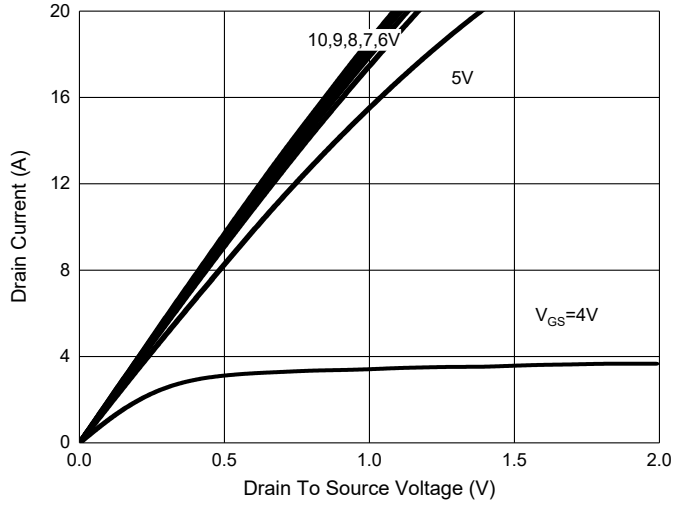


Fig. 2 - $I_S - V_{SD}$

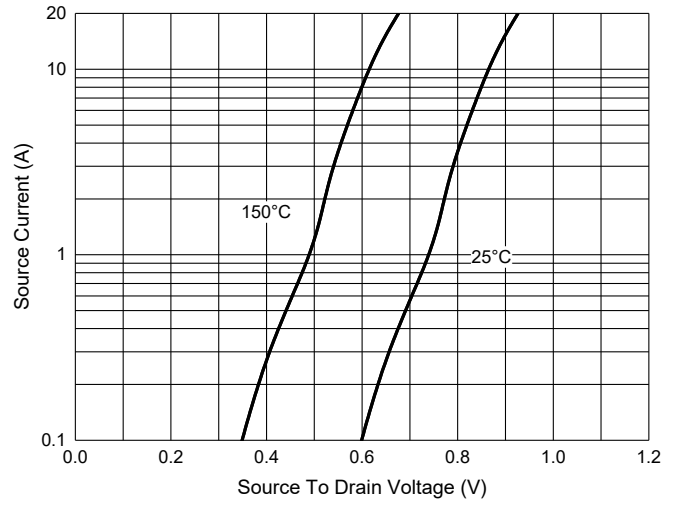


Fig. 3 - $R_{DS(ON)} - I_D$

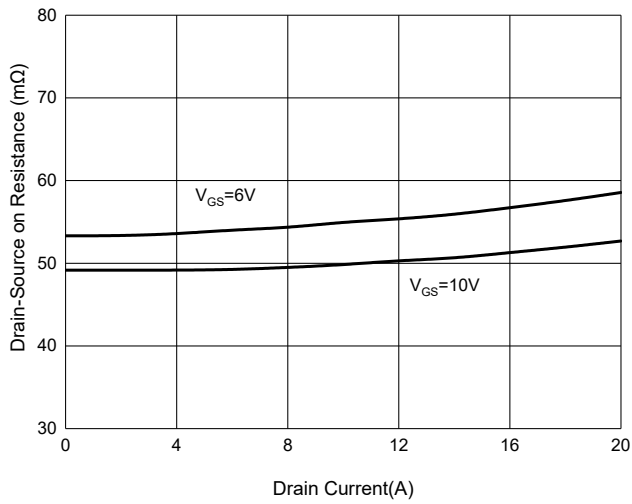


Fig. 4 - Normalized On Resistance Characteristics

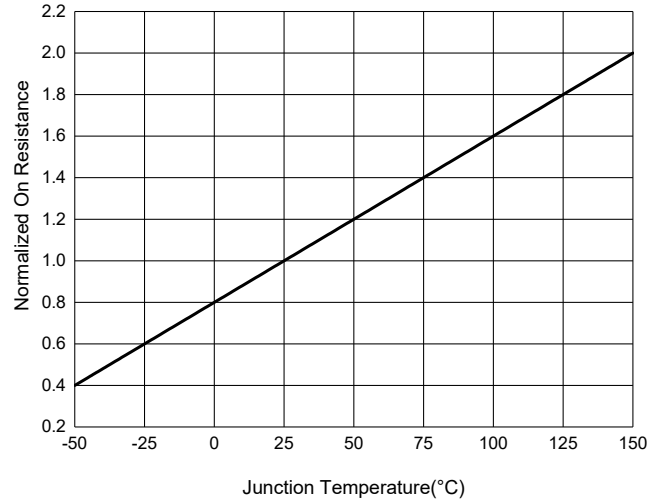


Fig. 5 - Capacitance Characteristics

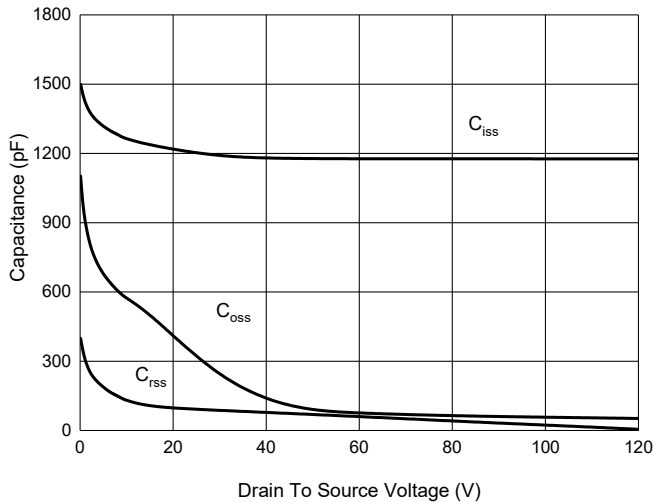
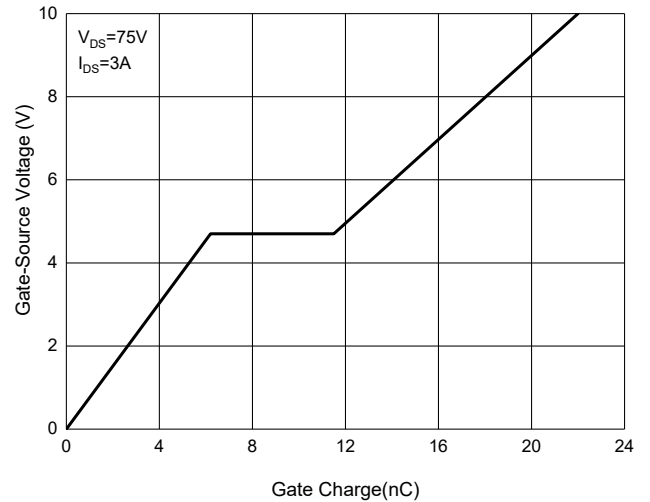
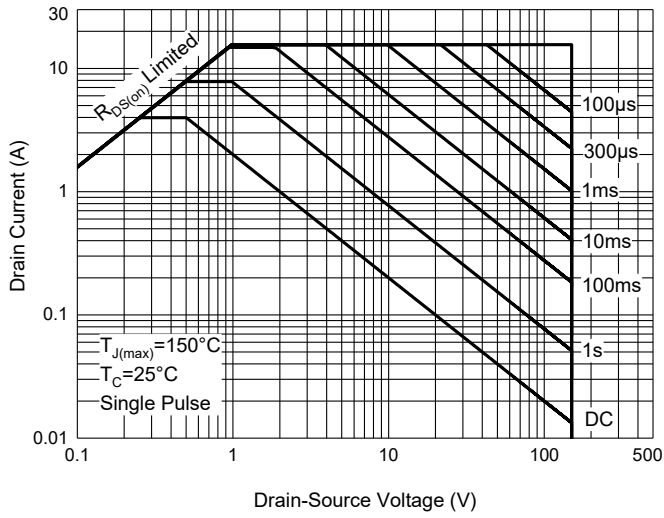


Fig. 6 - Gate Charge



Curve Characteristics

Fig. 7 - Safe Operation Area



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 4Kpcs/Reel

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