TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74LCX14F,TC74LCX14FN,TC74LCX14FT,TC74LCX14FK

Low-Voltage Hex Schmitt Inverter with 5-V Tolerant Inputs and Outputs

The TC74LCX14F/FN/FT/FK is a high-performance CMOS schmitt inverter. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low power dissipation.

The device is designed for low-voltage (3.3 V) V<sub>CC</sub> applications, but it could be used to interface to 5-V supply environment for inputs.

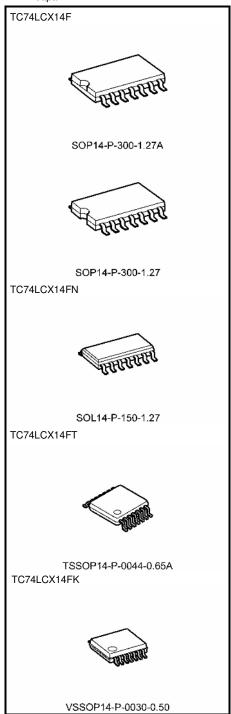
Pin configuration and function are the same as the TC74LCX04 but the inputs have hysteresis and with Schmitt trigger function, the TC74LCX14F/FN/FT can be used as a line receivers which will receive slow input signals.

All inputs are equipped with protection circuits against static discharge.

#### **Features**

- Low-voltage operation: VCC = 2.0 to 3.6 V
- High-speed operation:  $t_{pd} = 6.5 \text{ ns (max) (V}_{CC} = 3.0 \text{ to } 3.6 \text{ V})$
- Ouput current: |IOH|/IOL = 24 mA (min) (VCC = 3.0 V)
- Latch-up performance: ±500 mA
- Available in JEDEC SOP, JEITA SOP and TSSOP
- Power-down protection provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 14 type

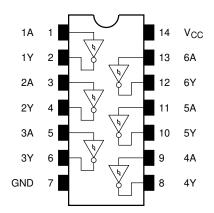
Note: xxxFN (JEDEC SOP) is not available in Japan.



Weight

SOP14-P-300-1.27A : 0.18 g (typ.) SOP14-P-300-1.27 : 0.18 g (typ.) SOL14-P-150-1.27 : 0.12 g (typ.) TSSOP14-P-0044-0.65A : 0.06 g (typ.) VSSOP14-P-0030-0.50 : 0.02 g (typ.)

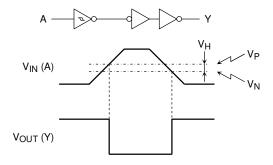
# Pin Assignment (top view)



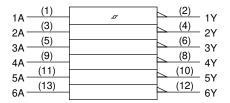
## **Truth Table**

Inputs	Outputs
Α	Y
L	Н
Н	L

# System Diagram and waveform



# **IEC Logic Symbol**



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#### **Absolute Maximum Ratings (Note 1)**

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V <sub>CC</sub>	−0.5 to 7.0	V	
DC input voltage	V <sub>IN</sub>	−0.5 to 7.0	V	
		-0.5 to 7.0 (Note 2)		
DC output voltage	V <sub>OUT</sub>	-0.5 to V <sub>CC</sub> + 0.5 (Note 3)	V	
Input diode current	I <sub>IK</sub>	-50	mA	
Output diode current	lok	±50 (Note 4)	mA	
DC output current	lout	±50	mA	
Power dissipation	P <sub>D</sub>	180	mW	
DC V <sub>CC</sub> /ground current	I <sub>CC</sub> /I <sub>GND</sub>	±100	mA	
Storage temperature	T <sub>stg</sub>	-65 to 150	°C	

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Note 2:  $V_{CC} = 0 V$ 

Note 3: High or low state.  $I_{\mbox{OUT}}$  absolute maximum rating must be observed.

Note 4:  $V_{OUT} < GND, V_{OUT} > V_{CC}$ 

### **Recommended Operating Conditions (Note 1)**

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V <sub>CC</sub>	2.0 to 3.6		
Tower supply voltage	VCC	1.5 to 3.6 (Note 2)	V	
Input voltage	V <sub>IN</sub>	0 to 5.5	V	
Output voltage	V <sub>OUT</sub>	0 to 5.5 (Note 3)	V	
		0 to V <sub>CC</sub> (Note 4)	V	
Output current	1/1	±24 (Note 5)	mA	
	IOH/IOL	±12 (Note 6)	IIIA	
Operating temperature	T <sub>opr</sub>	-40 to 85	°C	

Note 1: The recommended operating conditions are required to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.

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Note 2: Data retention only

Note 3:  $V_{CC} = 0 V$ 

Note 4: High or low state

Note 5:  $V_{CC} = 3.0 \text{ to } 3.6 \text{ V}$ 

Note 6:  $V_{CC} = 2.7 \text{ to } 3.0 \text{ V}$ 



#### **Electrical Characteristics**

#### DC Characteristics (Ta = -40 to 85°C)

Characteri	stics	Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Max	Unit
H-level		V <sub>P</sub>	_		3.0	1.2	2.2	V
Threshold voltage	L-level	V <sub>N</sub>		_		0.6	1.5	V
Hysteresis voltage		V <sub>H</sub>	_		3.0	0.4	1.2	V
			$V_{IN} = V_{IL}$	I <sub>OH</sub> = -100 μA	2.7 to 3.6	V <sub>CC</sub> - 0.2	_	V
	H-level	V <sub>OH</sub>		I <sub>OH</sub> = -12 mA	2.7	2.2	_	
Output voltage				I <sub>OH</sub> = -18 mA	3.0	2.4	_	
				$I_{OH} = -24 \text{ mA}$	3.0	2.2	_	
	L-level V <sub>OL</sub>		$I_{OL} = 100 \mu A$	2.7 to 3.6	_	0.2		
		V	$V_{IN} = V_{IH}$	I <sub>OL</sub> = 12 mA	2.7	_	0.4	
		VOL		I <sub>OL</sub> = 16 mA	3.0	_	0.4	
			I <sub>OL</sub> = 24 mA	3.0	_	0.55		
Input leakage current		I <sub>IN</sub>	V <sub>IN</sub> = 0 to 5.5 V		2.7 to 3.6	_	±5.0	μΑ
Power-off leakage current IO		l <sub>OFF</sub>	V <sub>IN</sub> /V <sub>OUT</sub> = 5.5 V		0	_	10.0	μА
Quiescent supply current		laa	V <sub>IN</sub> = V <sub>CC</sub> or GND		2.7 to 3.6	_	10.0	
		lcc	V <sub>IN</sub> = 3.6 to 5.5 V		2.7 to 3.6	_	±10.0	μΑ
Increase in Icc per input		Δlcc	$V_{IH} = V_{CC} - 0.6 V$		2.7 to 3.6	_	500	

### AC Characteristics ( $Ta = -40 \text{ to } 85^{\circ}\text{C}$ )

	Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Max	Unit
Γ	Propagation delay time	t <sub>pLH</sub>	Figure 1, Figure 2	2.7		7.5	- ns
ľ		$t_{pHL}$		$3.3 \pm 0.3$	1.5	6.5	
Ľ	Output to output skew	t <sub>osLH</sub>	(Night)	2.7	_	_	20
ľ		t <sub>osHL</sub>	(Note)	$3.3 \pm 0.3$		1.0	ns

Note: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{PLHm} - t_{PLHn}|, t_{OSHL} = |t_{PHLm} - t_{PHLn}|)$ 

## Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.5$ ns, $C_L = 50$ pF, $R_L = 500~\Omega$ )

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Quiet output maximum dynamic V <sub>OL</sub>	V <sub>OLP</sub>	$V_{IH} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	3.3	0.8	٧
Quiet output minimum dynamic $V_{\mbox{OL}}$	V <sub>OLV</sub>	$V_{IH}=3.3\;V,\;V_{IL}=0\;V$	3.3	0.8	٧

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#### **Capacitive Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Input capacitance	C <sub>IN</sub>	_	3.3	7	рF
Output capacitance	C <sub>OUT</sub>	_	0	8	pF
Power dissipation capacitance	C <sub>PD</sub>	$f_{IN} = 10 \text{ MHz}$ (No	te) 3.3	25	pF

Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/6 \text{ (per gate)}$ 

#### **AC Test Circuit**

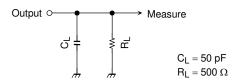


Figure 1

#### **AC Waveform**

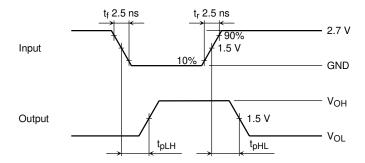
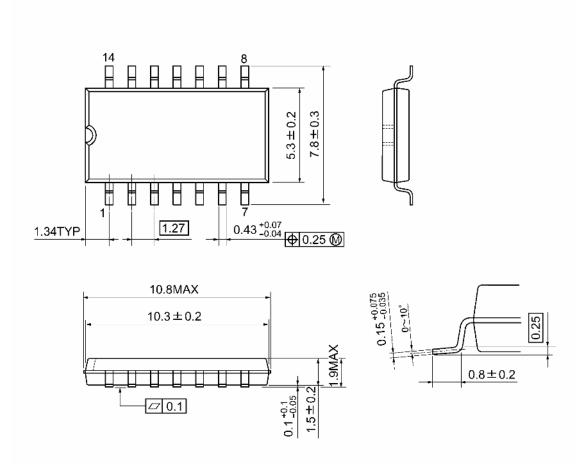


Figure 2 tpLH, tpHL

# TOSHIBA Package Dimensions

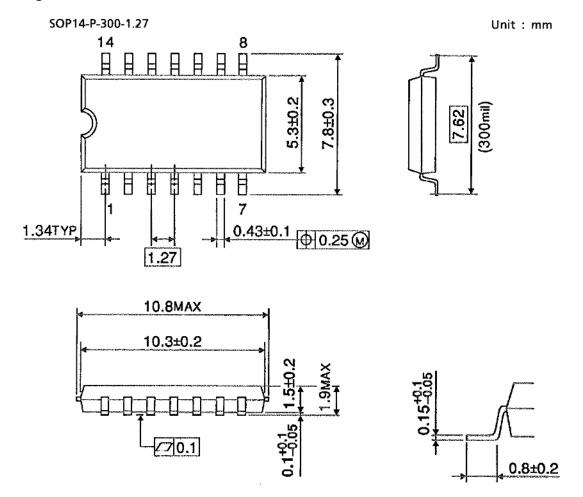
SOP14-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)



## **Package Dimensions**

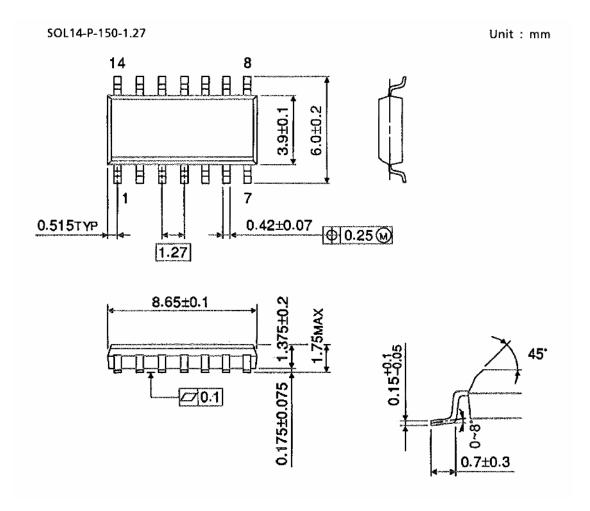


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Weight: 0.18 g (typ.)



# **Package Dimensions (Note)**

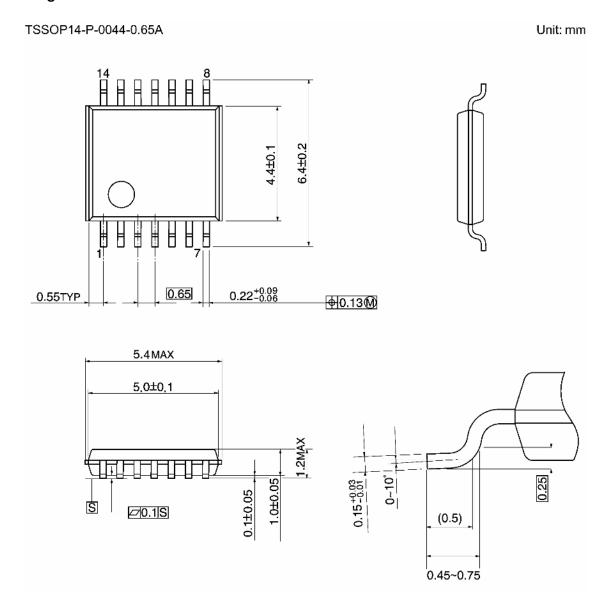


Note: This package is not available in japan.

Weight: 0.12 g (typ.)



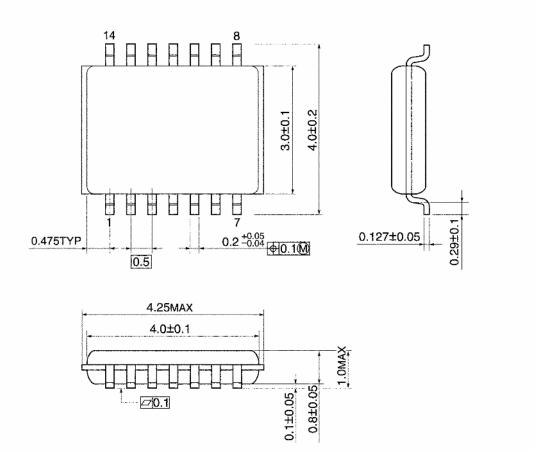
# **Package Dimensions**



Weight: 0.06 g (typ.)

# **Package Dimensions**

VSSOP14-P-0030-0.50 Unit: mm



Weight: 0.02 g (typ.)

Note: Lead (Pb)-Free Packages

SOP14-P-300-1.27A SOL14-P-150-1.27 TSSOP14-P-0044-0.65A VSSOP14-P-0030-0.50

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