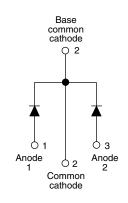




Vishay Semiconductors

# Schottky Rectifier, 2 x 30 A

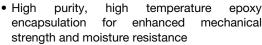




PRODUCT SUMMARY					
Package	TO-247AC				
I <sub>F(AV)</sub>	2 x 30 A				
V <sub>R</sub>	100 V				
V <sub>F</sub> at I <sub>F</sub>	0.64 V				
I <sub>RM</sub> max.	25 mA at 125 °C				
T <sub>J</sub> max.	175 °C				
Diode variation	Common cathode				
E <sub>AS</sub>	15 mJ				

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

#### **DESCRIPTION**

The VS-63CPQ100G... center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I <sub>F(AV)</sub>	Rectangular waveform	60	A			
$V_{RRM}$		100	V			
I <sub>FSM</sub>	$t_p = 5 \mu s sine$	2200	А			
V <sub>F</sub>	30 Apk, T <sub>J</sub> = 125 °C (per leg)	0.64	V			
T <sub>J</sub>	Range	- 55 to 175	°C			

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-63CPQ100PbF	VS-63CPQ100-N3	UNITS		
Maximum DC reverse voltage	V <sub>R</sub> 100 100		V			
Maximum working peak reverse voltage	$V_{RWM}$	100	100	V		

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average forward current	per leg		50 % duty cycle at T <sub>C</sub> = 153 °C, rectangular waveform		50 % distributed 4.50 %0 master and	30	
See fig. 5	per device	I <sub>F(AV)</sub>			60	Α	
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		I	5 μs sine or 3 μs rect. pulse Following any relation and condition a		2200	A	
		I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	rated V <sub>RRM</sub> applied	410		
Non-repetitive avalanche energy per leg		E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1 A, L = 30 mH		15	mJ	
Repetitive avalanche current per leg		I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by $T_J$ maximum $V_A = 1.5$ x $V_R$ typical		1	Α	



# VS-63CPQ100GPbF, VS-63CPQ100G-N3

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
		30 A	T <sub>.1</sub> = 25 °C	0.77		
Maximum forward voltage drop per leg	V (1)	60 A	1j=25 C	0.92	V	
See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	30 A	T 105.00	0.64		
		60 A	T <sub>J</sub> = 125 °C	0.76		
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V Detect V	0.3	mA	
See fig. 2	IRM (")	T <sub>J</sub> = 125 °C	V <sub>R</sub> = Rated V <sub>R</sub>	25		
Threshold voltage	V <sub>F(TO)</sub>	T T manyimum		0.38	V	
Forward slope resistance	r <sub>t</sub>	$T_J = T_J$ maximum		5.75	mΩ	
Maximum junction capacitance per leg	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range 100 kHz to 1 MHz) 25 °C		1300	pF	
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body		7.5	nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10		10 000	V/µs	

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 175	°C			
Maximum thermal resistance, junction to case per leg	D	DC operation See fig. 4	0.8				
Maximum thermal resistance, junction to case per package	- R <sub>thJC</sub>	DC operation	0.4	°C/W			
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.25				
Approximate weight			6	g			
Approximate weight			0.21	oz.			
Mounting torque			6 (5)	kgf · cm			
Mounting torque maximum			12 (10)	(lbf $\cdot$ in)			
Marking device		Case style TO-247AC (JEDEC)	63CPC	Q100G			

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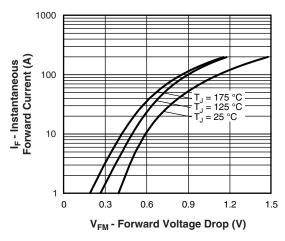


Fig. 1 - Maximum Forward Voltage Drop Characteristics

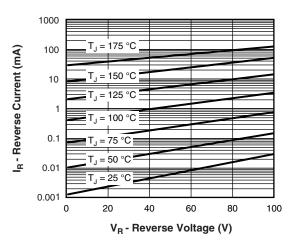


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

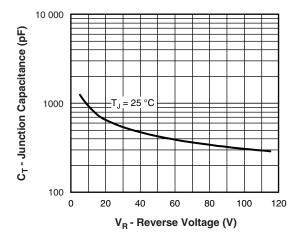


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

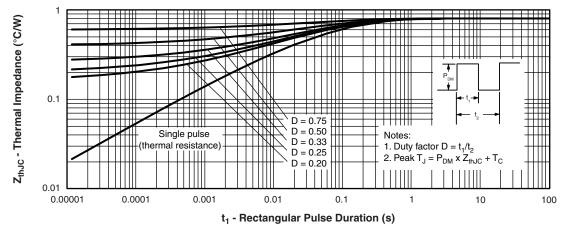


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

#### www.vishay.com

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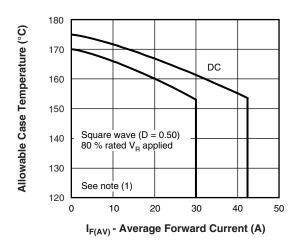


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

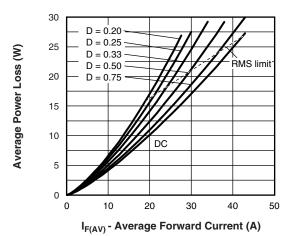


Fig. 6 - Forward Power Loss Characteristics

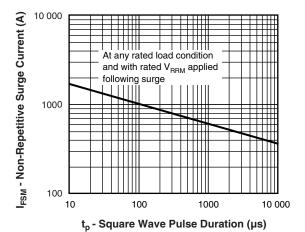


Fig. 7 - Maximum Non-Repetitive Surge Current

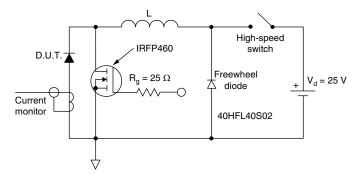


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

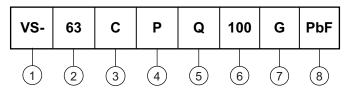
<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$ ;  $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = 80 \%$  rated  $V_R$ 

# VS-63CPQ100GPbF, VS-63CPQ100G-N3

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#### **ORDERING INFORMATION TABLE**

**Device code** 



Vishay Semiconductors product

2 - Current rating (60 A)

3 - Circuit configuration:

C = Common cathode

4 - Package:

P = TO-247

5 - Schottky "Q" series

6 - Voltage rating (100 V)

7 - G = Schottky generation

8 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

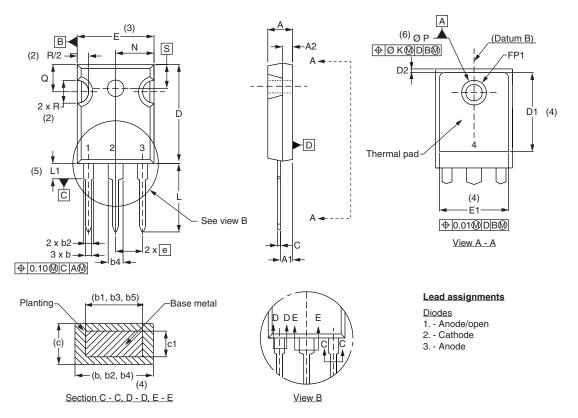
ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-63CPQ100GPbF	25	500	Antistatic plastic tube			
VS-63CPQ100G-N3	25	500	Antistatic plastic tube			

LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95223</u>				
Dort marking information	TO-247AC PbF	www.vishay.com/doc?95226		
Part marking information	TO-247AC -N3	www.vishay.com/doc?95007		



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#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIN	IETERS	INC	HES	NOTES	
STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.65	5.31	0.183	0.209		
A1	2.21	2.59	0.087	0.102		
A2	1.50	2.49	0.059	0.098		
b	0.99	1.40	0.039	0.055		
b1	0.99	1.35	0.039	0.053		
b2	1.65	2.39	0.065	0.094		
b3	1.65	2.37	0.065	0.094		
b4	2.59	3.43	0.102	0.135		
b5	2.59	3.38	0.102	0.133		
С	0.38	0.86	0.015	0.034		
c1	0.38	0.76	0.015	0.030		
D	19.71	20.70	0.776	0.815	3	
D1	13.08	-	0.515	-	4	

SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STIMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.72	-	0.540	-	
е	5.46	BSC	0.215	BSC	
FK	2.	2.54		10	
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
N	7.62	BSC	0.3		
ΦР	3.56	3.66	0.14	0.144	
ФР1	-	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	1.78	0.216	
S	5.51	BSC	0.217	BSC	

#### **Notes**

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC outline TO-247 with exception of dimension c



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