

COMPLIANT

High Performance Schottky Rectifier, 400 A



PRIMARY CHARACTERISTICS				
I _{F(AV)}	400 A			
V _R	40 V, 45 V			
Package	TO-244			
Circuit configuration	Two diodes common cathode			

FEATURES

- 175 °C T_J operation
- · Center tap module
- · Low forward voltage drop
- · High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- UL approved file E222165
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

The VS-401CNQ... center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, converters, freewheeling diodes, welding and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	400	Α	
V _{RRM}	Range	40/45	V	
I _{FSM}	t _p = 5 μs sine	25 000	Α	
V _F	200 A _{pk} , T _J = 125 °C (per leg)	0.56	V	
T _J	Range	-55 to +175	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-401CNQ040PbF	VS-401CNQ045PbF	UNITS
Maximum DC reverse voltage	V_R	40	45	V
Maximum working peak reverse voltage	V_{RWM}	40	40	V

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average forward per leg	I _{F(AV)}	50 % duty cycle at T _C = 147 °C, rectangular waveform —		50 0/ distriction of T 147 9C receip guilar manaferra	200	
current (fig. 5) per device				400	A	
Maximum peak one cycle non-repetitive		5 μs sine or 3 μs rect. pulse Following any rated loac		25 000	_ A	
surge current per leg (fig. 7)	I _{FSM}	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	3450		
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 24 A, L = 1 mH		270	mJ	
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		40	А	



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST COND	VALUES	UNITS	
	V _{FM} ⁽¹⁾	200 A	T _{.1} = 25 °C	0.67	V
Maximum forward voltage drop per leg		400 A	1J=25 C	0.78	
See fig. 1		200 A	T T manyimayan	0.56	
		400 A	$T_J = T_J$ maximum	0.69	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V_{R} = Rated V_{R}	20	mA
See fig. 2	'RM '''	T _J = 125 °C	v _R = nateu v _R	180] IIIA
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		10 300	pF
Typical series inductance per leg	L _S	From top of terminal hole to mounting plane		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}	-55	-	175	°C	
Thermal resistance, junction to case per leg	5	-	-	0.19	°C/W	
Thermal resistance, junction to case per module	- R _{thJC}	-	-	0.095		
Thermal resistance, case to heatsink	R _{thCS}	-	0.10	-		
		-	68	-	g	
Weight		-	2.4	-	OZ.	
Mounting torque		35.4 (4)		53.1 (6)	lbf · in (N · m)	
Mounting torque center hole		30 (3.4)		40 (4.6)		
Terminal torque		30 (3.4)	-	44.2 (5)		
Vertical pull		-	-	80	- lbf · in	
2" lever pull		-	-	35		

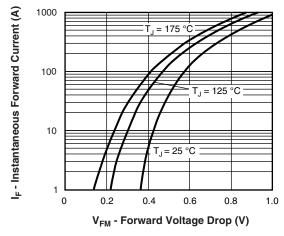


Fig. 1 - Maximum Forward Voltage Drop Characteristics

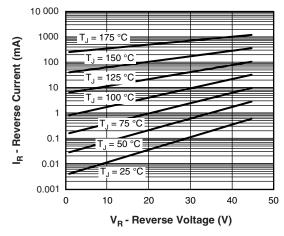


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



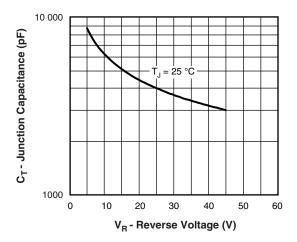


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

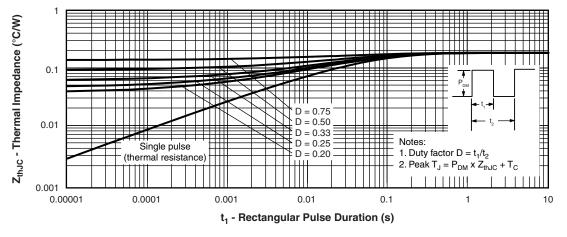


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

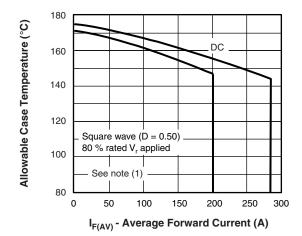


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

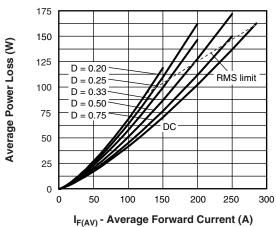
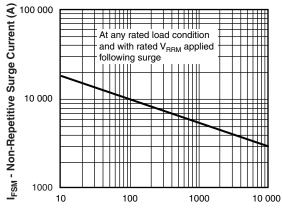


Fig. 6 - Forward Power Loss Characteristics



t_p - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current

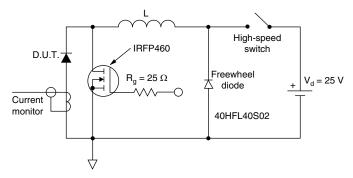
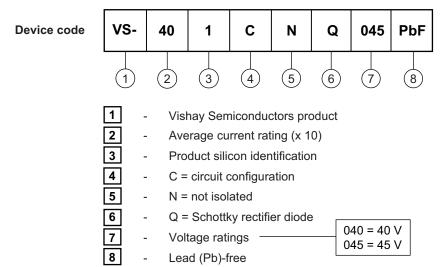


Fig. 8 - Unclamped Inductive Test Circuit

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

ORDERING INFORMATION TABLE

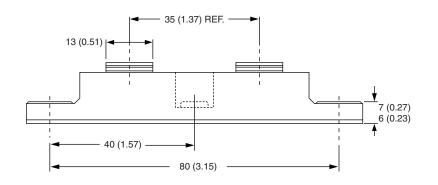


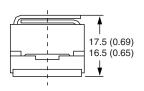
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95021			

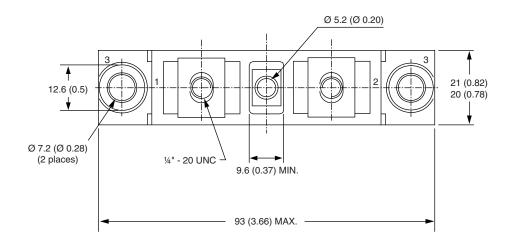


TO-244

DIMENSIONS in millimeters (inches)









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