# CTMMP73VCZF Series From .10µH to 33µH



# **CHARACTERISTICS**

**Description:** SMD (shielded) power inductor **Applications:** High density DC/DC converters. POL

converters. High current VRM/VRD for notebooks, servers, and

desktop CPUs. High speed chargers.

**Operating Temperature:** -55°C to +125°C [The part temperature (ambient + temp. rise)] should not exceed 125°C under worst case operating conditions. Component placement, PWB trace, size, thickness and other cooling provisions will affect the part temperature. Part temperature should be verified in the end application.

Inductance Tolerance: ±20%

Testing: Inducance is tested on an HP4285A at 100kHz, 0.5V

Packaging: Tape & Reel.

Marking: Parts are marked with inductance code.

Miscellaneous: RoHS Compliant.

Additional Information: Additional electrical & physical

information available upon request.

Samples available. See website for ordering information.

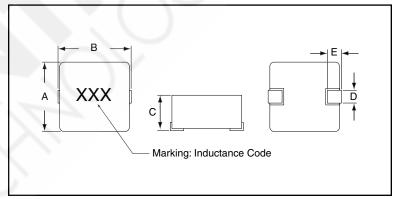
# **SPECIFICATIONS**

Parts are only available in ±20% inductance tolerance only \*Irms DC current (A) will cause an approximately ΔT of 40°C \*\*Isat DC current (A) will cause L0 to drop approximately 30%

Part Number	Inductance (µH)	L Test Freq. (KHz)	*Irms Typ. (A)	**Isat Typ. (A)	DCR Max. (mΩ)	DCR Typ. (mΩ)
CTMMP73VCZF-R10M	0.10	100	37	45	1.5	1.3
CTMMP73VCZF-R22M	0.22	100	23	40	2.8	2.5
CTMMP73VCZF-R33M	0.33	100	20	33	4.2	4.0
CTMMP73VCZF-R47M	0.47	100	16.5	27	5.5	5.0
CTMMP73VCZF-R68M	0.68	100	15	24	6.3	5.7
CTMMP73VCZF-R82M	0.82	100	13	23	8.0	7.5
CTMMP73VCZF-1R0M	1.0	100	12	22	10	9.0
CTMMP73VCZF-1R5M	1.5	100	9.5	18	15	13.5
CTMMP73VCZF-2R2M	2.2	100	8.5	14	20	17.0
CTMMP73VCZF-3R3M	3.3	100	6.0	12	35	32.0
CTMMP73VCZF-4R7M	4.7	100	5.5	9	40	35.0
CTMMP73VCZF-6R8M	6.8	100	4.5	8	60	54.0
CTMMP73VCZF-8R2M	8.2	100	4.5	6	60	54.0
CTMMP73VCZF-100M	10	100	4.0	5.5	68	62.0
CTMMP73VCZF-150M	15	100	3.0	5.0	122	111
CTMMP73VCZF-220M	22	100	3.0	3.2	145	130
CTMMP73VCZF-330M	33	100	2.0	3.0	270	245

# **PHYSICAL DIMENSIONS**

Size A	B Max.	C Max.	<b>D</b> Typ.	E
<b>mm</b> 6.6±0.2	7.3	3.0	2.9	1.6±0.5
inches 0.26±0.008	0.29	0.12	0.11	0.063±0.02



## **PAD LAYOUT**

