### 60V N-Channel Enhancement Mode MOSFET



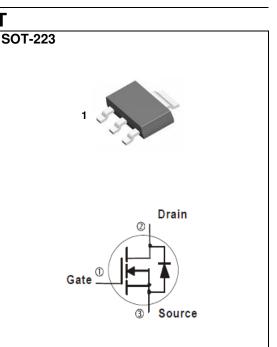
60 V Current

### Features

- $R_{DS(ON)}$ ,  $V_{GS}$ @10V, $I_D$ @3.0A<100m $\Omega$
- $R_{DS(ON)}$ ,  $V_{GS}$ @4.5V, $I_D$ @2.0A<110m $\Omega$
- Advanced Trench Process Technology
- High density cell design for ultra low on-resistance
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

#### **Mechanical Data**

- Case : SOT-223 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.043 ounces, 0.123 grams
- Marking: W4N06A



## **Maximum Ratings and Thermal Characteristics** (T<sub>A</sub>=25°C unless otherwise noted)

4.0 A

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V <sub>DS</sub>	60	V	
Gate-Source Voltage		V <sub>GS</sub>	<u>+</u> 20	V	
Continuous Drain Current	T <sub>A</sub> =25°C	I <sub>D</sub>	4	A	
	T <sub>A</sub> =70°C		3.2		
Pulsed Drain Current (Note 1)		I <sub>DM</sub>	8	А	
Power Dissipation	T <sub>A</sub> =25°C		3.1		
	T <sub>A</sub> =70°C	P <sub>D</sub>	2	W	
Operating Junction and Storage Temperature Range		$T_J, T_{STG}$	-55~150	°C	
Typical Thermal resistance - Junction to Ambient <sup>(Note 5)</sup>		R <sub>eja</sub>	40.3	°C/W	

• Limited only By Maximum Junction Temperature

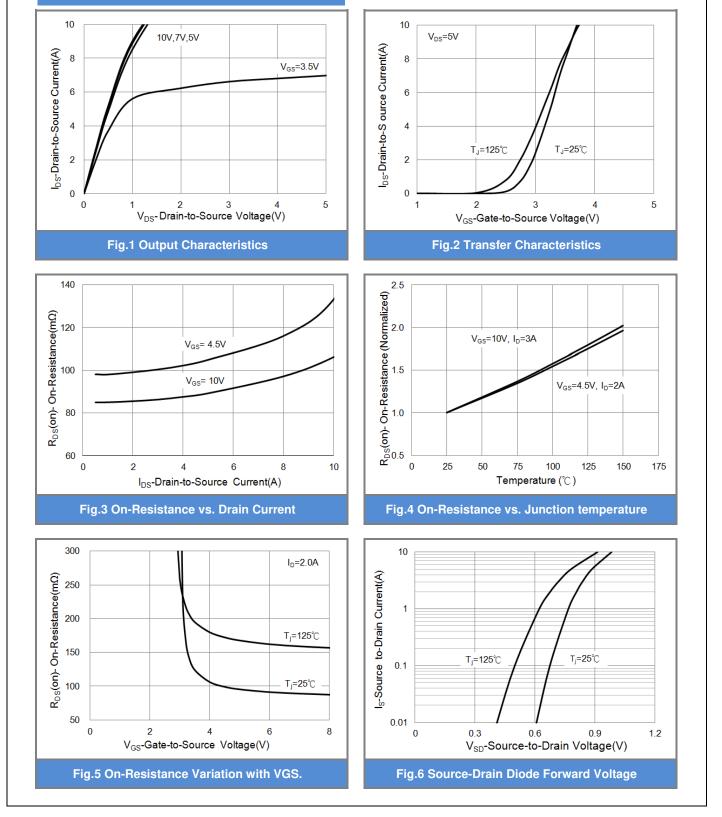


### Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	$V_{GS}=0V,I_{D}=250uA$	60	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250$ uA	1.0	1.86	2.5	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V,I <sub>D</sub> =3.0A	-	85	100	mΩ
		$V_{GS}$ =4.5V,I <sub>D</sub> =2.0A	-	95	110	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ =48V, $V_{GS}$ =0V	-	-	1.0	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = <u>+</u> 20V,V <sub>DS</sub> =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 6)						
Total Gate Charge	Qg		-	5.1	-	nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =48V, I <sub>D</sub> =3A, V <sub>GS</sub> =4.5V <sup>(Note 2,3)</sup>	-	1.2	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	1.9	-	
Input Capacitance	Ciss	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1.0MHZ	-	509	-	pF
Output Capacitance	Coss		-	39	-	
Reverse Transfer Capacitance	Crss		-	26	-	
Turn-On Delay Time	td <sub>(on)</sub>	$V_{DD}$ =30V, $I_{D}$ =3A, $V_{GS}$ =10V,	-	1.6	-	ns
Turn-On Rise Time	t <sub>r</sub>		-	7.3	-	
Turn-Off Delay Time	td <sub>(off)</sub>		-	25	-	
Turn-Off Fall Time	t <sub>f</sub>	$R_G=3.3\Omega^{(Note 2,3)}$	-	14	-	
Drain-Source Diode			·			
Maximum Continuous Drain-Source			-	-	4	А
Diode Forward Current	I <sub>S</sub>					
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =1A,V <sub>GS</sub> =0V	-	0.8	1.2	V

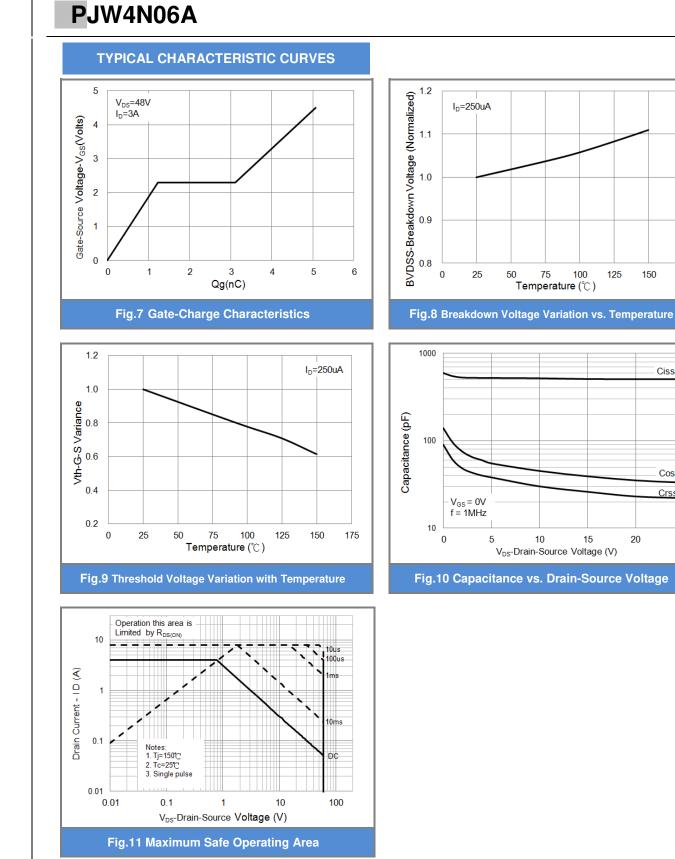
NOTES :

- 1. Pulse width <300us, Duty cycle <2%
- 2. Essentially independent of operating temperature typical characteristics
- 3. Repetitive rating, pulse width limited by junction temperature TJ(MAX)=150°C. Ratings are based on low frequency and duty cycles to keep initial TJ =25°C.
- 4. The maximum current rating is package limited
- 5. R<sub>®JA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
- 6. Guaranteed by design, not subject to production testing



**TYPICAL CHARACTERISTIC CURVES** 

# 4







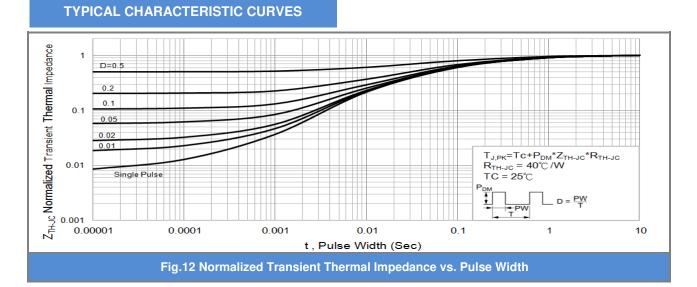
175

Ciss

Coss

Crss

25



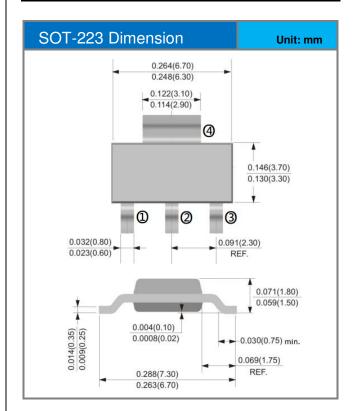






# 4

#### **Packaging Information**



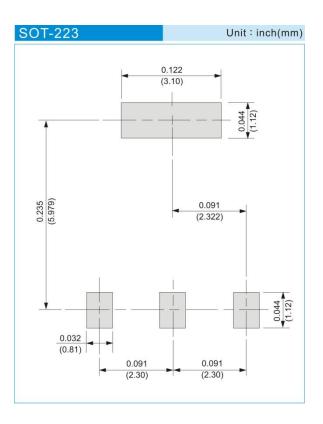




### PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version	
PJW4N06A_R2_00001	SOT-223	2,500pcs / 13" reel	W4N06A	Halogen free	

### MOUNTING PAD LAYOUT





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