# PJA3405-AU

### 30V P-Channel Enhancement Mode MOSFET

Current

-3.6A

### Features

Voltage

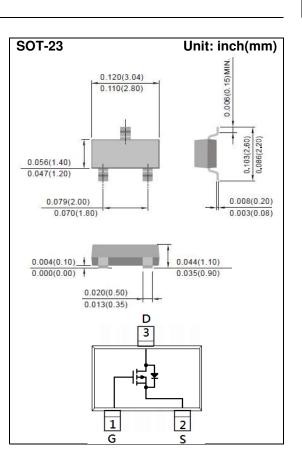
•  $R_{DS(ON)}$ ,  $V_{GS}$ @-10V,  $I_D$ @-3.6A<73m $\Omega$ 

-30 V

- $R_{DS(ON)}$ ,  $V_{GS}$ @-4.5V,  $I_D$ @-2.4A<97m $\Omega$
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### **Mechanical Data**

- Case: SOT-23 Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0003 ounces, 0.009 grams



## Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V <sub>DS</sub>	-30	v	
Gate-Source Voltage		V <sub>GS</sub>	<u>+</u> 20		
Continuous Drain Current		I <sub>D</sub>	-3.6	Α	
Pulsed Drain Current		I <sub>DM</sub>	-14.4		
Power Dissipation	T <sub>a</sub> =25°C		1.25	W	
	Derate above 25°C	P <sub>D</sub>	10	mW/°C	
Operating Junction and Storage Temperature Range		T <sub>J</sub> ,T <sub>STG</sub>	-55~150	°C	
Typical Thermal Resistance - Junction to Ambient <sup>(Note 3)</sup>		R <sub>eja</sub>	100	°C/W	

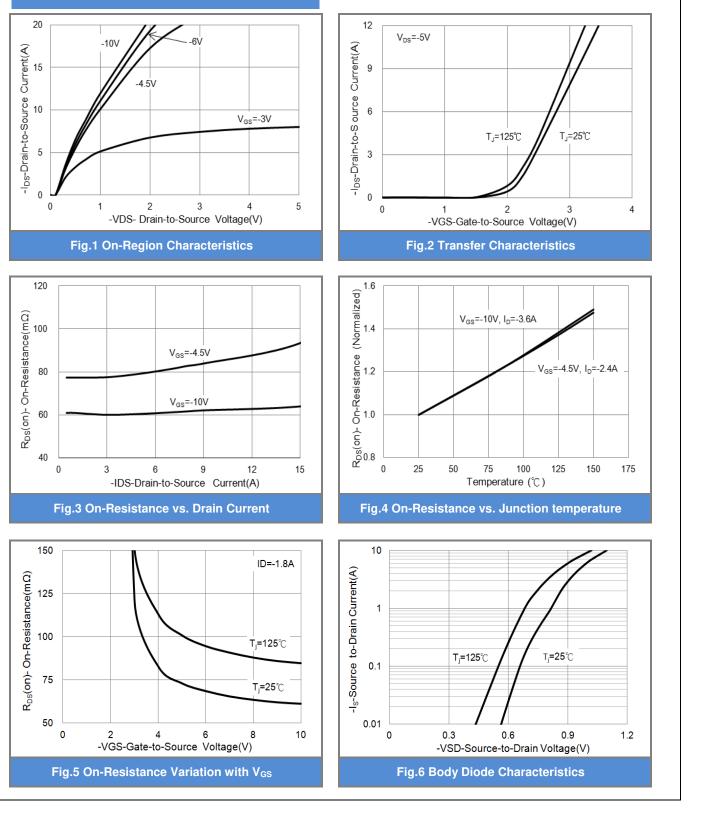


## **Electrical Characteristics** ( $T_A=25^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static		-		_		
Drain-Source Breakdown Voltage	$BV_{DSS}$ $V_{GS}=0V$ , $I_D=-250uA$	$V_{GS}$ =0V, I <sub>D</sub> =-250uA	-30	-	-	v
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_{D}=-250uA$	-1.0	-1.37	-2.1	v
Drain-Source On-State Resistance	$R_{\text{DS(on)}}$	$V_{GS}$ =-10V, I <sub>D</sub> =-3.6A	-	59	73	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2.4A	-	76	97	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V	-	-	-1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = <u>+</u> 20V, V <sub>DS</sub> =0V	-	-	<u>+</u> 100	nA
Dynamic (Note 5)						
Total Gate Charge	Qg	V <sub>DS</sub> =-15V, I <sub>D</sub> =-3.6A, V <sub>GS</sub> =-10V <sup>(Note 1,2)</sup>	-	10	-	nC
Gate-Source Charge	$Q_{gs}$		-	1.1	-	
Gate-Drain Charge	$Q_gd$		-	1.7	-	
Input Capacitance	Ciss	$V_{DS}$ =-15V, $V_{GS}$ =0V,	-	417	-	pF
Output Capacitance	Coss		-	50	-	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	36	-	
Turn-On Delay Time	td <sub>(on)</sub>	$V_{DD}$ =-15V, I <sub>D</sub> =-3.6A, $V_{GS}$ =-10V, $R_{G}$ =6 $\Omega$ <sup>(Note 1,2)</sup>	-	3.2	-	ns
Turn-On Rise Time	tr		-	33	-	
Turn-Off Delay Time	$td_{(off)}$		-	119	-	
Turn-Off Fall Time	tf	H <sub>G</sub> =017	-	68	-	
Drain-Source Diode						
Maximum Continuous Drain-Source	I <sub>S</sub>		-	-	-1.5	А
Diode Forward Current	-0					
Diode Forward Voltage	$V_{SD}$	$I_{S}$ =-1.0A, $V_{GS}$ =0V	-	-0.77	-1.2	V

NOTES :

- 1. Pulse width <300us, Duty cycle <2%.
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R<sub>OJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.

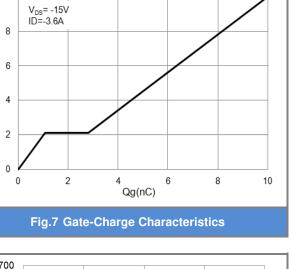


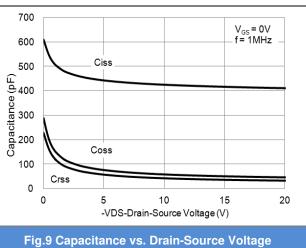
## TYPICAL CHARACTERISTIC CURVES

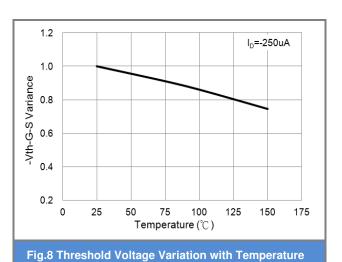
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10

-Gate-Source Voltage-V<sub>GS</sub>(Volts)



TYPICAL CHARACTERISTIC CURVES

**PJA3405-AU** 

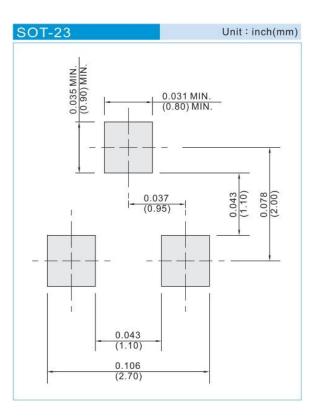


# **PJA3405-AU**

### Part No Packing Code Version

Part No Packing Code	Package Type	Packing Type	Marking	Version
PJA3405-AU_R1_000A1	SOT-23	3K pcs / 7" reel	A05	Halogen free

### **Mounting Pad Layout**





# **PJA3405-AU**

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