

DDTA (R2-ONLY SERIES) KA

PNP PRE-BIASED SMALL SIGNAL SURFACE MOUNT TRANSISTOR

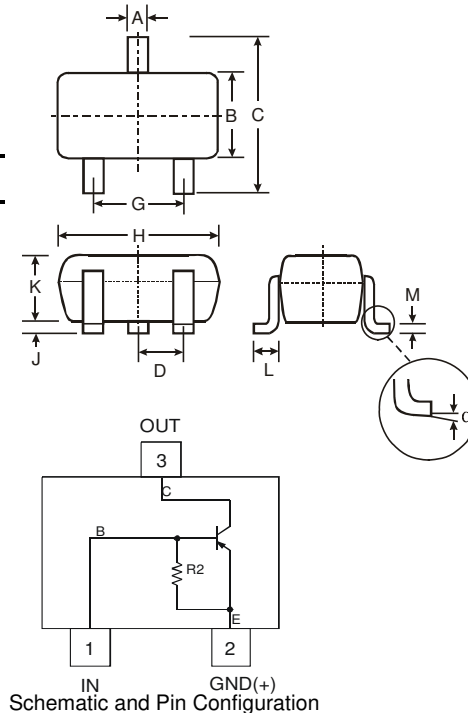
Features

- Epitaxial Planar Die Construction
- Complementary NPN Types Available (DDTC)
- Built-In Biasing Resistor, R2 only
- **Lead Free/RoHS Compliant (Note 2)**
- "Green" Device Note 3 & 4

Mechanical Data

- Case: SC-59
- Case Material: Molded Plastic, "Green" Molding Compound, Note 4. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminals: Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Copper leadframe).
- Terminal Connections: See Diagram
- Marking Information: See Table Below & Page 3
- Ordering Information: See Page 3
- Weight: 0.008 grams (approximate)

P/N	R2 (NOM)	Type Code
DDTA114GKA	10K Ω	P26
DDTA124GKA	22K Ω	P27
DDTA144GKA	47K Ω	P28
DDTA115GKA	100K Ω	P29



SC-59		
Dim	Min	Max
A	0.35	0.50
B	1.50	1.70
C	2.70	3.00
D	0.95	
G	1.90	
H	2.90	3.10
J	0.013	0.10
K	1.00	1.30
L	0.35	0.55
M	0.10	0.20
α	0°	8°
All Dimensions in mm		

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CB0}	-50	V
Collector-Emitter Voltage	V _{CEO}	-50	V
Emitter-Base Voltage	V _{EBO}	-5	V
Collector Current	I _C (Max)	-100	mA
Power Dissipation	P _d	200	mW
Thermal Resistance, Junction to Ambient Air (Note 1)	R _{θJA}	625	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

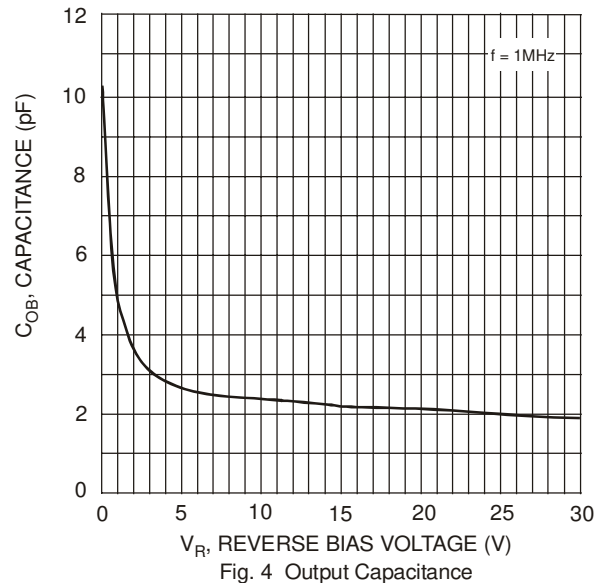
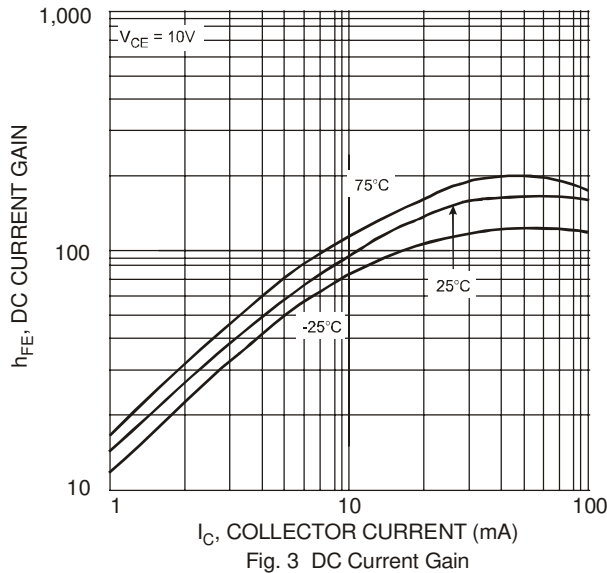
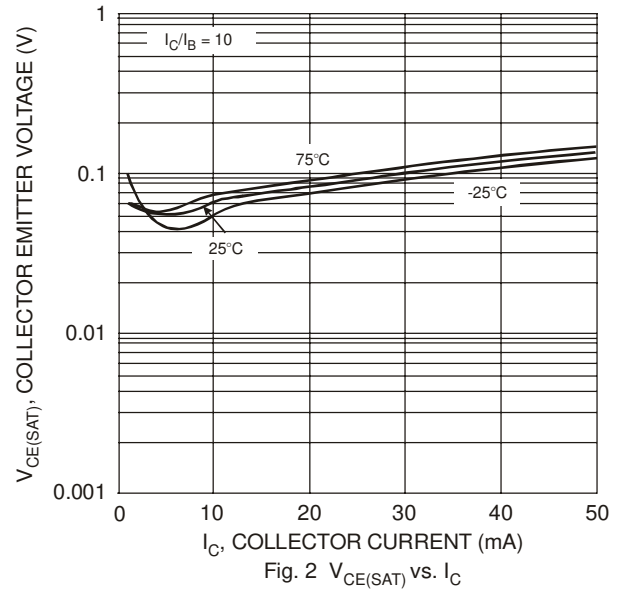
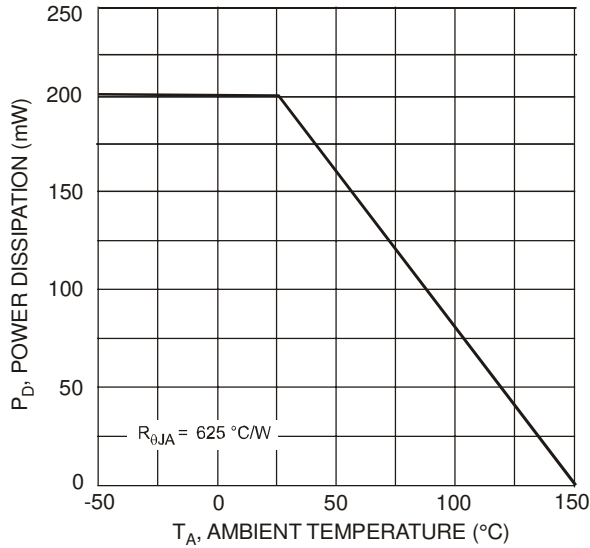
- Notes:
1. Mounted on FR4 PC Board with recommended pad layout at <http://www.diodes.com/datasheets/ap02001.pdf>.
 2. No purposefully added lead.
 3. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
 4. Product manufactured with Date Code 0627 (week 27, 2006) and newer are built with Green Molding Compound. Product manufactured prior to Date Code 0627 are built with Non-Green Molding Compound and may contain Halogens or Sb2O3 Fire Retardants.

Electrical Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic		Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage		BV_{CBO}	-50	—	—	V	$I_C = -50\mu\text{A}$
Collector-Emitter Breakdown Voltage		BV_{CEO}	-50	—	—	V	$I_C = -1\text{mA}$
Emitter-Base Breakdown Voltage		BV_{EBO}	5	—	—	V	$I_E = -720\mu\text{A}$, DDTA114GKA $I_E = -330\mu\text{A}$, DDTA124GKA $I_E = -160\mu\text{A}$, DDTA144GKA $I_E = -72\mu\text{A}$, DDTA115GKA
Collector Cutoff Current		I_{CBO}	—	—	-0.5	μA	$V_{CB} = -50\text{V}$
Emitter Cutoff Current		I_{EBO}	-300 -140 -65 -30	—	-580 -260 -130 -58	μA	$V_{EB} = -4\text{V}$
Collector-Emitter Saturation Voltage		$V_{CE(sat)}$	—	—	-0.3	V	$I_C = -10\text{mA}$, $I_B = -0.5\text{mA}$
DC Current Transfer Ratio		h_{FE}	30 56 68 82	—	—	—	$I_C = -5\text{mA}$, $V_{CE} = -5\text{V}$
Bleeder Resistor (R_2) Tolerance		ΔR_2	-30	—	+30	%	—
Gain-Bandwidth Product*		f_T	—	250	—	MHz	$V_{CE} = -10\text{V}$, $I_E = 5\text{mA}$, $f = 100\text{MHz}$

* Transistor - For Reference Only

Typical Curves – DDTA114KA



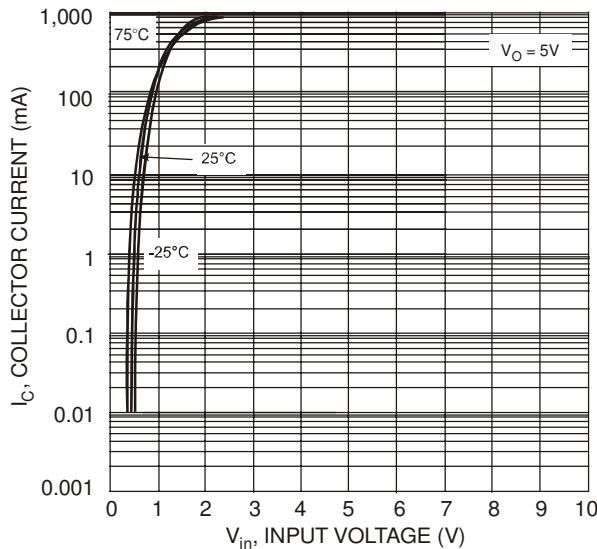


Fig. 5 Collector Current vs. Input Voltage

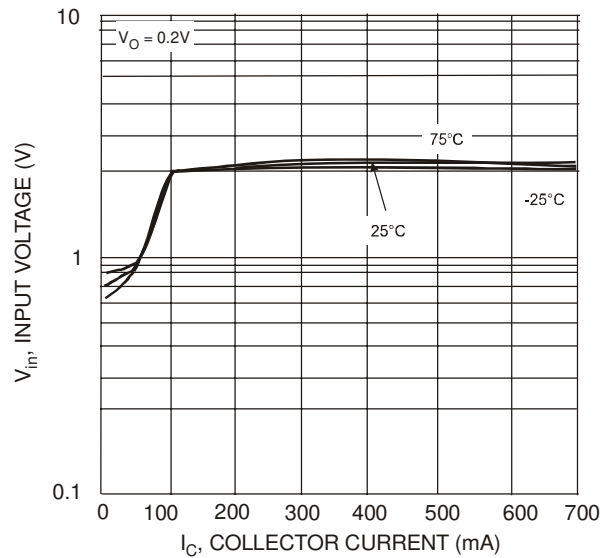


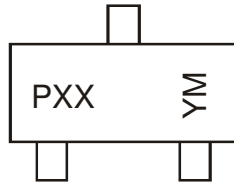
Fig. 6 Input Voltage vs. Collector Current

Ordering Information (Note 4 & 5)

Device	Packaging	Shipping
DDTA114GKA-7-F	SC-59	3000/Tape & Reel
DDTA124GKA-7-F	SC-59	3000/Tape & Reel
DDTA144GKA-7-F	SC-59	3000/Tape & Reel
DDTA115GKA-7-F	SC-59	3000/Tape & Reel

Notes: 5. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information



PXX = Product Type Marking Code, See Table on Page 1
 YM = Date Code Marking
 Y = Year ex: T = 2006
 M = Month ex: 9 = September

Date Code Key

Year	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012
Code	N	P	R	S	T	U	V	W	X	Y	Z

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

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