

SIDC24D30SIC3

Silicon Carbide Schottky Diode

FEATURES:

- Revolutionary semiconductor material -Silicon Carbide
- Switching behavior benchmark
- No reverse recovery
- No temperature influence on the switching behavior
- No forward recovery

Applications:

SMPS, snubber, secondary side rectification



Chip Type	V _{BR}	I _F	Die Size	Package	Ordering Code
SIDC24D30SIC3	300V	10A	1.706 x 1.38 mm ²	sawn on foil	Q67050-A4163- A103

MECHANICAL PARAMETER:

MEGHANIOAE I AHAMETEN:					
Raster size	1.706x 1.38	mm			
Anode pad size	1.405 x 1.08	— mm			
Area total / active	2.354 / 1.548	mm ²			
Thickness	355	μm			
Wafer size	75	mm			
Flat position	0	deg			
Max. possible chips per wafer	1649 pcs				
Passivation frontside	Photoimide				
node metalization 3200 nm Al					
Cathode metalization	1400 nm Ni Ag -system suitable for epoxy and soft solder die	1400 nm Ni Ag -system suitable for epoxy and soft solder die bonding			
Die bond	Electrically conductive glue or solder				
Wire bond	Al, ≤ 350μm				
Reject Ink Dot Size	Ø ≥ 0.3 mm				
Recommended Storage Environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C				
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Maximum Ratings

Parameter	Symbol	Condition	Value	Unit
Repetitive peak reverse voltage	V_{RRM}		300	V
Surge peak reverse voltage	V _{RSM}		300] *
Continuous forward current limited by	I _F		10	A
T_{jmax}	/F		10	
Single pulse forward current	I _{FSM}	$T_C = 25^{\circ} C$, $t_P = 10$ ms sinusoidal	36	
(depending on wire bond configuration)	1.1.2 IVI	17 -23 0, tp = 10 m3 3ma30idar	00	
Maximum repetitive forward current	I _{FRM}	$T_C = 100^{\circ} C, T_j = 150^{\circ} C,$	45	
limited by T _{jmax}	'FRM	D=0.1	7	
Non repetitive peak forward current	I_{FMAX}	$T_C = 25^{\circ} C$, $tp = 10 \mu s$	100	
Operating junction and storage temperature	$T_{\rm j}$, $T_{ m stg}$		-55+175	°C

$\textbf{Static Electrical Characteristics} \text{ (tested on chip)}, \ \textit{T}_{j}\text{=25 °C, unless otherwise specified}$

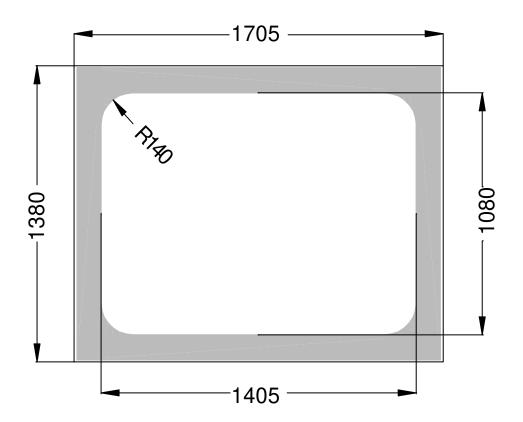
Parameter	Symbol Conditions			Value			Unit
raiametei	Symbol	Condi	itions	min.	Тур.	max. 200	Oiiit
Reverse leakage current	I _R	V _R =300V	<i>T_j</i> =25° <i>C</i>		15	200	μΑ
Forward voltage drop	V _F	I _F =10A	<i>T_j=25°C</i>		1.5	1.7	V

Dynamic Electrical Characteristics, at T_j = 25 °C, unless otherwise specified, tested at component

Parameter	Symbol	Conditions		Value			Unit
raiailletei	Syllibol			min.	Тур.	max.	T OILL
Total capacitive charge	Q_C	$I_F=10A$ $di/dt=200A/\mu s$ $V_R=200V$	$T_j = 150 ^{\circ}C$		23		nC
Switching time	t _{rr}	I _F =10A di/dt=200A/μs V _R = 200V	$T_j = 150 ^{\circ}C$		n.a.		ns
Total capacitance	С	$I_{F}=10A$ $di/dt=200A/\mu s$ $T_{j}=25^{\circ}C$ f=1MHz	V _R = 1 V		600		
			V _R =150V		55		pF
			V _R =300V		40		



CHIP DRAWING:





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FURTHER ELECTRICAL CHARACTERISTICS:

This chip data sheet refers to the device data sheet s

Description:

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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