



DMP1022UFDF

Product Summary

BV _{DSS}	R _{DS(ON)} max	I _D max T _A = +25°C
	14.8mΩ @ V _{GS} = -4.5V	-9.5A
-12V	19mΩ @ V _{GS} = -2.5V	-8.5A
-12V	26mΩ @ VGS = -1.8V	-7.2A
	32mΩ @ V _{GS} = -1.5V	-6.6A

Description

This MOSFET is designed specifically for use in battery management applications.

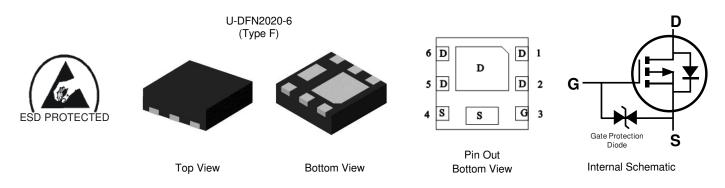
Features

- 0.6mm Profile Ideal for Low Profile Applications
- PCB Footprint of 4mm²
- Low Gate Threshold Voltage
- Fast Switching Speed
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

12V P-CHANNEL ENHANCEMENT MODE MOSFET

Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @
- Weight: 0.0065 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMP1022UFDF-7	U-DFN2020-6 (Type F)	3,000/Tape & Reel
DMP1022UFDF-13	U-DFN2020-6 (Type F)	10,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information

Site 1



PU = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date Code Kev

Year	2013		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	А		Н		J	K	L	М	N	0	Р	R
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Site 2



PU = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 0 = 2020) W = Week (ex: a = Week 27; z Represents Week 52 and 53) X = Internal Code (ex: U = Monday)

Data	Codo	Kov
Date	Code	ĸev

Year	2013		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	3		0	1	2	3	4	5	6	7	8	9
Week		1-	26			27-	-52			5	3	
Code		A	-Z		a-z			Z				
Internal Code	Sun	1	Mon		Tue	W	ed	Thu		Fri		Sat
Code	Т		U		V	V	V	Х		Y		Ζ



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage			VDSS	-12	V
Gate-Source Voltage			V _{GSS}	±8	V
Continuous Ducio Current (Note C) V 4 5V	Steady State	T _A = +25°C T _A = +70°C	ID	-9.5 -7.6	А
Continuous Drain Current (Note 6) $V_{GS} = -4.5V$	t<5s	T _A = +25°C T _A = +70°C	lD	-11.0 -8.8	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{DM}	-90	А
Continuous Source-Drain Diode Current	T _A = +25°C T _C = +25°C	ls	-2.5 -7.1	А	
Pulsed Source-Drain Diode Current (10µs Pulse, Du	uty Cycle = 1%)		lsм	-50	А

Thermal Characteristics

Characteristic		Symbol	Value	Unit	
Tetal Dower Dissinction (Note 5)	$T_A = +25^{\circ}C$	T _A = +25°C		W	
Total Power Dissipation (Note 5)	$T_A = +70^{\circ}C$	PD	0.47	VV	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Deve	172	°C/W	
Thermal Resistance, Junction to Amblent (Note 5)	t<5s	t<5s R _{0JA}		-0/00	
Tatal Bawar Dissinction (Nata 6)	$T_A = +25^{\circ}C$	D-	2.1	W	
Total Power Dissipation (Note 6)	$T_A = +70^{\circ}C$	PD	1.3		
Thermal Desistance, lunction to Ambient (Note C)	Steady State	Deve	59		
Thermal Resistance, Junction to Ambient (Note 6)	t<5s	Reja	45	°C/W	
Thermal Resistance, Junction to Case (Note 6)	Steady State	Rejc	5.1		
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Мах	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	0,		.,,,,	шах	onit	
Drain-Source Breakdown Voltage	BV _{DSS}	-12	—	_	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current TJ = +25°C	IDSS		—	-200	nA	V _{DS} = -12V, V _{GS} = 0V
Zero Gate Voltage Drain Current T _J = +55°C (Note 8)	IDSS	_	—	-2	μA	$V_{DS} = -12V, V_{GS} = 0V$
Gate-Source Leakage	Igss	_	—	±10	μA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	-0.35	—	-0.8	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$
			12	14.8		$V_{GS} = -4.5V, I_{D} = -4A$
Static Drain-Source On-Resistance	Preven		15	19	mΩ	$V_{GS} = -2.5V, I_{D} = -4A$
Static Drain-Source On-Nesistance	R _{DS(ON)}	_	20	26	11122	$V_{GS} = -1.8V, I_{D} = -4A$
			23	32		VGS = -1.5V, ID = -2A
Diode Forward Voltage	V _{SD}	_	-0.8	-1.2	V	$V_{GS} = 0V, I_{S} = -8A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss		2,712			
Output Capacitance	Coss		514		pF	$V_{DS} = -10V$, $V_{GS} = 0V$, f = 1.0MHz
Reverse Transfer Capacitance	Crss		467			
Gate Resistance	Rg		8.6	18	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge	Qg		48.3			$V_{GS} = -8V, V_{DS} = -6V, I_{D} = -10A$
Total Gate Charge	Qg		28.6		nC	$V_{GS} = -4.5V$, $V_{DS} = -6V$.
Gate-Source Charge	Qgs		4.2		no	$V_{GS} = -4.5V, V_{DS} = -6V,$ $I_{D} = -10A$
Gate-Drain Charge	Qgd	—	7.0	_		ID = -IOA
Turn-On Delay Time	t _{D(ON)}	_	25.1	_		
Turn-On Rise Time	tR		39.8	—	20	VDS = -6V, VGS = -4.5V,
Turn-Off Delay Time	tD(OFF)	—	141	—	ns	$R_G = 1\Omega, \ I_D = -8A$
Turn-Off Fall Time	tF	_	147	_		

Notes:

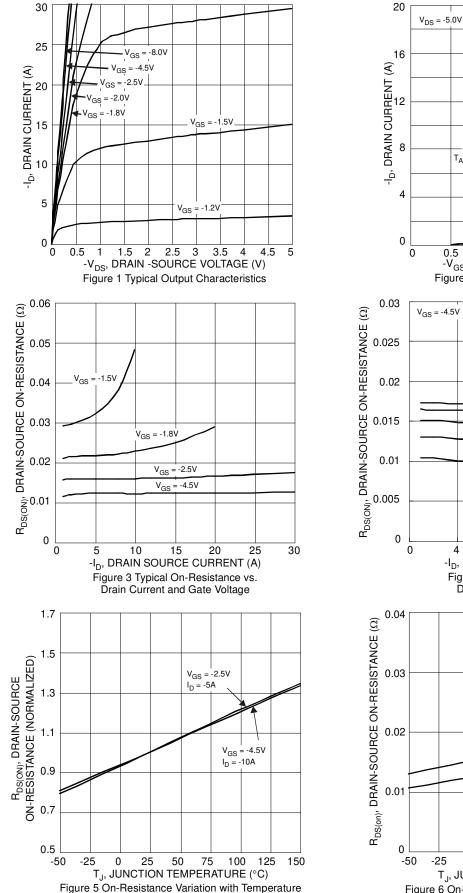
5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.

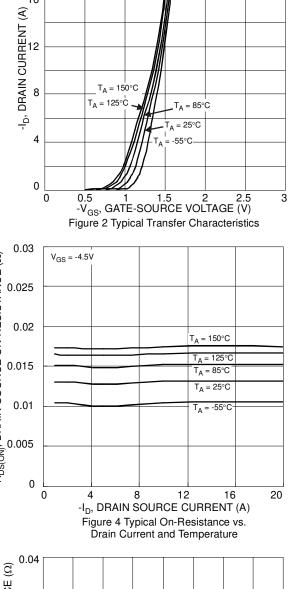
6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal vias to bottom layer 1inch square copper plate.

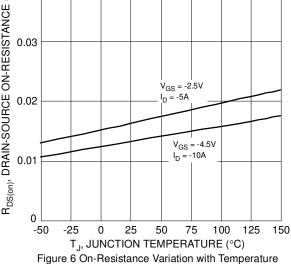
7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to production testing.



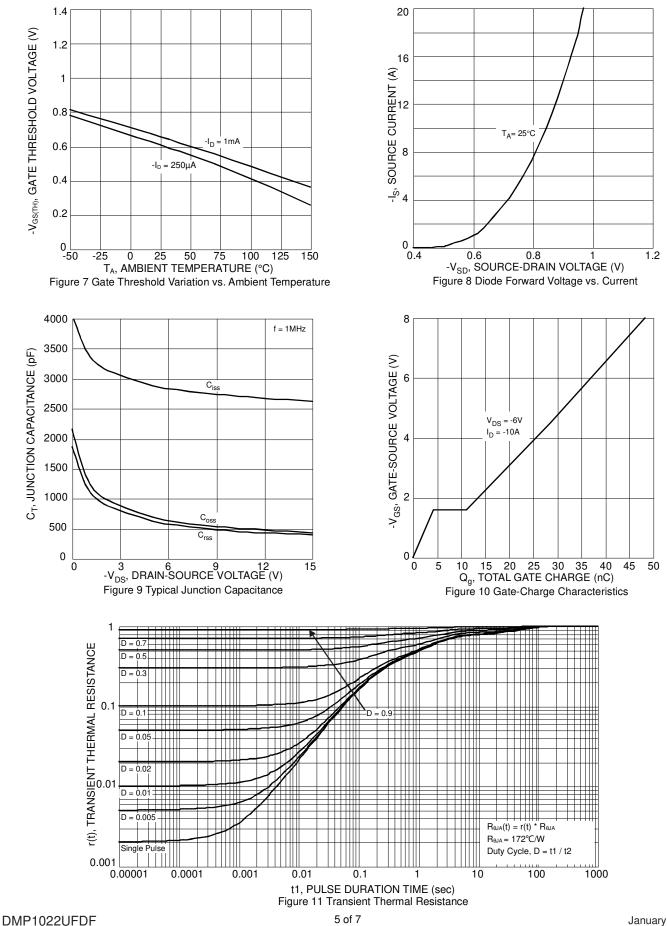








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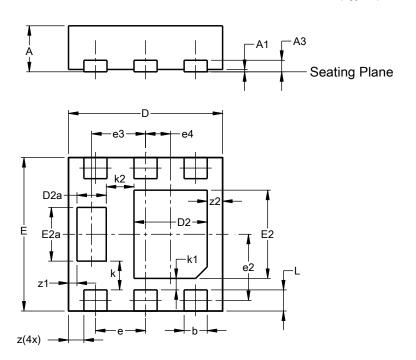
Datasheet number: DS36624 Rev. 6 - 2

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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



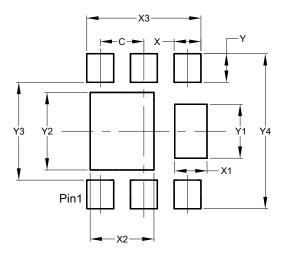
	U-DFN2020-6 (Type F)							
Dim	Min	Max	Тур					
Α	0.57	0.63	0.60					
A1	0.00	0.05	0.03					
A3	-	-	0.15					
b	0.25	0.35	0.30					
D	1.95	2.05	2.00					
D2	0.85	1.05	0.95					
D2a	0.33	0.43	0.38					
E	1.95	2.05	2.00					
E2	1.05	1.25	1.15					
E2a	0.65	0.75	0.70					
е		0.65 BS	С					
e2	-).863 BS						
e3		0.70 BS	С					
e4	C).325 BS	SC					
k		0.37 BS						
k1	0.15 BSC							
k2	0.36 BSC							
L	0.225 0.325 0.275							
z	0.20 BSC							
z1	-	0.110 BSC						
z2		0.20 BS						
All C)imens	ions in	mm					

U-DFN2020-6 (Type F)

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type F)



Dimensions	Value (in mm)
С	0.650
Х	0.400
X1	0.480
X2	0.950
X3	1.700
Y	0.425
Y1	0.800
Y2	1.150
Y3	1.450
Y4	2.300



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