



P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
- 30	0.0075 at V _{GS} = - 10 V	- 15		
- 30	0.011 at V _{GS} = - 4.5 V	- 12.3		

SO-8

Top View

FEATURES

• Halogen-free According to IEC 61249-2-21 Available

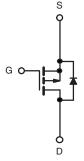


HALOGEN

FREE

APPLICATIONS

- Notebook
 - Load Switch
 - Battery Switch



P-Channel MOSFET



Ordering Information: Si4413ADY-T1-E3 (Lead (Pb)-free) Si4413ADY-T1-GE3 (Lead (Pb)-free and Halogen-free)

S

D

D

ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unles	ss otherwise n	oted		_
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V_{DS}	- 30		V
Gate-Source Voltage		V_{GS}	± 20		V
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	I_	- 15	- 10.5	۸
	T _A = 70 °C	- ID	- 11.8	- 8.3	
Pulsed Drain Current		I _{DM}	- 50		Α
Continuous Source Current (Diode Conduction) ^a		I _S	- 2.7	- 1.36	
Mariana Barra Birata di ad	T _A = 25 °C	P _D	3.0	1.5	W
Maximum Power Dissipation ^a	T _A = 70 °C	' D	1.9	0.95	vv
Operating Junction and Storage Temperature Range		T _J , T _{stq}	- 55	to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	t ≤ 10 s	R _{thJA}	33	42	
Maximum Junction-to-Ambient	Steady State	' 'thJA	70	84	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	16	21	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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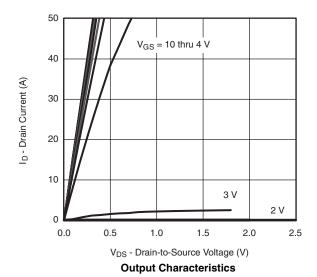
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			•				
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.0		- 3.0	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$	V _{DS} = - 30 V, V _{GS} = 0 V		- 1	μΑ	
		V _{DS} = - 30 V, V _{GS} = 0 V, T _J = 70 °C			- 10		
On-State Drain Current ^a	$I_{D(on)}$ $V_{DS} = -5 \text{ V}, V_{GS} = -10 \text{ V}$		- 30			Α	
	В	V _{GS} = - 10 V, I _D = - 13 A		0.0063	0.0075	0	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -4.5 \text{ V}, I_D = -10 \text{ A}$		0.0083	0.011	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 13 A		50		S	
Diode Forward Voltage ^a	V_{SD}	I _S = - 2.7 A, V _{GS} = 0 V		- 0.74	- 1.1	V	
Dynamic ^b			•				
Total Gate Charge	Q_g			61	95	nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = -15 \text{ V}, V_{GS} = -5 \text{ V}, I_{D} = -13 \text{ A}$		15.5			
Gate-Drain Charge	Q_{gd}			32		1	
Turn-On Delay Time	t _{d(on)}			21	35		
Rise Time	t _r	V_{DD} = - 15 V, R_L = 15 Ω		18	30	- ns	
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ - 1 A, V_{GEN} = - 10 V, R_G = 6 Ω		170	260		
Fall Time	t _f			97	150		
Gate Resistance	R_{g}			3.4		Ω	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 2.1 A, dl/dt = 100 A/μs		70	110	ns	

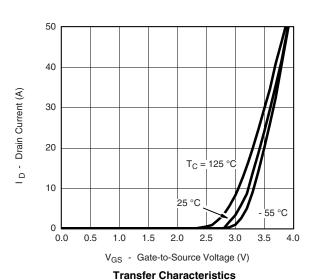
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



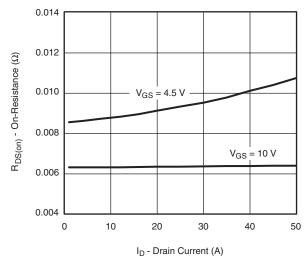




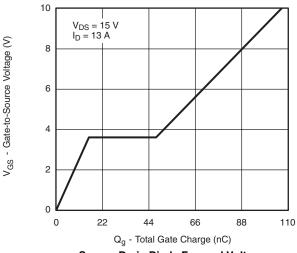




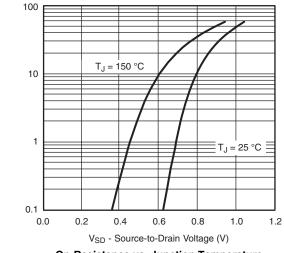
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



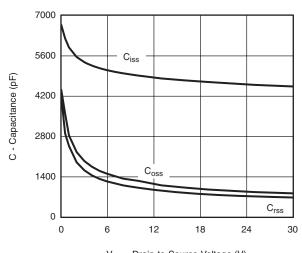
On-Resistance vs. Drain Current



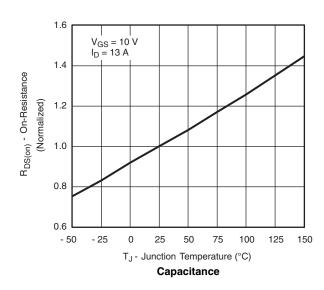
Source-Drain Diode Forward Voltage

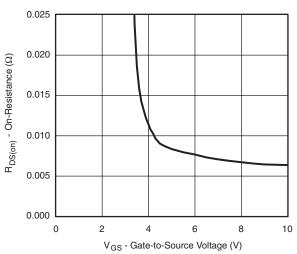


On-Resistance vs. Junction Temperature



V_{DS} - Drain-to-Source Voltage (V) **Gate Charge**





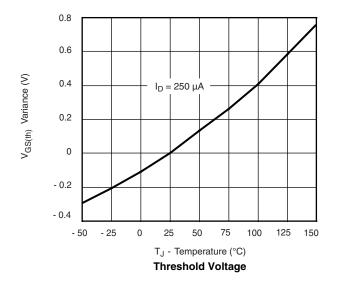
On-Resistance vs. Gate-to-Source Voltage

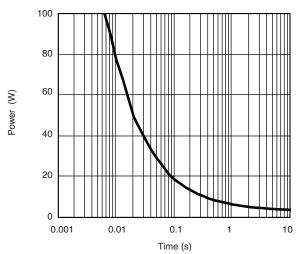
Is - Source Current (A)

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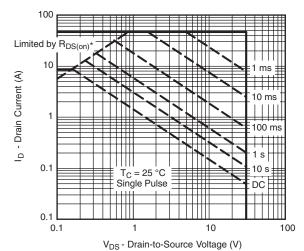
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

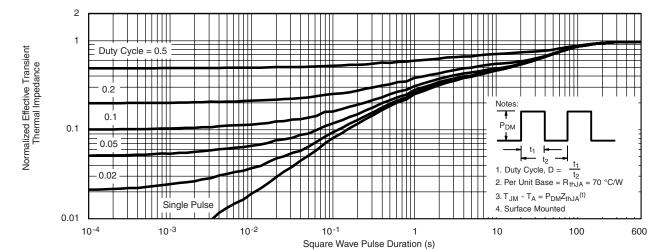




Single Pulse Power, Junction-to-Ambient



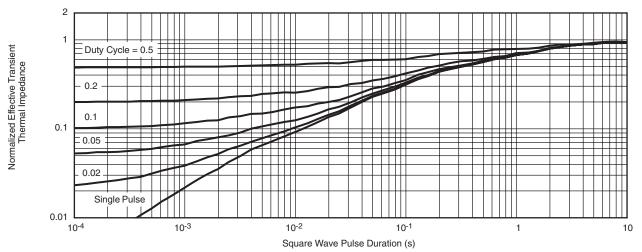
* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified Safe Operating Area, Junction-to-Case



Normalized Thermal Transient Impedance, Junction-to-Ambient



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

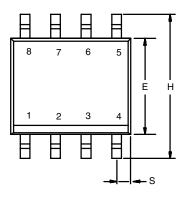


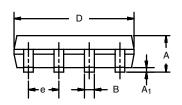
Normalized Thermal Transient Impedance, Junction-to-Foot

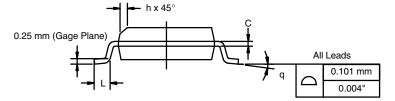
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SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







	MILLIM	IETERS	INCHES			
DIM	Min	Max	Min	Max		
Α	1.35	1.75	0.053	0.069		
A ₁	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
Е	3.80	4.00	0.150	0.157		
е	1.27	BSC	0.050	BSC		
Н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
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DWG: 5498

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APPLICATION NOTE



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

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