

# STGW35NB60S

N-channel 35A - 600V - TO-247 Low drop PowerMESH™ IGBT

### **Features**

Туре	V <sub>CES</sub>	V <sub>CE(sat)</sub> (Max)@ 25°C	I <sub>C</sub> @100°C
STGW35NB60S	600V	< 1.7V	35A

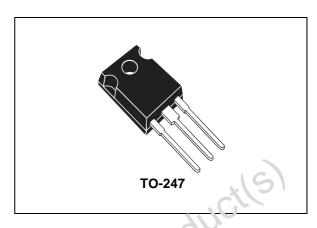
- Low on-voltage drop (V<sub>CFsat</sub>)
- Low input capacitance
- High current capability

### **Description**

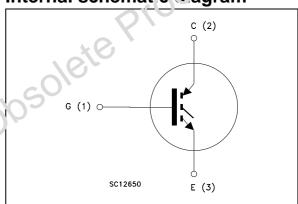
Using the latest high voltage technology based on a patented strip layout, STMicroelectronics has designed an advanced family of IGBTs, the PowerMESH  $^{\mathsf{TM}}$  IGBTs, with outstanding performances.

## **Applications**

- Light dimmer
- HID
- Welding
- lete Product(s) Motor control
- Static relays



### Internal schematic diagram



### Cider code

Part number	Marking	Package	Packaging
STGW35NB60S	GW35NB60S	TO-247	Tube

Contents STGW35NB60S

### **Contents**

1	Electrical ratings 3
2	Electrical characteristics4
	2.1 Electrical characteristics (curves) 6
3	Test Circuits
4	Package mechanical data
5	Revision history12
005	Revision history

STGW35NB60S Electrical ratings

#### **Electrical ratings** 1

Table 1. **Absolute maximum ratings** 

Symbol	Parameter	Value	Unit
V <sub>CES</sub>	Collector-emitter voltage (V <sub>GS</sub> = 0)	600	V
I <sub>C</sub> <sup>(1)</sup>	Collector current (continuous) at 25°C	70	Α
I <sub>C</sub> <sup>(1)</sup>	Collector current (continuous) at 100°C	35	Α
I <sub>CM</sub> <sup>(2)</sup>	Collector current (pulsed)	250	Α
V <sub>GE</sub>	Gate-emitter voltage	± 20	V
P <sub>TOT</sub>	Total dissipation at T <sub>C</sub> = 25°C	200	W
T <sub>j</sub>	Operating junction temperature	- 55 to 150	°C

$$I_{C}(T_{C}) = \frac{T_{JMAX}^{-T}C}{R_{THJ-C}^{VCESAT(MAX)}(T_{C}, I_{C})}$$

Table 2. Thermal resistance

	' J	operating junction temperature	33 10 130	J
	1. Calculate	d according to the iterative formula:		
	I <sub>C</sub> (T <sub>0</sub>	$C^{(1)} = \frac{T_{JMAX}^{-}T_{C}}{R_{THJ-C}^{V}CESAT(MAX)^{(T_{C}, I_{C})}}$	Jucile	
	2. Pulse widt	th limited by max. junction temperature	AUIO	
			~400	
	Table 2.	Thermal resistance	010	
			Value	Unit
	Rthj-case	Thermal resistance junction-case max	0.625	°C/W
	Rthj-amb	Thermal resistance junction-ambient max	50	°C/W
Obsole	te P	coducits		

STGW35NB60S Electrical characteristics

#### **Electrical characteristics** 2

(T<sub>CASE</sub> = 25 °C unless otherwise specified)

Table 3. Static

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
V <sub>BR(CES)</sub>	Collector-Emitter Breakdown Voltage	$I_C = 1$ mA, $V_{GE} = 0$	600			V
V <sub>CE(SAT)</sub>	Collector-Emitter Saturation Voltage	V <sub>GE</sub> = 15V, I <sub>C</sub> = 20A, V <sub>GE</sub> = 15V, I <sub>C</sub> = 20A, Tj= 125°C		1.25 1.2	1.7	V V
V <sub>GE(th)</sub>	Gate Threshold Voltage	$V_{CE} = V_{GE}$ , $I_{C} = 250\mu A$	2.5		5	V
I <sub>CES</sub>	Collector-Emitter Leakage Current (V <sub>GE</sub> = 0)	V <sub>CE</sub> = Max Rating, V <sub>CE</sub> = Max Rating, Tc=125°C			10 100	μA μA
I <sub>GES</sub>	Gate-Emitter Leakage Current (V <sub>CE</sub> = 0)	V <sub>GE</sub> = ± 20V , V <sub>CE</sub> = 0		AU!	± 100	nA
9 <sub>fs</sub>	Forward Transconductance	V <sub>CE</sub> = 10V, I <sub>C</sub> = 18A	OYC	20		S

Table 4. **Dynamic** 

0	Transconductance	S= , S				
Table 4.	Dynamic	16/6				
Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
C <sub>ies</sub> C <sub>oes</sub> C <sub>res</sub>	Input Capacitance Output Capacitance Reverse Transfer Capacitance	$V_{CE} = 25V, f = 1 \text{ MHz}, V_{GE} = 0$		1820 167 27		pF pF pF
Q <sub>g</sub> Q <sub>ge</sub> Q <sub>gc</sub>	Total Gate Charge Gate-Emitter Charge Gate-Collector Charge	$V_{CE} = 480V$ , $I_{C} = 20A$ , $V_{GE} = 15V$ , (see Figure 16)		83 10 27	115	nC nC nC
IcL	Turn-Off SOA Minimum Current	$V_{clamp} = 480V$ , $Tj = 125^{\circ}C$ $R_G = 100\Omega$	80			Α

STGW35NB60S Electrical characteristics

Table 5. Switching on/off (inductive load)

Symbol	Parameter Test conditions		Min.	Тур.	Max.	Unit
t <sub>d(on)</sub> t <sub>r</sub> (di/dt) <sub>on</sub>	Turn-on Delay Time Current Rise Time Turn-on Current Slope	$V_{CC} = 480V$ , $I_{C} = 20A$ $R_{G} = 100\Omega$ , $V_{GE} = 15V$ , see <i>Figure 15</i> and <i>17</i>		92 70 340		ns ns A/µs
t <sub>d(on)</sub> t <sub>r</sub> (di/dt) <sub>on</sub>	Turn-on Delay Time Current Rise Time Turn-on Current Slope	$V_{CC} = 480 \text{V}, I_{C} = 20 \text{A}$ $R_{G} = 100 \Omega, V_{GE} = 15 \text{V},$ $T_{J} = 125 ^{\circ}\text{C}$ see <i>Figure 15</i> and <i>17</i>		80 73 320		ns ns A/µs
$t_r(V_{off})$ $t_d(_{off})$ $t_f$	Off Voltage Rise Time Turn-off Delay Time Current Fall Time	$V_{cc}$ = 480V, $I_{C}$ = 20A, $R_{GE}$ = 100 $\Omega$ , $V_{GE}$ = 5V, see <i>Figure 15</i> and <i>17</i>		0.78 1.1 0.79		μs μs μs
t <sub>r</sub> (V <sub>off</sub> ) t <sub>d</sub> ( <sub>off</sub> ) t <sub>f</sub>	Off Voltage Rise Time Turn-off Delay Time Current Fall Time	$V_{cc}$ = 480V, $I_{C}$ = 20A, $R_{GE}$ =100 $\Omega$ V <sub>GE</sub> =15V, Tj=125°C see <i>Figure 15</i> and <i>17</i>		1.1 2.4 1.2	*16	µs µs µs

Table 6. Switching energy (inductive load)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Eon E <sub>off</sub> <sup>(1)</sup> E <sub>ts</sub>	Turn-on Switching Losses Turn-off Switching Losses Total Switching Losses			0.84 7.4 8.24		mJ mJ mJ
Eon E <sub>off</sub> <sup>(1)</sup> E <sub>ts</sub>	Turn-on Switching Losses Turn-off Switching Losses Total Switching Losses	Ra=1000 Var= 15V		0.86 11.5 12.4		mJ mJ mJ

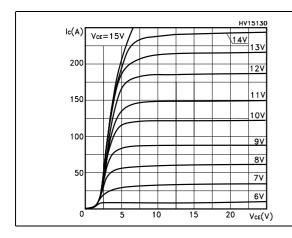
<sup>1.</sup> Turn-off losses include also the tail of the collector current

Electrical characteristics STGW35NB60S

## 2.1 Electrical characteristics (curves)

Figure 1. Output characterisics

Figure 2. Transfer characteristics



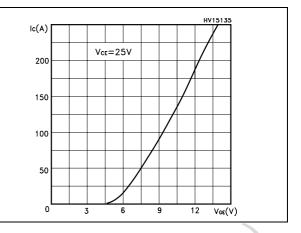
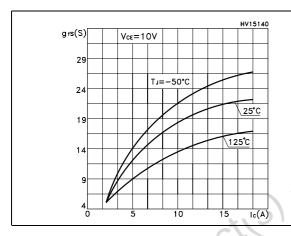


Figure 3. Transconductance

Figure 4. Normalized collector-emitter on voltage vs temperature



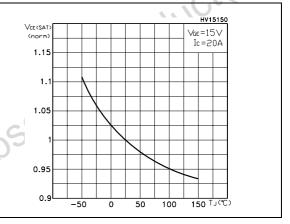
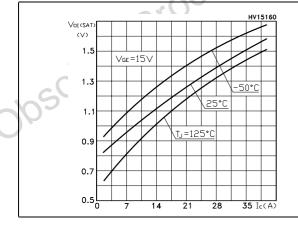
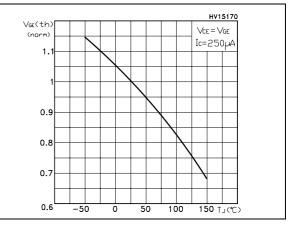


Figure 5. Collector-emitter on voltage vs collector current

Figure 6. Gate threshold vs temperature





STGW35NB60S Electrical characteristics

Figure 7. Normalized breakdown voltage vs Figure 8. Gate charge vs gate-emitter voltage temperature

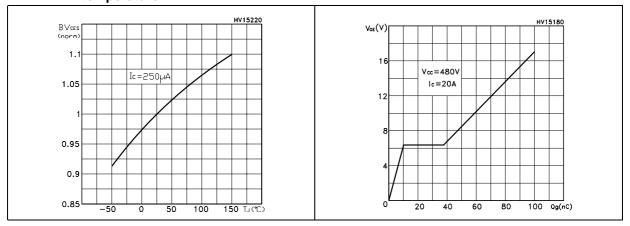


Figure 9. Capacitance variations

Figure 10. Switching losses vs gate charge

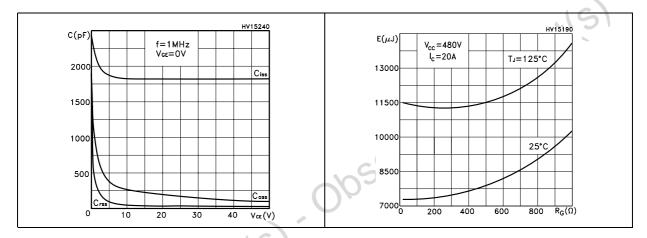
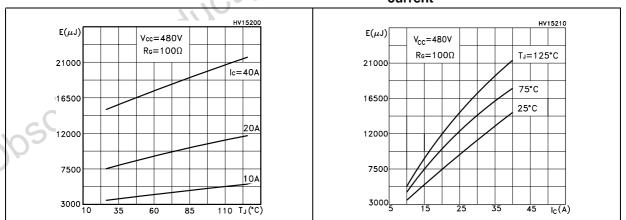


Figure 11. Switching losses vs temperature

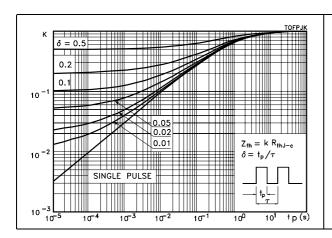
Figure 12. Switching losses vs collector current

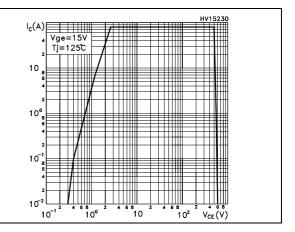


Electrical characteristics STGW35NB60S

Figure 13. Thermal impedance

Figure 14. Turn-off SOA





Obsolete Producits). Obsolete Producits)

STGW35NB60S **Test Circuits** 

#### 3 **Test Circuits**

Figure 15. Test circuit for inductive load switching

Figure 16. Gate charge test circuit

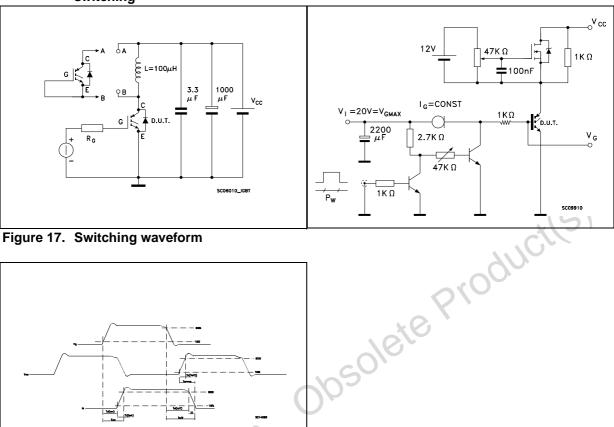
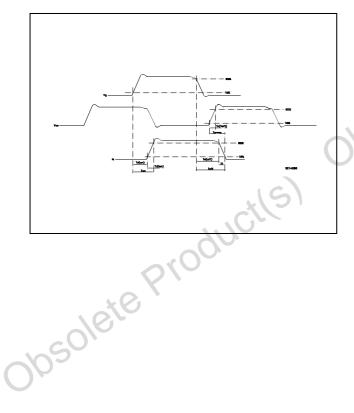


Figure 17. Switching waveform



5//

Package mechanical data STGW35NB60S

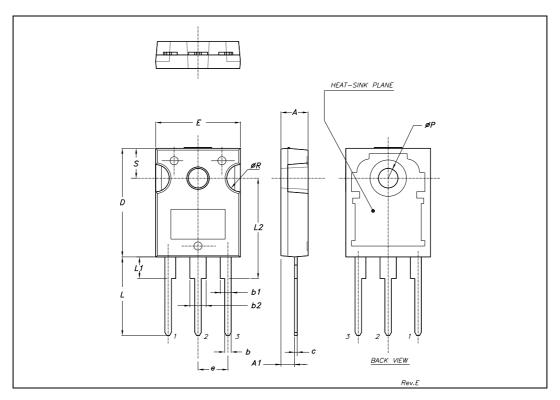
## 4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: <a href="https://www.st.com">www.st.com</a>



### **TO-247 MECHANICAL DATA**

DIM.		mm.			inch	
DIWI.	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A	4.85		5.15	0.19		0.20
A1	2.20		2.60	0.086		0.102
b	1.0		1.40	0.039		0.055
b1	2.0		2.40	0.079		0.094
b2	3.0		3.40	0.118		0.134
С	0.40		0.80	0.015		0.03
D	19.85		20.15	0.781		0.793
E	15.45		15.75	0.608		0.620
е		5.45			0.214	
L	14.20		14.80	0.560		0.582
L1	3.70		4.30	0.14		0.17
L2		18.50			0.728	
øΡ	3.55		3.65	0.140		0.143
øR	4.50		5.50	0.177		0.216
S		5.50			0.216	



**5**//

Revision history STGW35NB60S

# 5 Revision history

Table 7. Revision history

Date	Revision	Changes
28-Mar-2007	1	Initial release.

Obsolete Product(s). Obsolete Product(s)

#### Please Read Carefully:

Information in this document is provided solely in connection with ST products. STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, modifications or improvements, to this document, and the products and services described herein at any time, without notice.

All ST products are sold pursuant to ST's terms and conditions of sale.

Purchasers are solely responsible for the choice, selection and use of the ST products and services described herein, and ST assumes no liability whatsoever relating to the choice, selection or use of the ST products and services described herein.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted under this document. If any part of this document refers to any third party products or services it shall not be deemed a license grant by ST for the use of such third party products or services, or any intellectual property contained therein or considered as a warranty covering the use in any manner whatsoever of such third party products or services or any intellectual property contained therein.

UNLESS OTHERWISE SET FORTH IN ST'S TERMS AND CONDITIONS OF SALE ST DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY WITH RESPECT TO THE USE AND/OR SALE OF ST PRODUCTS INCLUDING WITHOUT LIMITATION IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION), OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

UNLESS EXPRESSLY APPROVED IN WRITING BY AN AUTHORIZED ST REPRESENTATIVE, ST PRODUCTS ARE NOT RECOMMENDED, AUTHORIZED OR WARRANTED FOR USE IN MILITARY, AIR CRAFT, SPACE, LIFE SAVING, OR LIFE SUSTAINING APPLICATIONS, NOR IN PRODUCTS OR SYSTEMS WHERE FAILURE OR MALFUNCTION MAY RESULT IN PERSONAL INJURY, DEATH, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE. ST PRODUCTS WHICH ARE NOT SPECIFIED AS "AUTOMOTIVE GRADE" MAY ONLY BE USED IN AUTOMOTIVE APPLICATIONS AT USER'S OWN RISK.

Resale of ST products with provisions different from the statements and/or technical features set forth in this document shall immediately void any warranty granted by ST for the ST product or service described herein and shall not create or extend in any manner whatsoever, any liability of ST.

ST and the ST logo are trademarks or registered trademarks of ST in various countries.

Information in this document supersedes and replaces all information previously supplied.

The ST logo is a registered trademark of STMicroelectronics. All other names are the property of their respective owners.

© 2007 STMicroelectronics - All rights reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America

www.st.com

**577**