

3-Terminal 100mA Negative Voltage Regulator

DESCRIPTION

The TS79L05CY of negative voltage regulators are inexpensive, easy-to-use devices suitable for a multitude of applications that require a regulated supply of up to 100mA. Like their higher power TS7905 and TS79M05 Series cousins, these regulators feature internal current limiting and thermal shutdown making them remarkably rugged. No external components are required with the TS79L05CY devices in many applications. These devices offer a substantial performance advantage over the traditional zener dioderesistor combination, as output impedance quiescent current are substantially reduced.

FEATURES

- Output Voltage Range -5V
- Output current up to 100mA
- Internal thermal overload protection
- Internal short-circuit current limiting
- Output transistor safe-area compensation
- Output voltage offered in 4% tolerance
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

APPLICATION

- Switching power supply
- Home appliance







SOT-89

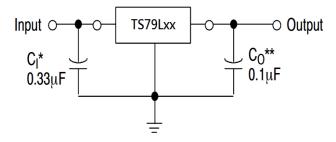


Pin Definition:

- 1. Ground
- 2. Input
- 3. Output

Notes: MSL 3 (Moisture Sensitivity Level) per J-STD-020

STANDARD APPLICATION CIRCUIT



A common ground is required between the input and the output voltages. The input voltage must remain typically 2.0V above the output voltage even during the low point on the Input ripple voltage.

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XX = these two digits of the type number indicate voltage.

* = Cin is required if regulator is located an appreciable distance from power supply filter.

** = Co is not needed for stability; however, it does improve transient response.





ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	LIMIT	UNIT		
DC Input Voltage	V _{IN}	-35	V		
Power Dissipation	P _D	Internally Limited	W		
Operating Temperature range	T _{OPR}	0 ~ +125	°C		
Operating Junction Temperature Range	T _J	0 ~ +150	°C		
Storage Temperature Range	T _{STG}	-65 ~ +150	°C		

THERMAL PERFORMANCE				
PARAMETER	SYMBOL	TYP	UNIT	
Junction to Case Thermal Resistance	R _{eJC}	18	°C/W	

Notes: Considering 6cm² of copper board heat-sink

PARAMETER	T _J ≤125°C, C _{IN} =0.33μF, C _{OUT} =0.1μF CONDITIONS		SYMBOL	MIN	TYP	MAX	UNIT
	T_J =25°C -7.5V \leq V _{IN} \leq -20V, 5mA \leq I _{OUT} \leq 100mA		V _{OUT}	-4.80	-5	-5.20	V
Output voltage				-4.75	-5	-5.25	V
Line Regulation	T _J =25°C	-7.5V≤Vin≤-20V	REG _{LINE}		50	150	mV
L. J.DJ.C	T 05°0	5mA≤ I _{OUT} ≤100mA	DEO		20	60	mV
Load Regulation	T _J =25°C	5mA≤I _{OUT} ≤40mA	REG _{LOAD}		10	30	
Quiescent Current	I _{OUT} =0, T _J =25°C		IQ		3	6	mA
0: 10 10	-7.5V≤Vin	≤-25V	- Δl _Q			1.5	mA
Quiescent Current Change	5mA≤I _{OUT}	≤40mA				0.1	
Output Noise Voltage	10Hz≤f≤100kHz, T _J =25°C		V_N		40		μV
Ripple Rejection Ratio	f=120Hz, -8V≤Vin≤-18V		RR	41	49		dB
Voltage Drop	I _{OUT} =100mA, T _J =25°C		V_{DROP}		1.7		V
Peak Output Current	T _J =25°C		lo peak		0.15		Α
Temperature Coefficient of Output Voltage	I _{OUT} =5mA, 0°C≤T _J ≤125°C		$\Delta V_{OUT} / \Delta T_{J}$		-0.65		mV/ °C

Note:

ORDERING INFORMATION

ORDERING CODE	PACKAGE	PACKING
TS79L05CY RMG	SOT-89	1,000pcs / 7" Reel

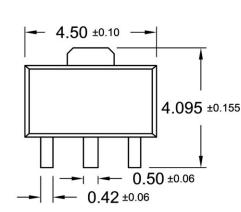
^{1.} Pulse testing techniques are used to maintain the junction temperature as close to the ambient temperature as possible, and thermal effects must be taken into account separately

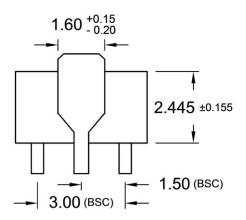
^{2.} This specification applies only for DC power dissipation permitted by absolute maximum ratings.

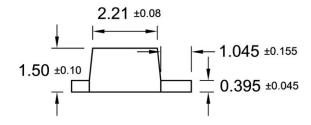


PACKAGE OUTLINE DIMENSIONS (Unit: Millimeters)

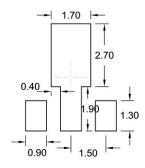
SOT-89







SUGGESTED PAD LAYOUT (Unit: Millimeters)



MARKING DIAGRAM



XX = Output Voltage (**05**=-5V)

Y = Year Code

M = Month Code for Halogen Free Product

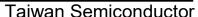
O =Jan P =Feb Q =Mar R =Apr

S =May T =Jun U =Jul V =Aug

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W =Sep X =Oct Y =Nov Z =Dec

L = Lot Code





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