

Rochester Electronics Manufactured Components

Rochester branded components are manufactured using either die/wafers purchased from the original suppliers or Rochester wafers recreated from the original IP. All recreations are done with the approval of the OCM.

Parts are tested using original factory test programs or Rochester developed test solutions to guarantee product meets or exceed the OCM data sheet.

Quality Overview

- ISO-9001
- AS9120 certification
- Qualified Manufacturers List (QML) MIL-PRF-35835
 - Class Q Military
 - Class V Space Level
- Qualified Suppliers List of Distributors (QSLD)
 - Rochester is a critical supplier to DLA and meets all industry and DLA standards.

Rochester Electronics, LLC is committed to supplying products that satisfy customer expectations for quality and are equal to those originally supplied by industry manufacturers.

The original manufacturer's datasheet accompanying this document reflects the performance and specifications of the Rochester manufactured version of this device. Rochester Electronics guarantees the performance of its semiconductor products to the original OEM specifications. 'Typical' values are for reference purposes only. Certain minimum or maximum ratings may be based on product characterization, design, simulation, or sample testing.

SN54ALS2242, SN74ALS2242 QUADRUPLE BUS TRANSCEIVERS/MOS DRIVER

MAY 1985 - REVISED MAY 1986

- Bidirectional Quadruple Bus Transceivers For Driving MOS Devices
- I/O Ports Have 25 Ohm Series Resistors so No External Resistors are Required
- Package Options Include Plastic "Small Outline" Packages, Plastic and Ceramic Chip Carriers, and Standard Plastic and Ceramic 300-mil DIPs
- Dependable Texas Instruments Quality and Reliability

description

These quadruple bus transceivers are designed to drive the capacitive input characteristics of MOS devices and allow asynchronous two-way communication between data buses. The control function implementation allows for maximum flexibility in timing.

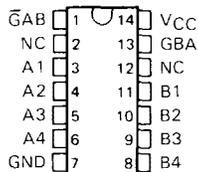
These devices allow data transmission from A bus to the B bus or from the B bus to the A bus depending upon the logic levels at the enable inputs (GBA and $\bar{G}AB$).

The enable inputs can be used to disable the device so that the buses are effectively isolated.

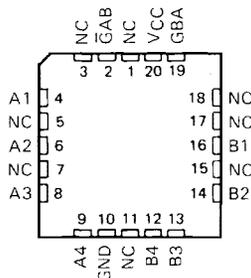
The dual-enable configuration gives the 'ALS2242 the capability to store data by simultaneous enabling of GBA and $\bar{G}AB$. Each output reinforces its input in this transceiver configuration. Thus, when both control inputs are enabled and all other data sources to the two sets of bus lines are at high impedance, both sets of bus lines (16 in all) will remain at their last states. The 8-bit codes appearing on the two sets of buses will be complementary.

The SN54ALS2242 is characterized for operation over the full military temperature range of -55°C to 125°C . The SN74ALS2242 is characterized for operation from 0°C to 70°C .

SN54ALS2242 . . . J PACKAGE
SN74ALS2242 . . . D OR N PACKAGE
(TOP VIEW)



SN54ALS2242 . . . FK PACKAGE
(TOP VIEW)



NC - No internal connection

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ALS and AS Circuits

PRODUCTION DATA

This document contains information current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

TEXAS
INSTRUMENTS

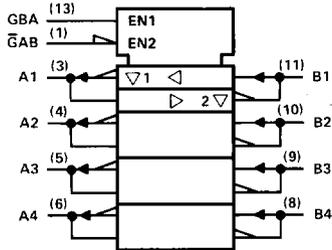
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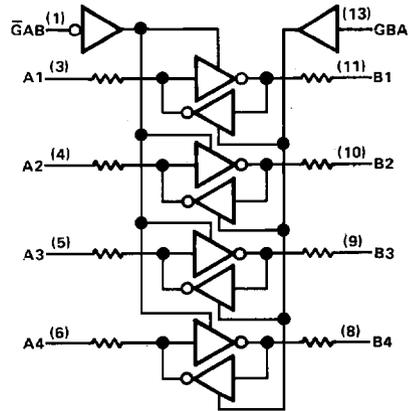
SN54ALS2242, SN74ALS2242 QUADRUPLE BUS TRANSCEIVERS/MOS DRIVER

logic symbol†



† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12. Pin numbers shown are for D, J, and N packages.

logic diagrams (positive logic)



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, V_{CC}	7 V
Input voltage: All inputs	7 V
I/O ports	5.5 V
Operating free-air temperature range: SN54ALS2242	-55°C to 125°C
SN74ALS2242	0°C to 70°C
Storage temperature range	-65°C to 150°C

recommended operating conditions

	SN54ALS2242			SN74ALS2242			UNIT
	MIN	NOM	MAX	MIN	NOM	MAX	
V_{CC} Supply voltage	4.5	5	5.5	4.5	5	5.5	V
V_{IH} High-level input voltage	2			2			V
V_{IL} Low-level input voltage			0.7			0.8	V
T_A Operating free-air temperature	-55		125	0		70	°C

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ALS and AS Circuits

SN54ALS2242, SN74ALS2242 QUADRUPLE BUS TRANSCEIVERS/MOS DRIVER

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS	SN54ALS2242			SN74ALS2242			UNIT	
		MIN	TYP [†]	MAX	MIN	TYP [†]	MAX		
V _{IK}	V _{CC} = 4.5 V, I _I = -18 mA			1.2			-1.2	V	
V _{OH}	V _{CC} = 4.5 V to 5.5 V, I _{OH} = 0.4 mA	V _{CC} / 2			V _{CC} / 2			V	
V _{OL}	V _{CC} = 4.5 V, I _{OL} = 1 mA		0.15	0.5		0.15	0.5	V	
	V _{CC} = 4.5 V, I _{OL} = 12 mA		0.35	0.8		0.35	0.8		
I _I	Control inputs	V _{CC} = 5.5 V, V _I = 7 V		0.1		0.1		mA	
	A or B ports	V _{CC} = 5.5 V, V _I = 5.5 V		0.1		0.1			
I _{IH}	Control inputs	V _{CC} = 5.5 V, V _I = 2.7 V		20		20		μA	
	A or B ports [‡]			20		20			
I _{IL}	Control inputs	V _{CC} = 5.5 V, V _I = 0.4 V		0.1		0.1		mA	
	A or B ports [‡]			0.1		0.1			
I _{O3}	V _{CC} = 5.5 V, V _O = 2.25 V		30	112		30	112	mA	
I _{OH}	V _{CC} = 4.5 V, V _O = 2 V		15			15		mA	
I _{OL}	V _{CC} = 4.5 V, V _O = 2 V		30			30		mA	
I _{CC}	V _{CC} = 5.5 V	Outputs high		10	20		10	16	mA
		Outputs low		14	26		14	21	
		Outputs disabled		13	24		13	19	

[†]All typical values are at V_{CC} = 5 V, T_A = 25°C.

[‡]For I/O ports, the parameters I_{IH} and I_{IL} include the off state output current.

[§]The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, I_{O3}.

switching characteristics (see Note 1)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V _{CC} = 5 V, C _L = 50 pF, R ₁ = 500 Ω, R ₂ = 500 Ω, T _A = 25°C		V _{CC} = 4.5 V to 5.5 V, C _L = 50 pF, R ₁ = 500 Ω, R ₂ = 500 Ω, T _A = MIN to MAX		UNIT	
			ALS2242		SN74ALS2242			
			TYP	MIN	MAX	MIN		MAX
t _{PLH}	A or B	B or A	5	2	15	2	11	ns
t _{PHL}			5	2	14	2	10	
t _{PZH}	GAB	B	8	3	18	3	16	ns
t _{PZL}			11	5	22	5	20	
t _{PHZ}	GAB	B	6	2	12	2	10	ns
t _{PLZ}			6	2	18	2	12	
t _{PZH}	GBA	A	10	3	18	3	16	ns
t _{PZL}			12	5	22	5	20	
t _{PHZ}	GBA	A	6	2	12	2	10	ns
t _{PLZ}			6	2	18	2	14	

NOTE 1: Load circuit and voltage waveforms are shown in Section 1.

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ALS and AS Circuits

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