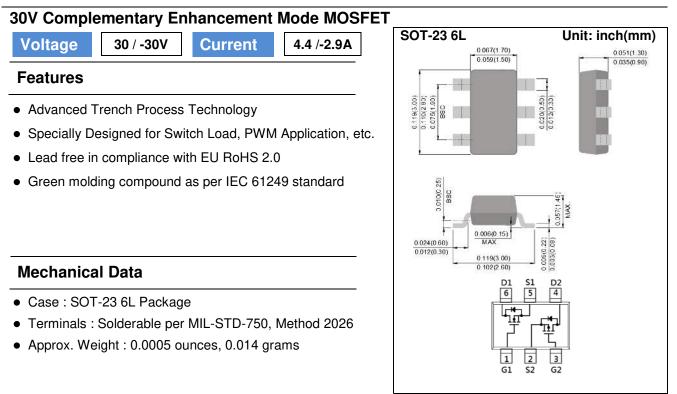
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	CONDUCTOR



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	N-Ch LIMIT	P-Ch LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	30	-30	V
Gate-Source Voltage	V _{GS}	<u>+</u> 20	<u>+</u> 20	V	
Continuous Drain Current		١D	4.4	-2.9	А
Pulsed Drain Current ^(Note 4)		I _{DM}	17.6	-11.6	А
Power Dissipation	T _a =25°C		1.25		W
	Derate above 25°C	PD	1	mW/∘C	
Operating Junction and Storage Ten	TJ,TSTG	-55~150		°C	
Typical Thermal Resistance - Junction to Ambient ^(Note 3)		Reja	100		°C/W



N-Channel Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static	•		•			
Drain-Source Breakdown Voltage	BV _{DSS}	$V_{GS}=0V$, $I_{D}=250uA$	30	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	1.0	1.37	2.1	V
Drain-Source On-State Resistance		V_{GS} =10V, I_{D} =4.4A	-	36	48	
	R _{DS(on)}	V _{GS} =4.5V, I _D =2.8A	-	52	70	mΩ
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =30V, V_{GS} =0V	-	-	1	uA
Gate-Source Leakage Current	Igss	V _{GS} = <u>+</u> 20V, V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic ^(Note 5)		·				
Total Gate Charge	Qg		-	5.8	-	
Gate-Source Charge	Q _{gs}	V_{DS} =15V, I _D =4.4A, V _{GS} =10V ^(Note 1,2)	-	1	-	nC
Gate-Drain Charge	Q _{gd}		-	1	-	
Input Capacitance	Ciss	V _{DS} =15V, V _{GS} =0V, f=1.0MHZ	-	235	-	
Output Capacitance	Coss		-	36	-	pF
Reverse Transfer Capacitance	Crss		-	24	-	
Turn-On Delay Time	td _(on)		-	3	-	
Turn-On Rise Time	tr	V _{DD} =15V, I _D =4.4A,	-	39	-	
Turn-Off Delay Time	td _(off)	V _{GS} =10V, R _G =6Ω ^(Note 1,2)	-	23	-	ns
Turn-Off Fall Time	tf		-	28	-	
Drain-Source Diode	·	·	·	·	·	·
Maximum Continuous Drain-Source	_				1.5	٨
Diode Forward Current	ls		-	-	1.5	A
Diode Forward Voltage	V _{SD}	I _S =1.0A, V _{GS} =0V	-	0.8	1.2	V

NOTES :

1. Pulse width</br>

2. Essentially independent of operating temperature typical characteristics.

3. RoJA is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.

4. The maximum current rating is package limited.

5. Guaranteed by design, not subject to production testing



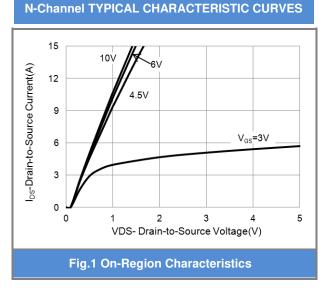
P-Channel Electrical Characteristics (T_A=25°C unless otherwise noted)

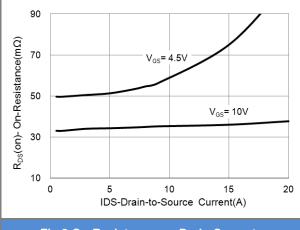
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static	1	1		1	r	1
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =-250uA	-1	-1.3	-2.1	V
Drain-Source On-State Resistance	_	V _{GS} =-10V, I _D =-2.9A	-	94	110	mΩ
	RDS(on)	V _{GS} =-4.5V, I _D =-1.9A	-	120	150	
Zero Gate Voltage Drain Current	IDSS	V _{DS} =-30V, V _{GS} =0V	-	-	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = <u>+</u> 20V, V _{DS} =0V	-	-	<u>+</u> 100	nA
Dynamic ^(Note 5)						
Total Gate Charge	Qg	V _{DS} =-15V, I _D =-2.9A, V _{GS} =-10V ^(Note 1,2)	-	9.8	-	
Gate-Source Charge	Q _{gs}		-	1.5	-	nC
Gate-Drain Charge	Q _{gd}		-	2.2	-	
Input Capacitance	Ciss	V _{DS} =-15V, V _{GS} =0V, f=1.0MHZ	-	396	-	
Output Capacitance	Coss		-	47	-	рF
Reverse Transfer Capacitance	Crss		-	36	-	
Turn-On Delay Time	td _(on)	$V_{DD}{=}{-}15V, I_{D}{=}{-}2.9A, \\ V_{GS}{=}{-}10V, \\ R_{G}{=}6\Omega^{(Note \ 1,2)}$	-	5	-	
Turn-On Rise Time	tr		-	30	-	
Turn-Off Delay Time	td _(off)		-	25	-	ns
Turn-Off Fall Time	tf		-	8	-	
Drain-Source Diode						
Maximum Continuous Drain-Source				-	-1.5	A
Diode Forward Current	ls		-			
Diode Forward Voltage	V _{SD}	I _S =-1.0A, V _{GS} =0V	-	-0.85	-1.2	V

NOTES :

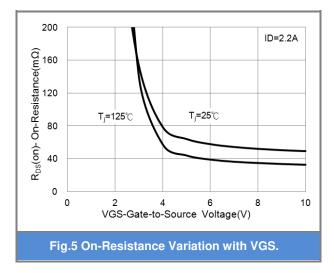
- 1. Pulse width <300us, Duty cycle <2%
- 2. Essentially independent of operating temperature typical characteristics.
- 3. RoJA is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.

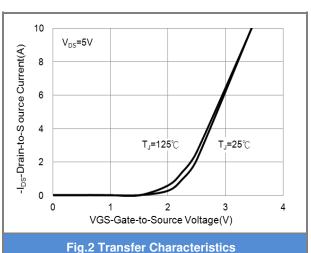


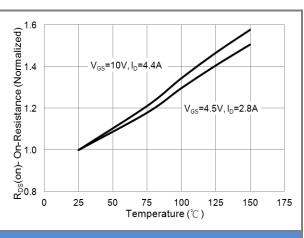


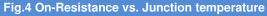


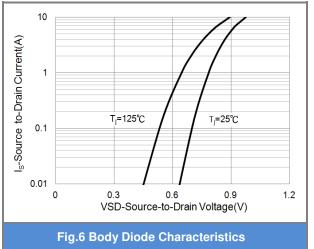














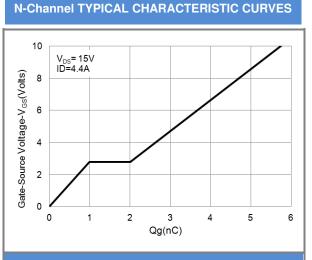


Fig.7 Gate-Charge Characteristics

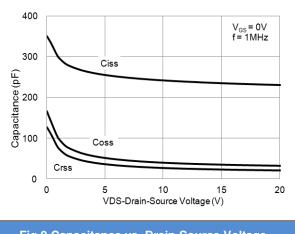
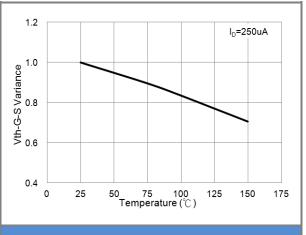
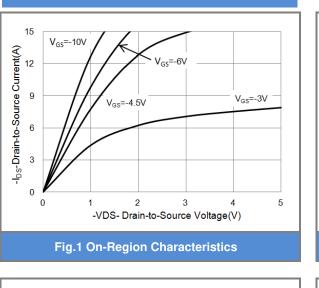


Fig.9 Capacitance vs. Drain-Source Voltage.

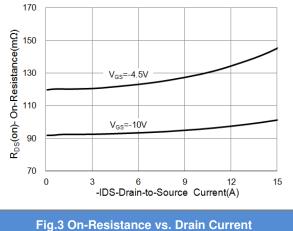


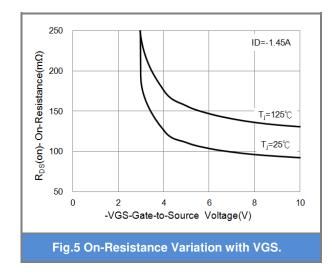






P-Channel TYPICAL CHARACTERISTIC CURVES





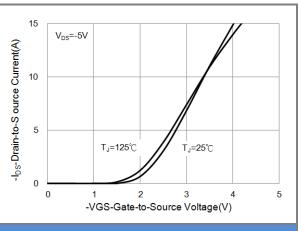


Fig.2 Transfer Characteristics

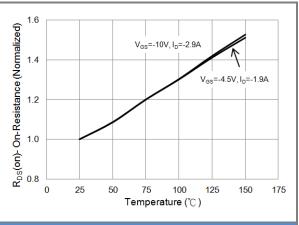
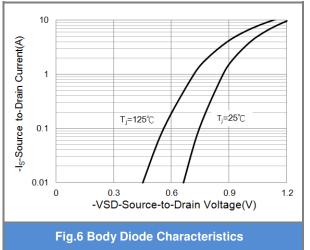
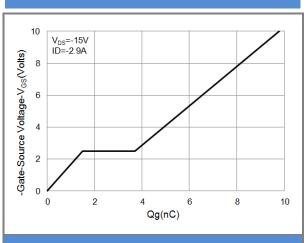


Fig.4 On-Resistance vs. Junction temperature







P-Channel TYPICAL CHARACTERISTIC CURVES

Fig.7 Gate-Charge Characteristics

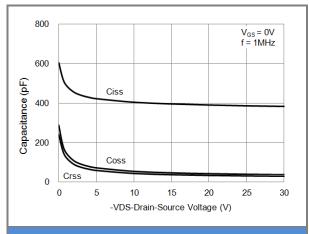
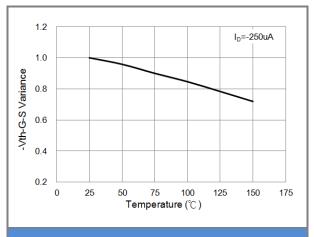


Fig.9 Threshold Voltage Variation with Temperature.



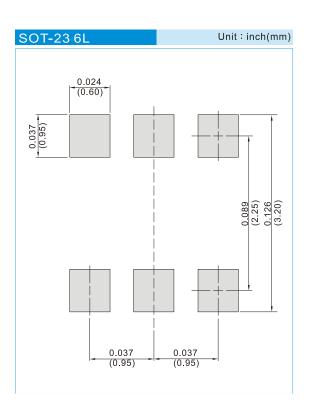




PART NO. PACKING CODE VERSION

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJS6603_S1_00001	SOT-23 6L	3K pcs / 7" reel	SC3	Halogen free RoHS compliant
PJS6603_S2_00001	SOT-23 6L	10K pcs / 13" reel	SC3	Halogen free RoHS compliant

MOUNTING PAD LAYOUT





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