TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (U-MOS V-H)

# **TPCC8001-H**

High-Efficiency DC-DC Converter Applications Notebook PC Applications Portable Equipment Applications

- · Small footprint due to a small and thin package
- · High-speed switching
- Small gate charge: Qsw = 7.1 nC (typ.)
- Low drain-source ON-resistance:

 $R_{DS (ON)} = 7.6 \text{ m}\Omega \text{ (typ.)} \text{ (V}_{GS} = 4.5 \text{ V)}$ 

- High forward transfer admittance:  $|Y_{fs}| = 65 \text{ S (typ.)}$
- Low leakage current:  $I_{DSS} = 10 \mu A (max) (V_{DS} = 30 V)$
- Enhancement mode:  $V_{th} = 1.5$  to 2.5 V ( $V_{DS} = 10$  V,  $I_D = 1$  mA)

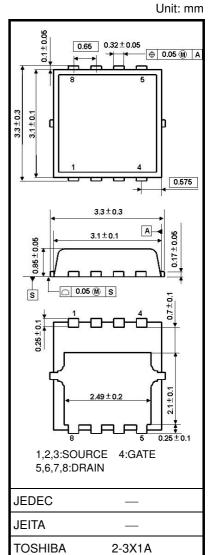
### **Absolute Maximum Ratings (Ta = 25°C)**

Characte	eristic	Symbol	Rating	Unit	
Drain-source voltage		$V_{DSS}$	30	V	
Drain-gate voltage (R	$GS = 20 \text{ k}\Omega$	$V_{DGR}$	30	٧	
Gate-source voltage		$V_{GSS}$	±20	٧	
Drain current	DC (Note 1)	I <sub>D</sub>	22	Α	
Diain current	Pulsed (Note 1)	I <sub>DP</sub>	66		
Drain power dissipation	on (Tc = 25°C)	$P_{D}$	30	W	
Drain power dissipation	on $(t = 10 s)$ (Note 2a)	$P_{D}$	1.9	W	
Drain power dissipation	on (t = 10 s) (Note 2b)	P <sub>D</sub>	0.7	w	
Single-pulse avalanch	ne energy (Note 3)	E <sub>AS</sub>	126	mJ	
Avalanche current		I <sub>AR</sub>	22	Α	
Repetitive avalanche (To	energy c = 25°C) (Note 4)	E <sub>AR</sub>	2.1	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature	range	T <sub>stg</sub>	-55 to 150	°C	

Note: For Notes 1 to 4, refer to the next page.

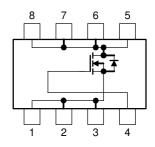
Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.



Weight: 0.02 g (typ.)

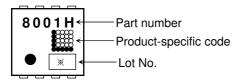
### **Circuit Configuration**



#### **Thermal Characteristics**

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case (Tc = 25°C)	R <sub>th (ch-c)</sub>	4.2	°C/W
Thermal resistance, channel to ambient $(t = 10 \text{ s})$ (Note 2a)	R <sub>th (ch-a)</sub>	66	°C/W
Thermal resistance, channel to ambient $(t = 10 \text{ s})$ (Note 2b)	R <sub>th (ch-a)</sub>	180	°C/W

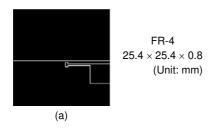
## Marking (Note 5)

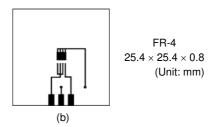


Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)





Note 3:  $V_{DD} = 24~V,~T_{Ch} = 25^{\circ}C$  (initial),  $L = 200~\mu H,~R_{G} = 25~\Omega,~I_{AR} = 22~A$ 

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: \* Weekly code: (Three digits)



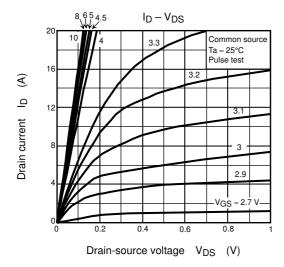
# Electrical Characteristics (Ta = 25°C)

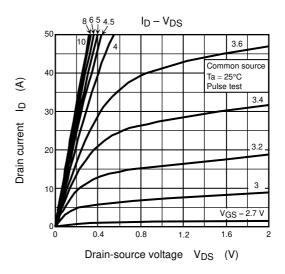
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I <sub>GSS</sub>	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA
Drain cutoff curre	nt	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V		_	10	μА
Drain-source bre	okdowa voltago	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	30 — —		_	v
Diain-source bre	akdowii voitage	V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15	_	_	v
Gate threshold vo	oltage	V <sub>th</sub>	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$	1.5	_	2.5	٧
Drain-source ON	resistance	Bro (ON)	$V_{GS} = 4.5 \text{ V}, I_D = 11 \text{ A}$		7.6	10.6	mΩ
Diain-source ON	resistance	R <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 11 A	—     —     10       30     —     —       15     —     —       1.5     —     2.5       —     7.6     10.6       —     5.5     8.3       33     65     —       —     1900     2500       —     110     170       —     400     —       —     1.0     1.5       —     2.8     —	11122		
Forward transfer	admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 11 A	33	65	_	S
Input capacitance	)	C <sub>iss</sub>		_	1900	2500	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	110	170	pF
Output capacitance		Coss		_	400	_	
Gate resistance		rg	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 5 \text{ MHz}$	_	1.0	1.5	Ω
Switching time Fall ti	Rise time	t <sub>r</sub>	ACS O A D O	_	2.8	_	- ns
	Turn-on time	t <sub>on</sub>		_	9.8	_	
	Fall time	t <sub>f</sub>		_	5.9	_	
	Turn-off time	t <sub>off</sub>	$V_{DD} \approx 15 \text{ V}$ Duty $\leq$ 1%, $t_W = 10 \mu\text{s}$	_	27	_	
Total gate charge	otal gate charge		$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 22 \text{ A}$	_	27	_	
(gate-source plus	gate-drain)	Qg	$V_{DD} \approx 24 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 22 \text{ A}$	_	_ 14.3 _		
Gate-source charge 1		Q <sub>gs1</sub>		_	6.8	_	nC
Gate-drain ("Miller") charge		Q <sub>gd</sub>	$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 22 \text{ A}$	_	4.3	_	-
Gate switch char	Gate switch charge			_	7.1	_	

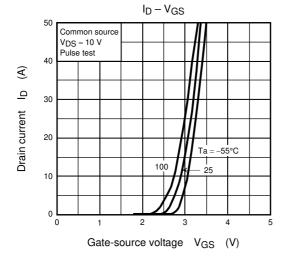
## **Source-Drain Ratings and Characteristics (Ta = 25°C)**

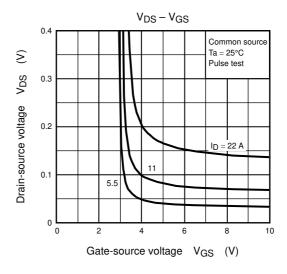
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit	
Drain reverse current	Pulse	(Note 1)	I <sub>DRP</sub>	_	_	_	66	Α
Forward voltage (diode)			$V_{DSF}$	$I_{DR} = 22 \text{ A}, V_{GS} = 0 \text{ V}$		_	-1.2	V

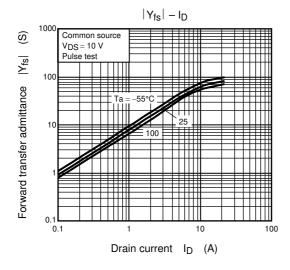
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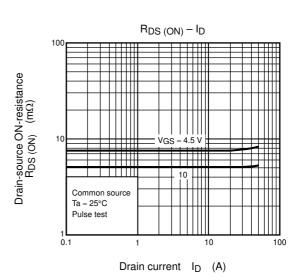




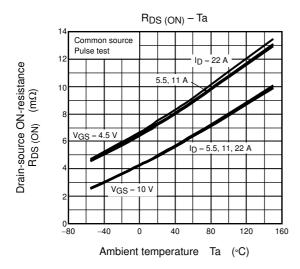


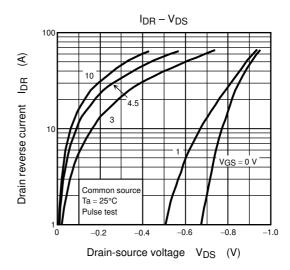


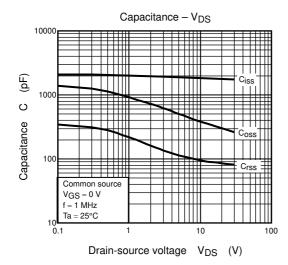


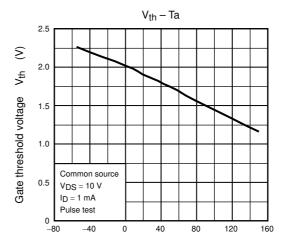


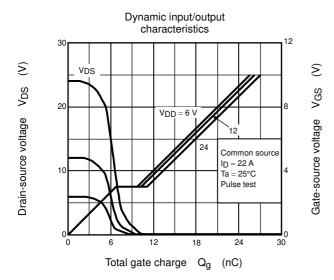
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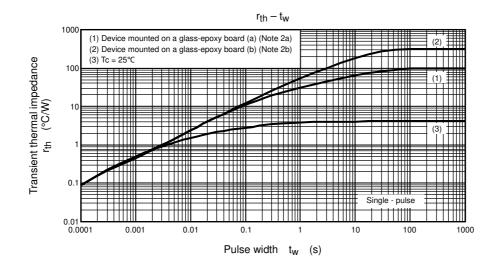


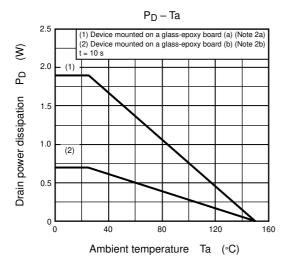


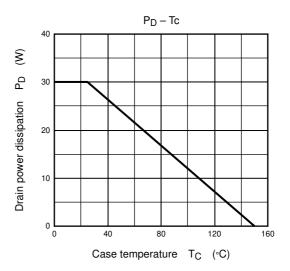


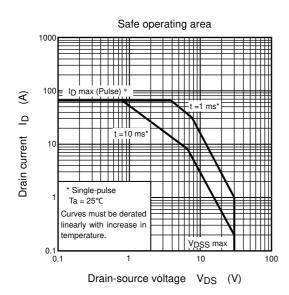












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