

Electrical Specifications @ 25 °C

Turns Ratio ± 5% Inductance	1CT*:1CT*
	E0 uH min @ 100 KH-
	50 μH min. @ 100 KHz,
	0.2 Vrms, 8 mA DC Bias
DCR	
Pin 1-3, 6-8	0.6 Ω max.
Pin 9-11, 14-16	5 1.1 Ω max.
Insertion Loss	
0.3-100 MHz	1.15 dB max.
Return Loss	
30 MHz	18 dB min.
60 MHz	12 dB min.
100 MHz	10 dB min.
Common Mode F	Rejection
60 MHz	35 dB Min.
	30 dB Min.
Crosstalk	
1-60 MHz	45 dB Min.
60-100 MHz	35 dB Min.
Hipot	
1 mA. 2 sec	1500 VAC (PRI-SEC)
	rature0 °C to +70 °C
	ture25 °C to +125 °C
clorage tempera	

*CT: Center tap

Material

TerminationT	in
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Packaging Specifications

Tape & Reel 600 pcs./reel

How To Order

F	PT61017	Ρ	E	Ļ
Model				
Construction — P = Potted				
Packaging E = Tape and Reel (600 p	cs./reel)			
Termination L = Tin only (RoHS Comp	liant)			

REV. 02/16



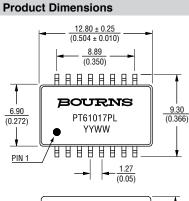
Features

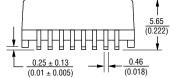
- IEEE 802.3 Ethernet compatible
- Designed for AMD IC manufacturers
- Fully integrated for adapter, hub and motherboard applications
- RoHS compliant*

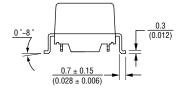
Applications

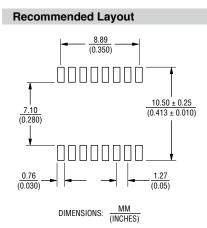
- LAN (Local Area Network)
- Ethernet

PT61017PEL LAN 10/100 Base-Tx



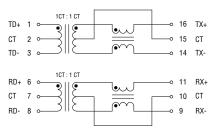




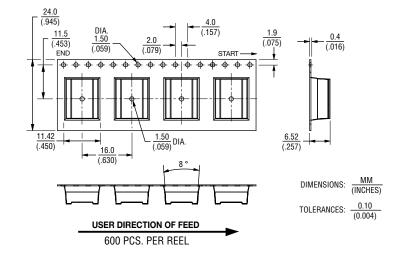


TOLERANCES: $\frac{0.05}{(0.002)}$ UNLESS OTHERWISE NOTED

Electrical Schematic



Packaging Specifications



*RoHS Directive 2002/95/EC Jan. 27, 2003 including annex and RoHS Recast 2011/65/EU June 8, 2011. Specifications are subject to change without notice. Users should verify actual device performance in their specific applications. The products described herein and this document are subject to specific legal disclaimers as set forth on the last page of this document, and at www.bourns.com/docs/legal/disclaimer.pdf.

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