

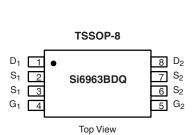
## **Dual P-Channel 2.5-V (G-S) MOSFET**

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)		
- 20	0.045 at V <sub>GS</sub> = - 4.5 V	- 3.9		
	0.080 at V <sub>GS</sub> = - 2.5 V	- 3.0		

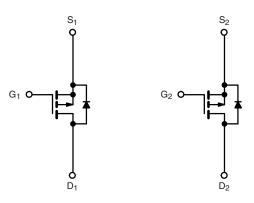
#### **FEATURES**

· Halogen-free





Ordering Information: Si6963BDQ-T1-GE3 (Lead (Pb)-free and Halogen-free)



P-Channel MOSFET

P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> T <sub>A</sub> = 25 °C, unless otherwise noted					
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	- 20		<b>V</b>
Gate-Source Voltage		V <sub>GS</sub>	± 12		
Continuous Drain Current /T 150 °C\8	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	- 3.9	- 3.4	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		- 3.1	- 2.7	Δ.
Pulsed Drain Current		I <sub>DM</sub>	- 30		Α
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	- 1.0	- 0.75	
Mariana Barray Dissipation	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	1.13	0.83	W
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		0.73	0.53	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum landing to Audienta	t ≤ 10 s	- R <sub>thJA</sub>	90	110	°C/W
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		125	150	
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	67	80	

#### Notes:

a. Surface Mounted on FR4 board.

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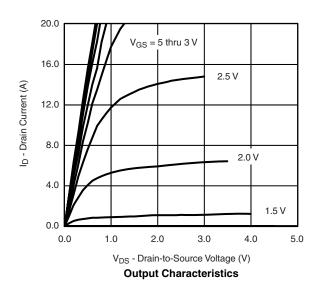
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static				•			
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 0.6		- 1.4	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			- 1	μΑ	
					- 10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 20			Α	
Drain-Source On-State Resistance <sup>a</sup>	В	$V_{GS} = -4.5 \text{ V}, I_D = -3.9 \text{ A}$	0.036 0.0		0.045		
	R <sub>DS(on)</sub>	$V_{GS} = -2.5 \text{ V}, I_D = -3.0 \text{ A}$		0.065	0.080	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 3.9 A		10		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = - 1.0 A, V <sub>GS</sub> = 0 V		- 0.71	- 1.1	٧	
Dynamic <sup>b</sup>				•			
Total Gate Charge	$Q_g$			8.6	11	nC	
Gate-Source Charge	$Q_{gs}$	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -3.9 \text{ A}$		1.2			
Gate-Drain Charge	$Q_{gd}$			2.8		1	
Gate Resistance	$R_{g}$			7.0		Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			33	50		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 10 V, $R_L$ = 10 $\Omega$		57	90		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong$ - 1 A, $V_{GEN}=$ - 4.5 V, $R_g=$ 6 $\Omega$		65	100	ns	
Fall Time	t <sub>f</sub>			40	60		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 1.0 A, dl/dt = 100 A/μs		30	50		

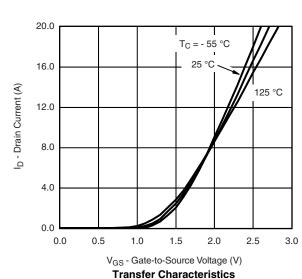
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

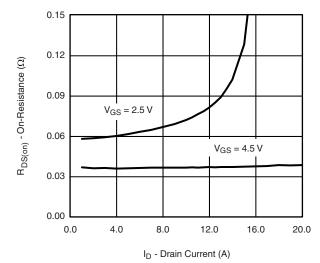
## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



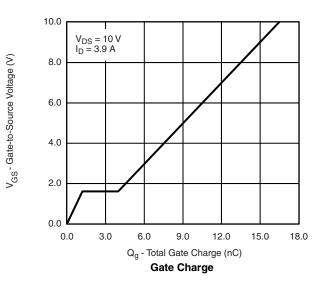




## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



On-Resistance vs. Drain Current



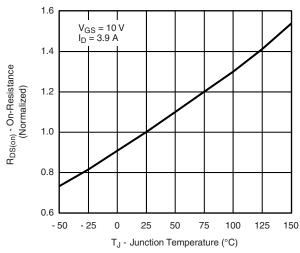
 $T_{\rm J} = 150~{\rm ^{\circ}C}$   $T_{\rm J} = 25~{\rm ^{\circ}C}$   $T_{\rm J} = 25~{\rm ^{\circ}C}$   $V_{\rm SD}$  - Source-to-Drain Voltage (V)

Source-Drain Diode Forward Voltage

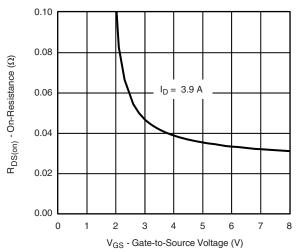
1200.0 1000.0  $C_{\text{iss}}$ C - Capacitance (pF) 800.0 600.0 400.0  $C_{oss}$ 200.0  $C_{\text{rss}}$ 0.0 0.0 4.0 8.0 12.0 20.0

V<sub>DS</sub> - Drain-to-Source Voltage (V)





On-Resistance vs. Junction Temperature



On-Resistance vs. Gate-to-Source Voltage

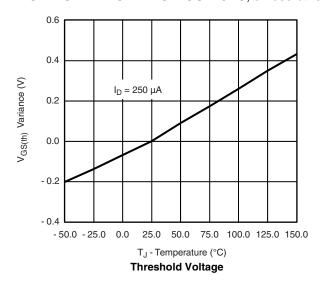
30

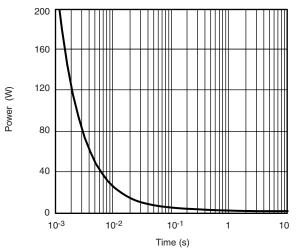
Is - Source Current (A)

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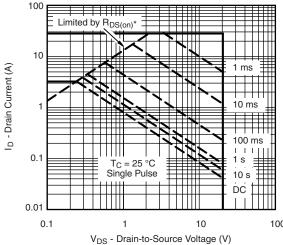
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## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



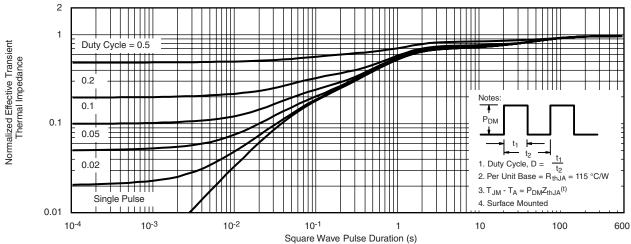


Single Pulse Power, Junction-to-Ambient



\*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

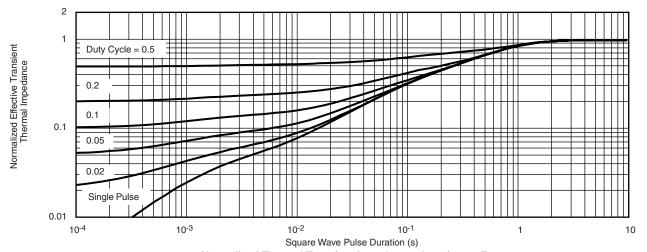
#### Safe Operating Area, Junction-to-Case



Normalized Thermal Transient Impedance, Junction-to-Ambient



## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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