

# TOSHIBA Transistor Silicon NPN Epitaxial Type

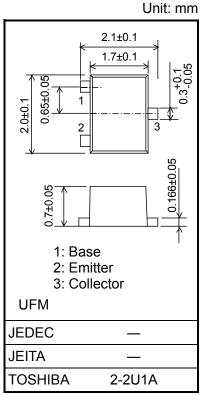
# 2SC6135

High-Speed Switching Applications DC-DC Converter Applications Strobe Applications

- High DC current gain: hFE = 400 to 1000 (IC = 0.1A)
- Low collector-emitter saturation voltage: VCE (sat) = 0.17 V (max)
- High-speed switching: tf = 85 ns (typ.)

# **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics		Symbol	Rating	Unit	
Collector-base voltage		V <sub>CBO</sub>	100	V	
Collector-emitter voltage		VCEX	80	V	
Collector-emitter voltage		VCEO	50	V	
Emitter-base voltage		V <sub>EBO</sub>	7	V	
Collector current	DC	Ic	1.0	Α	
	Pulse	ICP	2.0		
Base current		lΒ	0.1	Α	
Collector power dissipation		P <sub>C</sub> (Note 1)	800	mW	
		Pc (Note 2)	500		
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	



Weight: 6.6 mg (typ.)

- Note 1: Mounted on ceramic board.
  - $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 0.8 \text{ mm}, \text{Cu Pad: } 645 \text{ mm}^2)$
- Note 2: Mounted on FR4 board.

(25.4 mm  $\times$  25.4 mm  $\times$  1.6 mm, Cu Pad: 645 mm² )

Note 3: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

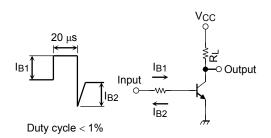
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production 2007-08



# Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I <sub>CBO</sub>	V <sub>CB</sub> = 100 V, I <sub>E</sub> = 0 A	_	_	100	nA
Emitter cut-off current		I <sub>EBO</sub>	V <sub>EB</sub> = 7 V, I <sub>C</sub> = 0 A	_	_	100	nA
Collector-emitter breakdown voltage		V (BR) CEO	I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0 A	50	_	_	V
DC current gain		h <sub>FE</sub> (1)	V <sub>CE</sub> = 2 V, I <sub>C</sub> = 0.1 A	400	_	1000	_
		h <sub>FE</sub> (2)	V <sub>CE</sub> = 2 V, I <sub>C</sub> = 0.3 A	200	_	_	
Collector-emitter saturation voltage		VCE (sat)	I <sub>C</sub> = 300 mA, I <sub>B</sub> = 6 mA	_	_	0.12	V
Base-emitter saturation voltage		V <sub>BE</sub> (sat)	I <sub>C</sub> = 300 mA, I <sub>B</sub> = 6 mA	_	_	1.10	V
Collector output capacitance		C <sub>ob</sub>	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0 A, f = 1 MHz	_	5	_	pF
Switching time	Rise time	tr	See Figure 1. $V_{CC} \approx 30 \text{ V, R}_L = 100 \ \Omega$ $I_{B1} = -I_{B2} = 10 \text{ mA}$	_	35	_	ns
	Storage time	t <sub>stg</sub>		_	680	_	
	Fall time	tf		_	85	_	



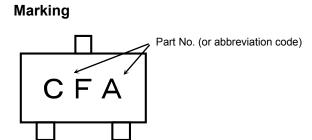
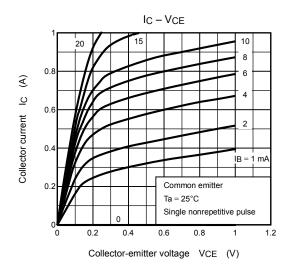
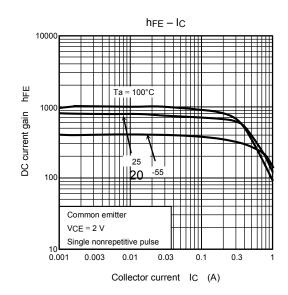
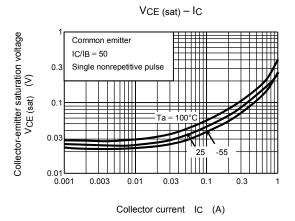


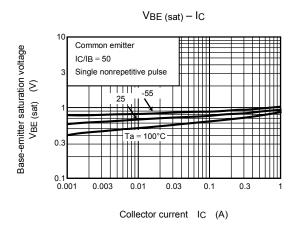
Figure 1: Switching Time Test Circuit & Timing Chart

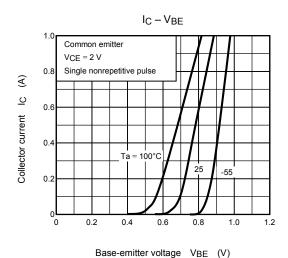














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