



Ultra Low Capacitance ESD Protection

VRWM

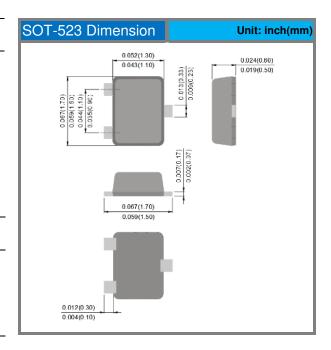
5 V

Features

- IEC61000-4-2(ESD): ±18kV Air, ±12kV Contact Compliance
- IEC61000-4-4(EFT): 40A(5/50nS)
- IEC61000-4-5(Lightning): 4A(8/20μS)
- Low leakage current, maximum 1μA at rated voltage
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 Standard



- Case: SOT-523, Plastic
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.002 grams



Applications

- USB3.0 Data Line Protection
- High Definition Multi-Media Interface Protection
- Monitors and Flat Panel Displays Notebook computers
- Video Line Protection & Base Stations
- 10/100/1000 Ethernet
- HDSL, IDSL Secondary IC Side Protection
- Control Signal Lines Protection



Fig.25(Top View)

Maximum Ratings (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS	
ESD IEC61000-4-2(Air)	V	±18	kV	
ESD IEC61000-4-2(Contact)	V _{ESD}	±12		
Operating Junction Temperature Range	TJ	-55 to +150	°C	
Storage Temperature Range	T _{STG}	-55 to +150	°C	





Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Reverse Stand-Off Voltage	V_{RWM}	-	-	1	5	V	
Reverse Breakdown Voltage	V_{BR}	I _{BR} =1mA, Between any I/O pins to GND	5.8	-	10.2	V	
Reverse leakage current	IR	V _R =5V, any I/O pin to GND	-	-	1	μΑ	
Clamping Voltage	V _{CL}	I _{PP} =1A, t _P =8/20μs, any I/O pin to GND	-	9	12		
		I _{PP} =4A, t _P =8/20μs, any I/O pin to GND	-	ı	15	V	
Clamping Voltage TLP(Note 1)	V _{CL}	I _{PP} =4A, t _P =100ns, any I/O pin to GND	-	9.6	-	V	
		IPP=8A, tP=100ns, any I/O pin to GND	-	10.6	-		
Dynamic Resistance ^(Note 1)	R _{DYN}	t _P =100ns	-	0.25	-	Ω	
Off State Junction Capacitance	Сл	0Vdc Bias f=1MHz, Between any I/O pins to GND	-	0.6	0.8	pF	

NOTES:

1. Testing using Transmission Line Pulse (TLP) conditions: $Z_0 = 50 \Omega$, $t_P = 100 \text{ ns}$.





TYPICAL CHARACTERISTIC CURVES

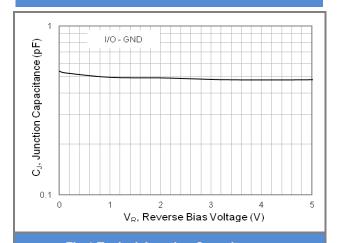


Fig.1 Typical Junction Capacitance

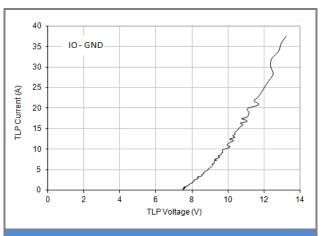


Fig.2 Transmission Line Pulsing (TLP) Measurement

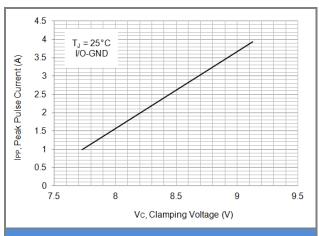


Fig.3 Typical Peak Clamping Voltage(8/20µs)

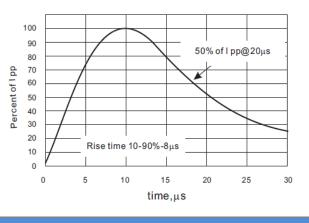


Fig.4 8/20µs Pulse Waveform

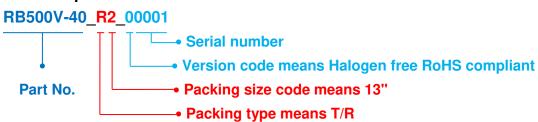




Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJE5V0U8TB_R1_00001	SOT-523	4K pcs / 7" reel	8T	Halogen free RoHS compliant

For example:

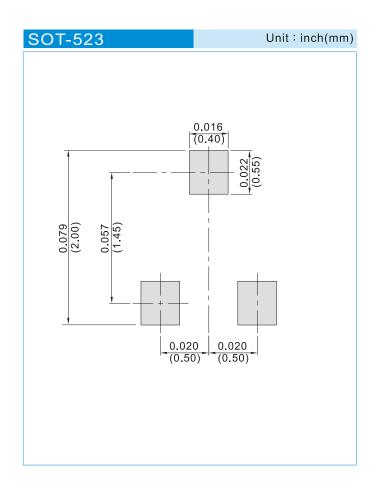


Packing Code XX			Version Code X		Serial number XXXX	
Packing type	1 st Code	Packing size code 2 nd Code		HSF Level	1 st Code	2 nd ~5 th Code
Tape and Ammunition Box(T/B)	A	N/A	0	Halogen free RoHS compliant	0	serial number
Tape and Reel (T/R)	R	7"	1	RoHS compliant	1	serial number
Bulk Packing(B/P)	В	13"	2			
Tube Packing(T/P)	Т	26mm	x			
Tape and Reel (Right Oriented) (TRR)	S	52mm	Y			
Tape and Reel (Left Oriented) (TRL)	L	PANASERT T/B CATHODE UP(PBCU)	U			
FORMING	F	PANASERT T/B CATHODE DOWN(PBCD)	D			





MOUNTING PAD LAYOUT







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