

#### 340KHZ SYNCHRONOUS DC-DC BUCK CONVERTER

#### Description

The AP3502F is a 340kHz fixed frequency, current mode, PWM synchronous buck (step-down) DC-DC converter, capable of driving a 2A load with high efficiency, excellent line and load regulation. The device integrates N-channel power MOSFET switches with low on-resistance. Current mode control provides fast transient response and cycle-by-cycle current limit.

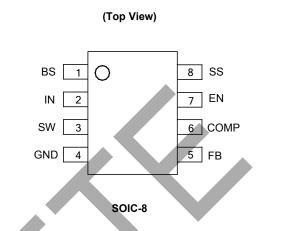
The AP3502F employs complete protection to ensure system security, including output Over Voltage Protection, input Under Voltage LockOut, programmable soft-start, and Over Temperature Protection.

This IC is available in SOIC-8 package.

#### Features

- Input Voltage Range: 4.5V to 18V
- Fixed 340kHz Frequency
- High Efficiency: up to 95%
- Output Current: 2A
- Current Mode Control
- Built-In Over Current Protection
- Built-In Thermal Shutdown Function
- Built-In UVLO Function
- Built-In Over Voltage Protection
- Programmable Soft-start

#### **Pin Assignments**

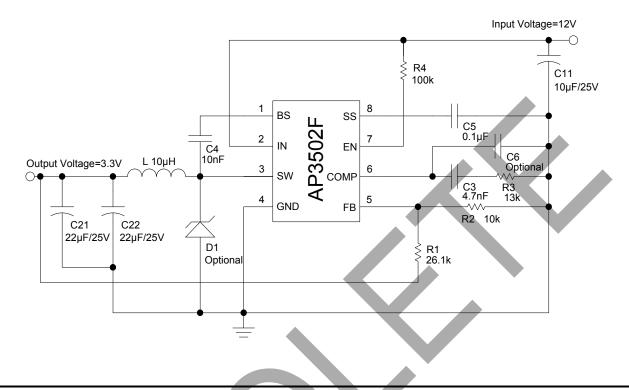


### Applications

- LCD TV
- Set Top Box
- Portable DVD
- Digital Photo Frame



# **Typical Applications Circuit**



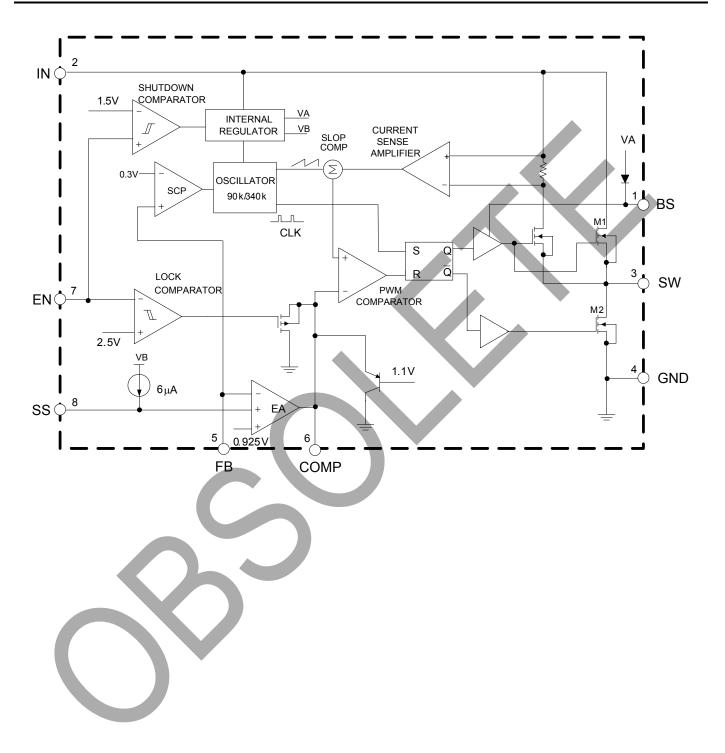
# Pin Descriptions

Pin Number	Pin Name	Function
1	BS	Bootstrap pin. A bootstrap capacitor is connected between the BS pin and SW pin. The voltage across the bootstrap capacitor drives the internal high-side power MOSFET
2	IN	Supply power input pin. A capacitor should be connected between the IN pin and GND pin to keep the input voltage constant
3	SW	Power switch output pin. This pin is connected to the inductor and bootstrap capacitor
4	GND	Ground pin
5	FB	Feedback pin. This pin is connected to an external resistor divider to program the system output voltage. When the FB pin voltage exceeds 1.1V, the over voltage protection is triggered. When the FB pin voltage is below 0.3V, the oscillator frequency is lowered to realize short circuit protection
6	СОМР	Compensation pin. This pin is the output of the transconductance error amplifier and the input to the current comparator. It is used to compensate the control loop. Connect a series RC network from this pin to GND. In some cases, an additional capacitor from this pin to GND pin is required
7	EN	Control input pin. EN is a digital input that turns the regulator on or off. Drive EN high/low to turn on/off the regulator. Pull up with $100k\Omega$ resistor for automatic startup
8	SS	Soft-start control input pin. SS controls the soft-start period. Connect a capacitor from SS to GND to set the soft-start period. A $0.1\mu$ F capacitor sets the soft-start period to 15ms. To disable the soft-start feature, leave SS unconnected



AP3502F

# **Functional Block Diagram**





### Absolute Maximum Ratings (Note 1)

Symbol	Parameter	Value	Unit
V <sub>IN</sub>	IN Pin Voltage	-0.3 to 20	V
V <sub>EN</sub>	EN Pin Voltage	-0.3 to V <sub>IN</sub>	V
V <sub>SW</sub>	SW Pin Voltage	21	V
V <sub>BS</sub>	BS Pin Voltage	-0.3 to V <sub>SW</sub> +6	V
V <sub>FB</sub>	FB Pin Voltage	-0.3 to 6	V
V <sub>COMP</sub>	COMP Pin Voltage	-0.3 to 6	V
V <sub>SS</sub>	SS Pin Voltage	-0.3 to 6	V
$\theta_{JA}$	Thermal Resistance	105	°C/W
TJ	Operating Junction Temperature	+150	°C
T <sub>STG</sub>	Storage Temperature	-65 to +150	℃
$T_{LEAD}$	Lead Temperature (Soldering, 10sec)	+260	°C
V <sub>HBM</sub>	ESD (Human Body Model)	2000	V
V <sub>MM</sub>	ESD (Machine Model)	200	V

Note 1: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

# **Recommended Operating Conditions**

Symbol Parameter		Min	Мах	Unit	
V <sub>IN</sub>	Input Voltage	4.5	18	V	
T <sub>A</sub>	Operating Ambient Temperature	-40	+85	°C	



# Electrical Characteristics (T<sub>A</sub>=+25°C, V<sub>IN</sub>=V<sub>EN</sub>=12V, V<sub>OUT</sub>=3.3V, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
SUPPLY VOLT	AGE (IN PIN)					1
V <sub>IN</sub>	Input Voltage	_	4.5	_	18	V
Ιq	Quiescent Current	V <sub>FB</sub> =1V,V <sub>EN</sub> =3V	_	1.2	1.4	mA
I <sub>SHDN</sub>	Shutdown Supply Current	V <sub>EN</sub> =0V	_	0.1	10	μA
UNDER VOLT	AGE LOCKOUT		L			
V <sub>UVLO</sub>	Input UVLO Threshold	V <sub>IN</sub> Rising	3.65	4.00	4.25	V
V <sub>HYS</sub>	Input UVLO Hysteresis	_	-	200	-	mV
ENABLE (EN F	PIN)					
_	EN Shutdown Threshold Voltage	_	1.1	1.5	2	V
_	EN Shutdown Threshold Voltage Hysteresis (Note 2)	-	_	350	_	mV
_	EN Lockout Threshold Voltage	-	2.2	2.5	2.7	V
_	EN Lockout Hysteresis	-	-	210	_	mV
VOLTAGE REF	ERENCE (FB PIN)					
V <sub>FB</sub>	Feedback Voltage	-	0.907	0.925	0.943	V
V <sub>FBOV</sub>	Feedback Over Voltage Threshold	-	-	1.1	_	V
I <sub>FB</sub>	Feedback Bias Current	V <sub>FB</sub> =1V	-0.1	-	0.1	μA
MOSFET						
R <sub>DSONH</sub>	High-side Switch On-resistance(Note 3)	I <sub>SW</sub> =0.2A/0.7A	_	100	_	mΩ
R <sub>DSONL</sub>	Low-side Switch On-resistance(Note 3)	I <sub>SW</sub> =-0.2A/-0.7A	_	100	_	mΩ
CURRENT LIM	п					
I <sub>LEAKH</sub>	High-side Switch Leakage Current	V <sub>IN</sub> =18V,V <sub>EN</sub> =V <sub>SW</sub> =0V	_	0.1	10	μA
I <sub>LIMH</sub>	High-side Switch Current Limit	_	2.7	3.5	_	А
I <sub>LIML</sub>	Low-side Switch Current Limit	From Drain to Source	_	1.4	_	А
SWITCHING R	EGULATOR					
f <sub>osc1</sub>	Oscillator Frequency	_	280	340	400	kHz
f <sub>OSC2</sub>	Short Circuit Oscillator Frequency	_	_	90	_	kHz
D <sub>MAX</sub>	Max. Duty Cycle	V <sub>FB</sub> =0.85V	_	90	-	%
D <sub>MIN</sub>	Min. Duty Cycle	V <sub>FB</sub> =1V	_	_	0	%



# Electrical Characteristics (Cont. T<sub>A</sub>=+25°C, V<sub>IN</sub>=V<sub>EN</sub>=12V, V<sub>OUT</sub>=3.3V, unless otherwise specified.)

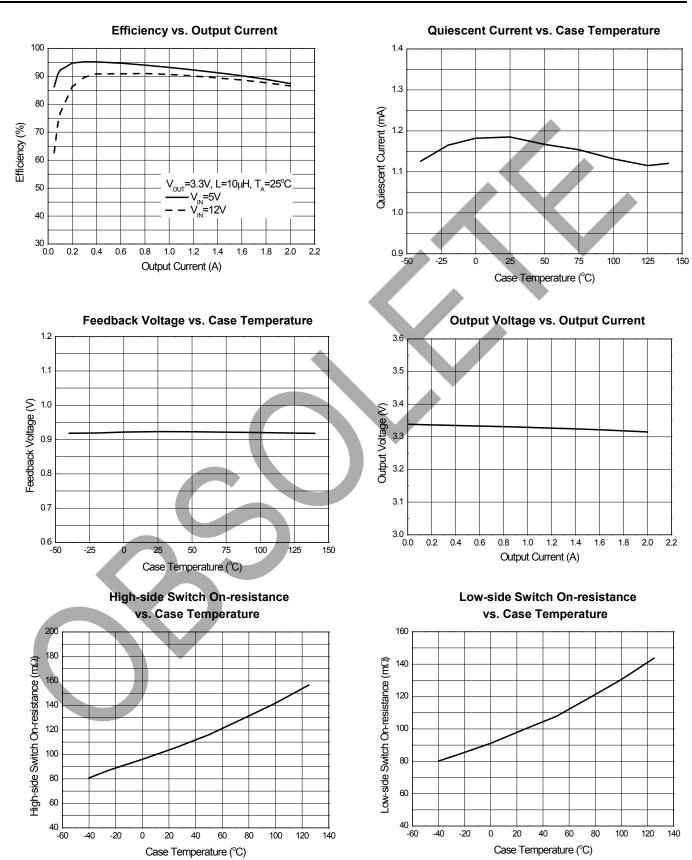
Symbol	Parameter	Conditio	ns	Min	Тур	Мах	Unit
ERROR AMPL	lFIER	-					1
$A_{EA}$	Error Amplifier Voltage Gain (Note 2)	_		_	400	_	V/V
G <sub>EA</sub>	Error Amplifier Transconductance	_		_	800	_	μΑ/ν
G <sub>cs</sub>	COMP to Current Sense Transconductance	_		_	3.5	_	A/V
THERMAL SH	IUTDOWN						
T <sub>OTSD</sub>	Thermal Shutdown (Note 2)	_		-	+160	_	°C
T <sub>HYS</sub>	Thermal Shutdown Hysteresis (Note 2)	_		-	+30	-	°C
SOFT START	(SS PIN)	·					
t <sub>ss</sub>	Soft-start Time (Note 2)	C <sub>ss</sub> =0.1µF		-	15	-	ms
_	Soft-start Current	V <sub>SS</sub> =0V		_	6	_	μA

Notes: 2. Not tested, guaranteed by design.

3. 
$$R_{\text{DSON}} = \frac{V_{\text{SW1}} - V_{\text{SW2}}}{I_{\text{SW1}} - I_{\text{SW2}}}$$

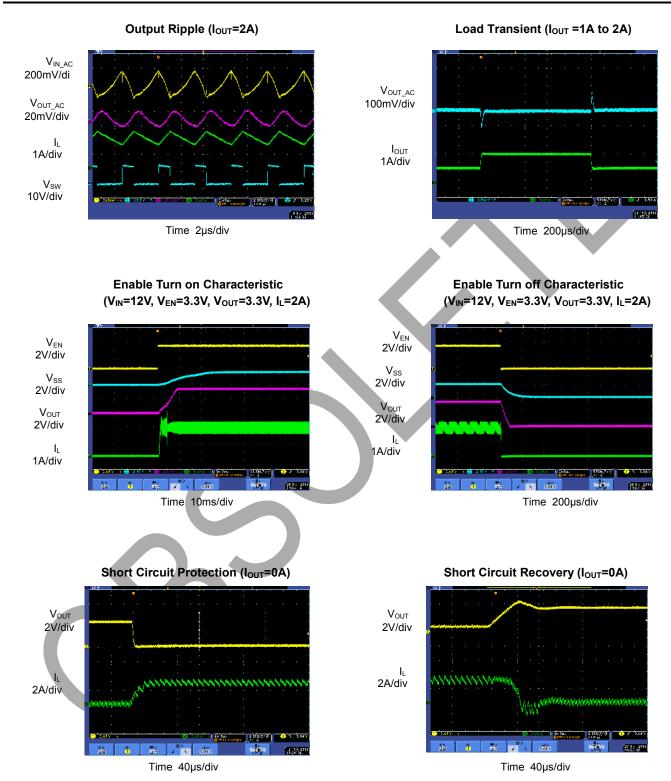


# Performance Characteristics (T<sub>A</sub>=+25°C, V<sub>IN</sub>=12V, V<sub>OUT</sub>=3.3V, unless otherwise noted.)



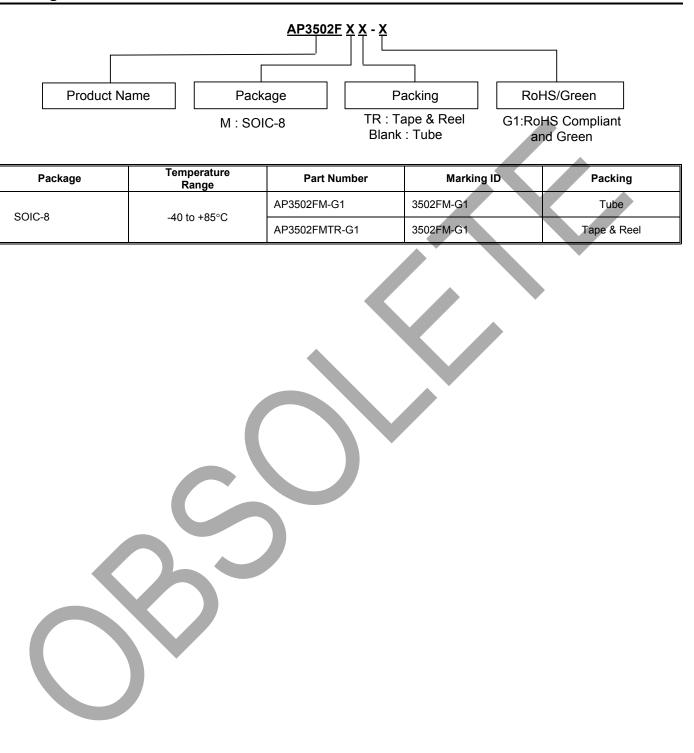


### Performance Characteristics (Cont. T<sub>A</sub>=+25°C, V<sub>IN</sub>=12V, V<sub>OUT</sub>=3.3V, unless otherwise noted.)





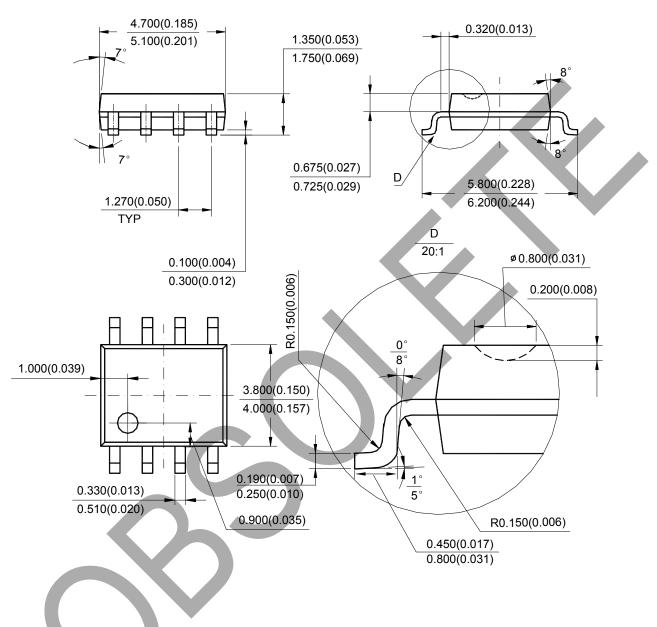
## **Ordering Information**





#### Package Outline Dimensions (All dimensions in mm(inch).)

#### (1) Package Type: SOIC-8



Note: Eject hole, oriented hole and mold mark is optional.



#### IMPORTANT NOTICE

1. DIODES INCORPORATED AND ITS SUBSIDIARIES ("DIODES") MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

2. The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes products. Diodes products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of the Diodes products for their intended applications, (c) ensuring their applications, which incorporate Diodes products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.

3. Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and liabilities.

4. Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.

Diodes products are provided subject Diodes' Standard Terms and Conditions of Sale 5 to (https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.

6. Diodes products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.

7. While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing, Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.

8. Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.

Copyright © 2021 Diodes Incorporated

www.diodes.com