



Product Summary

BV _{DSS}	R _{DS(ON)}	Ι _D T _C = +25°C
40V	$3m\Omega @V_{GS} = 10V$	205A

Description and Applications

This new generation MOSFET features low on-resistance and fast switching, making it ideal for high-efficiency power management applications.

- Motor Control
- Backlighting
- DC-DC Converters
- Power Management Functions

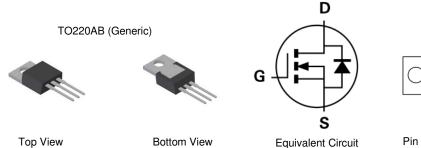
40V N-CHANNEL ENHANCEMENT MODE MOSFET

Features

- 100% Unclamped Inductive Switching (UIS) Test in Production Ensures More Reliable and Robust End Application
- Low Input Capacitance
- High BV_{DSS} Rating for Power Application
- Low Input/Output Leakage
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

- Case: TO220AB
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Terminals: Matte Tin Finish Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @3
- Terminal Connections: See Diagram Below
- Weight: 2.24 grams (Approximate)





Top View Pin Out Configuration

Ordering Information (Note 4)

Part Number	Case	Packaging
DMT4003SCT	TO220AB (Generic)	50 Pieces/Tube

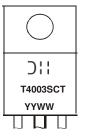
EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information

Notes:





Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	40	V	
Gate-Source Voltage	V _{GSS}	±20	V	
Continuous Drain Current (Note 5)	$T_{C} = +25^{\circ}C$ $T_{C} = +70^{\circ}C$	ID	205 164	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I _{DM}	350	A
Maximum Continuous Body Diode Forward Current (Note 5)	T _C = +25°C	ls	100	A
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)	I _{SM}	350	A	
Avalanche Current (Note 6), L = 0.1mH	I _{AS}	65.7	A	
Avalanche Energy (Note 6), L = 0.1mH	E _{AS}	215	mJ	

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	156	W
Thermal Resistance, Junction to Case (Note 5)	R _{eJC}	0.8	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	40	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μA	$V_{DS} = 32V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}		-	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	2	2.5	4	V	$V_{DS}=V_{GS},I_{D}=250\mu A$	
Static Drain-Source On-Resistance	R _{DS(ON)}		2.4	3	mΩ	$V_{GS} = 10V, I_D = 90A$	
Diode Forward Voltage	V _{SD}	_	0.8	1.2	V	$V_{GS} = 0V, I_{S} = 20A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	6865			$\label{eq:VDS} \begin{array}{l} V_{DS} = 20V, \ V_{GS} = 0V, \\ f = 1MHz \end{array}$	
Output Capacitance	Coss		1898	_	pF		
Reverse Transfer Capacitance	C _{rss}	-	21.4	_			
Gate Resistance	R _G	_	1.15	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge	Q _G	_	75.6	_		$\label{eq:VDD} \begin{split} V_{DD} &= 20V, \ I_D = 90A, \\ V_{GS} &= 10V \end{split}$	
Gate-Source Charge	Q _{GS}	_	23.8	_	nC		
Gate-Drain Charge	Q _{GD}	_	11.3	_			
Turn-On Delay Time	t _{D(ON)}		13.4			V_{DD} = 20V, V_{GS} = 10V, I_D = 90A, R_G = 3.5 Ω	
Turn-On Rise Time	t _R	_	41.2				
Turn-Off Delay Time	t _{D(OFF)}		34.4		ns		
Turn-Off Fall Time	tF	—	15.8				
Reverse Recovery Time	t _{RR}	_	59.4	_	ns		
Reverse Recovery Charge	Q _{RR}		102	_	nC	−I _F = 50A, di/dt = 100A/μs	

Notes: 5. Device mounted on an infinite heatsink.

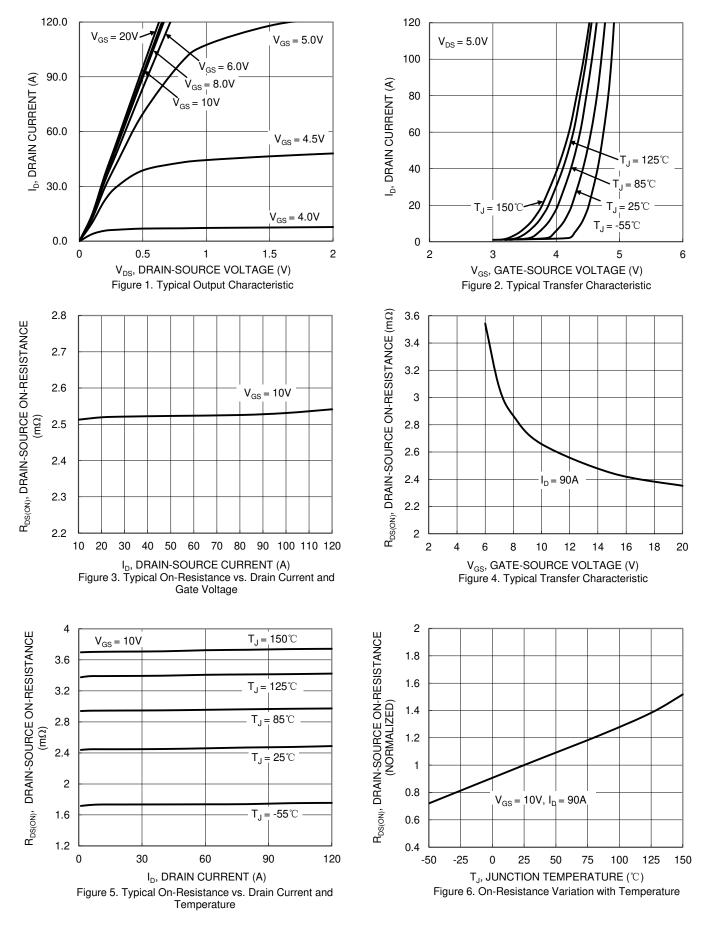
6. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.

7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.

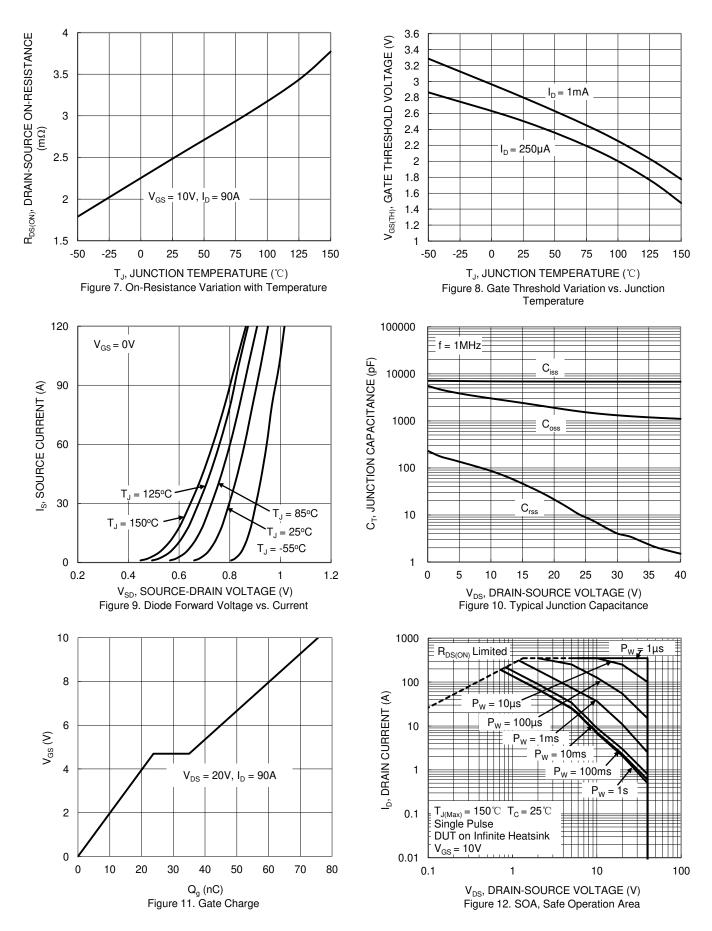


DMT4003SCT



DMT4003SCT Document number: DS40104 Rev. 4 - 2





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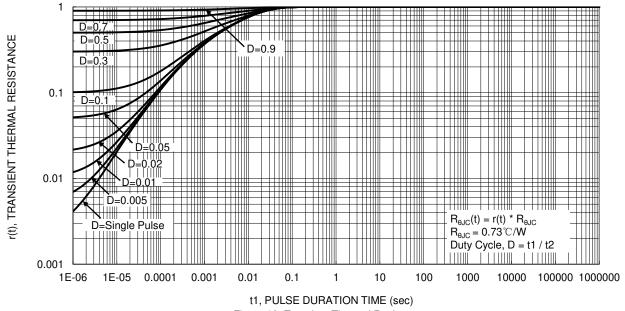


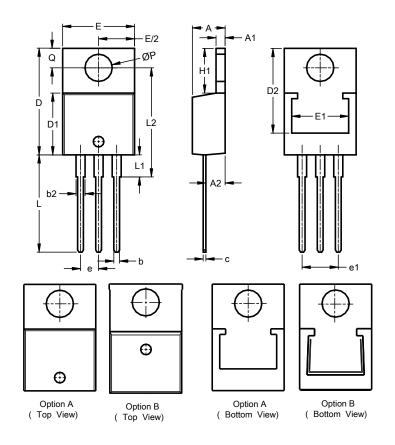
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO220AB (Generic)



TO220AB (Generic)					
Dim	Min	Max	Тур		
Α	3.56	4.82	-		
A1	0.51	1.39	-		
A2	2.04	2.92	-		
ط	0.39	1.01	0.81		
b2	1.15	1.77	1.24		
С	0.356	0.61	-		
D	14.22	16.51	-		
D1	8.39	9.01	-		
D2	11.45	12.87	-		
e	-	-	2.54		
e1	-	-	5.08		
Е	9.66	10.66	-		
E1	6.86	8.89	-		
H1	5.85	6.85	-		
L	12.70	14.73	-		
L1	-	4.42	-		
L2	15.80	17.51	16.00		
Ρ	3.54	4.08	-		
Q	2.54	3.42	-		
AIL	All Dimensions in mm				



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