

## 2SC5509

NPN SILICON RF TRANSISTOR

FOR MEDIUM OUTPUT POWER, LOW-NOISE, HIGH-GAIN AMPLIFICATION FLAT-LEAD 4-PIN THIN-TYPE SUPER MINIMOLD (M04)

R09DS0056EJ0300 Rev.3.00 Mar 5, 2013

### **FEATURES**

- Ideal for medium output power amplification
- NF = 1.2 dB TYP.,  $G_a$  = 12 dB TYP. @  $V_{CE}$  = 2 V,  $I_C$  = 10 mA, f = 2 GHz
- Maximum available power gain: MAG = 14 dB TYP.  $@V_{CE} = 2 \text{ V}, I_C = 50 \text{ mA}, f = 2 \text{ GHz}$
- $f_T = 25$  GHz technology adopted
- Flat-lead 4-pin thin-type super minimold (M04) package

### <R> ORDERING INFORMATION

Part Number	Order Number	Quantity	Package	Supplying Form
2SC5509	2SC5509-A	50 pcs (Non reel)	Flat-lead 4-pin	8 mm wide embossed taping
			thin-type super	Pin 1 (Emitter), Pin 2 (Collector) face
2SC5509-T2	2SC5509-T2-A	3 kpcs/reel	minimold (M04) (Pb-Free)	the perforation side of the tape

Remark To order evaluation samples, please contact your nearby sales office.

The unit sample quantity is 50 pcs.

### ABSOLUTE MAXIMUM RATINGS ( $T_C = 25^{\circ}C$ )

Parameter	Symbol	Ratings	Unit
Collector to Base Voltage	V <sub>CBO</sub>	15	V
Collector to Emitter Voltage	V <sub>CEO</sub>	3.3	V
Emitter to Base Voltage	V <sub>EBO</sub>	1.5	V
Collector Current	I <sub>C</sub>	100	mA
Total Power Dissipation	P <sub>tot</sub> Note	190	mW
Junction Temperature	Tj	150	°C
Storage Temperature	T <sub>stg</sub>	-65 to +150	°C

Note Free air.

### THERMAL RESISTANCE

Parameter	Symbol	Ratings	Unit
Junction to Case Resistance	R <sub>th j-c</sub>	95	°C /W
Junction to Ambient Resistance	R <sub>th j-a</sub>	650	°C /W

#### **CAUTION**

Observe precautions when handling because these devices are sensitive to electrostatic discharge.

The mark <R> shows major revised points.

The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.



## ELECTRICAL CHARACTERISTICS $(T_A = +25 \text{ °C})$

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
DC Characteristics						
Collector Cut-off Current	I <sub>CBO</sub>	V <sub>CB</sub> = 5 V, I <sub>E</sub> = 0	_	_	600	nA
Emitter Cut-off Current	I <sub>EBO</sub>	V <sub>EB</sub> = 1 V, I <sub>C</sub> = 0	_	_	600	nA
DC Current Gain	h <sub>FE</sub> Note 1	V <sub>CE</sub> = 2 V, I <sub>C</sub> = 10 mA	50	70	100	_
RF Characteristics						
Gain Bandwidth Product	f <sub>T</sub>	V <sub>CE</sub> = 3 V, I <sub>C</sub> = 90 mA, f = 2 GHz	13	15	_	GHz
Insertion Power Gain	$ S_{21e} ^2$	V <sub>CE</sub> = 2 V, I <sub>C</sub> = 50 mA, f = 2 GHz	8	11	_	dB
Noise Figure	NF	$V_{CE} = 2 \text{ V}, I_{C} = 10 \text{ mA}, f = 2 \text{ GHz},$	_	1.2	1.7	dB
		$Z_{S} = Z_{opt}$				
Reverse Transfer Capacitance	C <sub>re</sub> Note 2	$V_{CB} = 2 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$	_	0.5	0.75	pF
Maximum Available Power Gain	MAG Note 3	$V_{CE} = 2 \text{ V}, I_{C} = 50 \text{ mA}, f = 2 \text{ GHz}$	-	14	_	dB
Maximum Stable Power Gain	MSG Note 4	V <sub>CE</sub> = 2 V, I <sub>C</sub> = 50 mA, f = 2 GHz	_	15	_	dB
Gain 1 dB Compression Output	P <sub>O (1 dB)</sub>	$V_{CE} = 2 \text{ V}, I_{C} = 70 \text{ mA}^{\text{Note 5}}, f = 2 \text{ GHz}$	_	17	_	dBm
Power						
3rd Order Intermodulation	OIP <sub>3</sub>	$V_{CE} = 2 \text{ V}, I_{C} = 70 \text{ mA}^{\text{Note 5}}, f = 2 \text{ GHz}$	_	27	_	dBm
Distortion Output Intercept Point						

**Notes 1.** Pulse measurement: PW  $\leq$  350  $\mu$ s, Duty Cycle  $\leq$  2%

2. Collector to base capacitance when the emitter grounded

3. MAG = 
$$\left| \frac{S_{21}}{S_{12}} \right| (K - \sqrt{(K^2 - 1)})$$

**4.** MSG = 
$$\frac{S_{21}}{S_{12}}$$

5. Collector current when  $P_{O\,(1\,dB)}$  is output

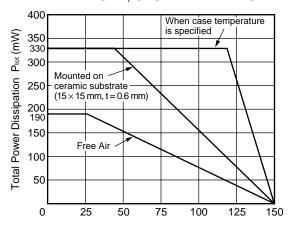
## **h**<sub>FE</sub> CLASSIFICATION

Rank	FB/YFB
Marking	T80
h <sub>FE</sub> Value	50 to 100

### TYPICAL CHARACTERISTICS (T<sub>A</sub> = +25°C, unless otherwise specified)

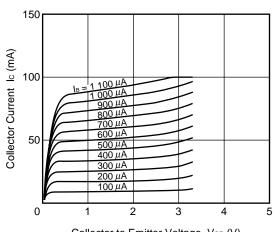
#### Thermal/DC Characteristics

### TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE. CASE TEMPERATURE



Ambient Temperature TA (°C), Case Temperature Tc (°C)

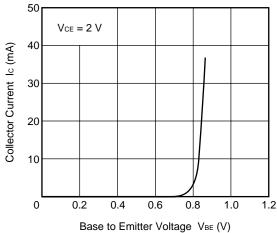
### COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE



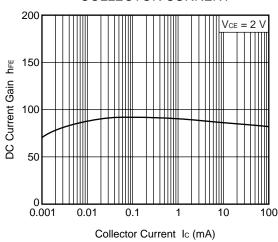
Collector to Emitter Voltage VcE (V)

## BASE TO EMITTER VOLTAGE 50

COLLECTOR CURRENT vs.

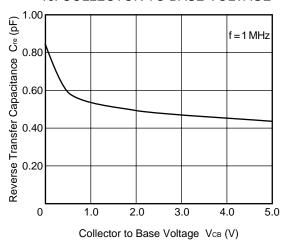






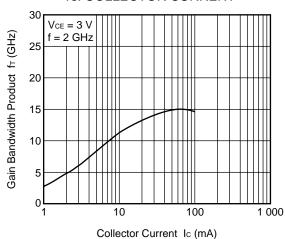
### Capacitance/f<sub>T</sub> Characteristics

### REVERSE TRANSFER CAPACITANCE vs. COLLECTOR TO BASE VOLTAGE

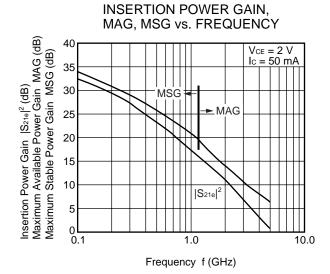


Remark The graphs indicate nominal characteristics.

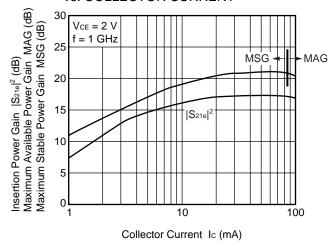
### **GAIN BANDWIDTH PRODUCT** vs. COLLECTOR CURRENT



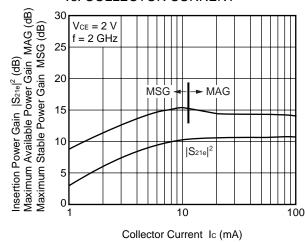
### **Gain Characteristics**



## INSERTION POWER GAIN, MAG, MSG vs. COLLECTOR CURRENT

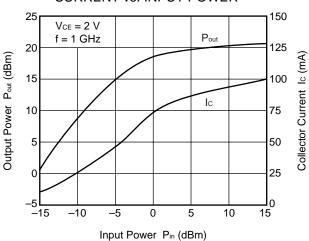


## INSERTION POWER GAIN, MAG, MSG vs. COLLECTOR CURRENT



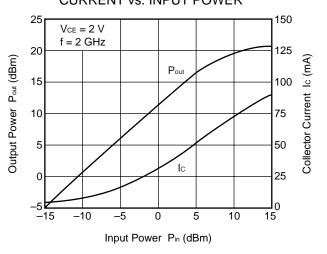
### **Output Characteristics**

# OUTPUT POWER, COLLECTOR CURRENT vs. INPUT POWER

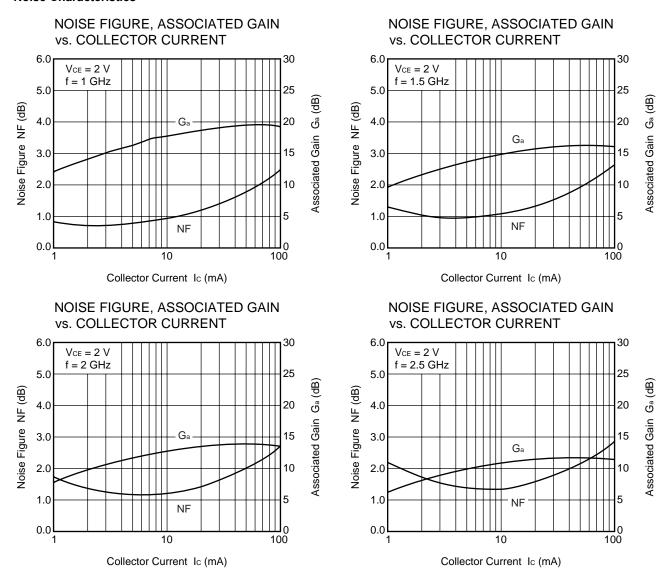


Remark The graphs indicate nominal characteristics.

# OUTPUT POWER, COLLECTOR CURRENT vs. INPUT POWER



### **Noise Characteristics**



Remark The graphs indicate nominal characteristics.

### <R> S-PARAMETERS

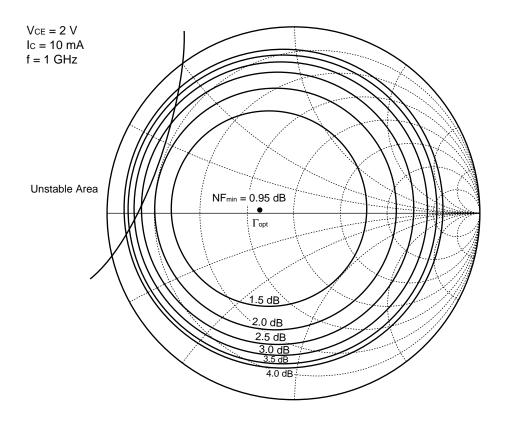
S-parameters and noise parameters are provided on our web site in a form (S2P) that enables direct import of the parameters to microwave circuit simulators without the need for keyboard inputs.

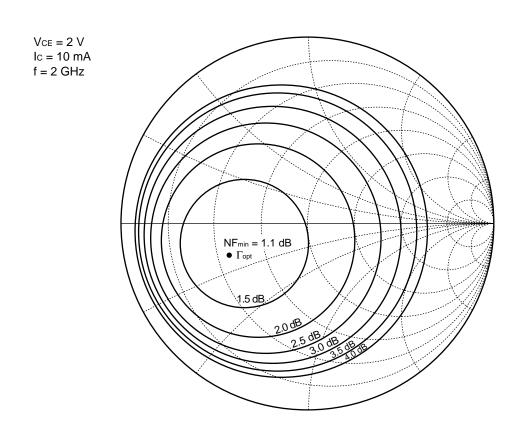
Click here to download S-parameters.

 $[\mathsf{Products}] \to [\mathsf{RF}\ \mathsf{Devices}] \to [\mathsf{Device}\ \mathsf{Parameters}]$ 

URL http://www.renesas.com/products/microwave/

## **EQUAL NF CIRCLE**





## **NOISE PARAMETERS**

 $V_{CE}$  = 2 V,  $I_{C}$  = 5 mA

f	NF <sub>min</sub>	Ga	$\Gamma_{opt}$		Rn/50
(GHz)	(dB)	(dB)	MAG.	ANG.	KII/50
8.0	0.70	18.0	0.17	93.0	0.11
0.9	0.74	17.0	0.18	103.0	0.11
1.0	0.78	16.2	0.20	112.7	0.11
1.5	0.98	13.6	0.32	155.4	0.09
1.8	1.10	12.5	0.40	176.2	0.07
1.9	1.14	12.2	0.43	-177.8	0.06
2.0	1.18	11.8	0.46	-172.2	0.06
2.5	1.39	9.9	0.56	-151.8	0.08

 $V_{CE}$  = 2 V,  $I_{C}$  = 10 mA

f	NF <sub>min</sub>	Ga	$\Gamma_{opt}$		Rn/50
(GHz)	(dB)	(dB)	MAG.	ANG.	Kill/30
8.0	0.87	19.6	0.13	170.3	0.09
0.9	0.90	18.6	0.15	171.5	0.09
1.0	0.93	17.8	0.17	173.0	0.09
1.5	1.07	14.8	0.30	-174.1	0.08
1.8	1.15	13.6	0.39	-164.1	0.07
1.9	1.18	13.2	0.41	-160.6	0.07
2.0	1.20	12.8	0.44	-157.2	0.07
2.5	1.35	10.9	0.53	-142.3	0.10

 $V_{CE}$  = 2 V,  $I_{C}$  = 20 mA

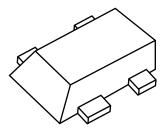
f	NF <sub>min</sub>	Ga	$\Gamma_{opt}$		Rn/50
(GHz)	(dB)	(dB)	MAG.	ANG.	KII/50
8.0	1.12	20.7	0.30	-164.8	0.08
0.9	1.15	19.7	0.31	-162.7	0.09
1.0	1.18	18.8	0.32	-160.7	0.09
1.5	1.31	15.7	0.39	-151.5	0.10
1.8	1.38	14.4	0.45	-146.3	0.10
1.9	1.41	14.0	0.47	-144.6	0.10
2.0	1.43	13.6	0.49	-142.9	0.11
2.5	1.56	11.5	0.56	-133.5	0.14

 $V_{CE}$  = 2 V,  $I_{C}$  = 50 mA

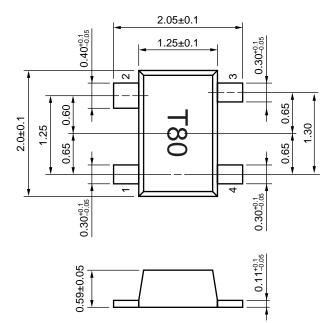
f	$NF_{min}$	Ga	$\Gamma_{opt}$		Rn/50
(GHz)	(dB)	(dB)	MAG.	ANG.	1311/30
8.0	1.75	21.3	0.49	-159.4	0.10
0.9	1.78	20.3	0.49	-157.2	0.10
1.0	1.80	19.4	0.50	-154.9	0.11
1.5	1.92	16.2	0.55	-144.7	0.14
1.8	2.00	14.8	0.59	-139.1	0.17
1.9	2.02	14.4	0.60	-137.3	0.19
2.0	2.04	13.9	0.61	-135.5	0.20
2.5	2.17	11.8	0.65	-126.4	0.28

### <R> PACKAGE DIMENSIONS

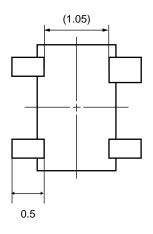
### FLAT-LEAD 4-PIN THIN-TYPE SUPER MINIMOLD (M04) PACKAGE (UNIT: mm)



(Top View)



(Bottom View)



### PIN CONNECTIONS

- 1. Emitter
- 2. Collector
- 3. Emitter
- 4. Base

**Revision History** 

## 2SC5509 Data Sheet

		Description				
Rev.	Date	Page	Summary			
1.00	Sep 9, 2004	-	First edition issued			
3.00	Mar 5, 2013	Throughout	Renesas format is applied to this data sheet.			
		p.1	p.1 ORDERING INFORMATION is modified.			
		p.5	Up to date S-PARAMETERS.			
		p.8	Added a drawing backside to PACKAGE DIMENSIONS.			

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California Eastern Laboratories, Inc. 4590 Patrick Henry Drive, Santa Clara, California 95054, U.S.A. Tel: +1-408-919-2500, Fax: +1-408-988-0279

Renesas Electronics Europe Limited
Dukes Meadow, Milliboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K
Tel: +44-1628-651-700, Fax: +44-1628-651-804 Renesas Electronics Europe GmbH

Arcadiastrasse 10, 40472 Düsseldorf, Germar Tel: +49-211-65030, Fax: +49-211-6503-1327 Renesas Electronics (China) Co., Ltd.
7th Floor, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100083, P.R.China
Tel: +86-10-9235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd. Unit 204, 205, AZIA Center, No.1233 Lujiazui Ring Rd., Pudong District, Shanghai 200120, China Tel: +86-21-5877-1818, Fax: +86-21-6887-7858 / -7898

Renesas Electronics Hong Kong Limited
Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong
Tel: +852-2868-9318, Fax: +852 2886-9022/9044

Renesas Electronics Taiwan Co., Ltd. 13F, No. 363, Fu Shing North Road, Taipei, Tai Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd. 80 Bendemeer Road, Unit #06-02 Hyflux Ini Tel: +65-6213-0200, Fax: +65-6213-0300 Innovation Centre Singapore 339949

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