TOSHIBA Photocoupler IRED+Photo-IC

# TLP759F

**Digital Logic Ground Isolation** Line Receiver **Microprocessor System Interfaces** Switching Power Supply Feedback Control Industrial Inverter

The TOSHIBA TLP759F consists of a high-output infrared emitting diode and a high speed detector of one chip photo diode-transistor. This unit is 8-lead DIP.

TLP759F has no internal base connection, and a Faraday shield integrated on the photodetector chip provides an effective common mode noise transient immunity.

So this is suitable for application in noisy environmental condition.

- Isolation voltage: 5000 Vrms (min)
- Switching speed:  $t_{pHL} = 0.2 \mu s$  (typ.)

$$t_{pLH} = 0.3 \mu s$$
 (typ.) (RL=1.9 kΩ)

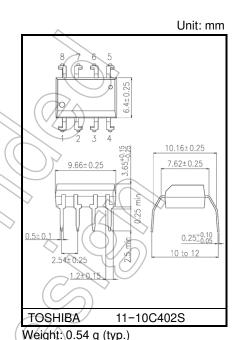
TTL compatible

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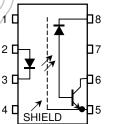
- UL-recognized: UL 1577, File No.E67349
- cUL-recognized: CSA Component Acceptance Service No.5A File No.E67349
- VDE-approved: EN 60747-5-5 (Note 1)

Note 1: When a VDE approved type is needed, please designate the Option (D4).

Mechanical Parameters Creepage distance: 8.0 mm (min) Clearance: 8.0 mm (min) Insulation thickness: 0.4 mm (min)



#### Pin Configuration (top view)



1: N.C. 2: Anode

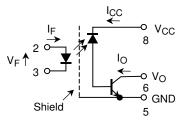
3: Cathode

- 4: N.C.
- 5: Emitter(gnd)
- 6: Collector(output)
- 7: N.C.

8: Vcc

#### **Schematic**

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Start of commercial production 1995-08

#### Absolute Maximum Ratings (Ta = 25°C)

	Characteristic		Symbol	Rating	Unit
LED	Forward current		lF	25	mA
	Forward current derating (Ta ≥70°C)		IF / Ta	-0.8	mA / °C
	Pulse forward current	(Note 1)	IFP	50	mA
	Peak transient forward current	(Note 2)	IFPT		A
	Reverse voltage		VR	5	V
	Diode power dissipation	(Note 3)	PD	45	mW
	Output current		9	8	mA
	Peak output current		IOP	16	mA
Detector	Output voltage		Vo	-0.5 to 20	V
Dete	Supply voltage	~	Vcc	-0.5 to 30	×
	Output power dissipation		Ро	100	mW
	Output power dissipation derating (Ta ≥70°C)	$(\mathcal{O})$	Po/Ta	-2	m₩ / °C
Ope	Operating temperature range			-55 to 100	್ಲಿ
Storage temperature range			T <sub>stg</sub>	-55 to 125	℃
Lead solder temperature (10 s) (Note 4			T <sub>sol</sub>	260	°C
Isolation voltage (AC, 60 s, R.H. ≤ 60 %)			BVs	5000	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 1) 50 % duty cycle, 1 ms pulse width. Derate 1.6 mA / °C above 70 °C.

(Note 2) Pulse width  $\leq 1 \mu s$ , 300 pps.

- (Note 3) Derate 0.9 mW / °C above 70 °C.
- (Note 4) Soldering portion of lead: Up to 2 mm from the body of the device.
- (Note 5) Device considered a two terminal device: Pins 1, 2, 3 and 4 shorted together and pins 5, 6, 7 and 8 shorted together.

Electrical Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	VF	I <sub>F</sub> = 16 mA	—	1.65	1.85	V
LDE	Forward voltage temperature coefficient	ΔV <sub>F</sub> / ΔTa	IF = 16 mA	_	-2		mV /°C
	Reverse current	IR	V <sub>R</sub> = 5 V	$\overline{\langle}$		10	μA
	Capacitance between terminals	Ст	V = 0 V, f = 1 MHz	$\bigcirc$	45	_	pF
	High level output current	IOH (1)	IF = 0 mA, VCC = VO = 5.5 V	$\bigcirc$	3	500	nA
or		IOH (2)	IF = 0 mA, V <sub>CC</sub> = 30 V, V <sub>O</sub> = 20 V		_	5	
Detector		Юн	$I_F = 0 \text{ mA}, V_{CC} = 30 \text{ V}, V_O = 20 \text{ V}$ Ta = 70 °C	9_	_	50	μA
	High level supply voltage	Іссн	IF = 0 mA, VCC = 30 V	_	0.01	1	μA
	Current transfer ratio	I <sub>O</sub> / I <sub>F</sub>	IF = 16 mA, VCC = 4.5 V VO = 0.4 V	20	40	1	%
Coupled	Low level output voltage	Vol	IF = 16 mA, VCC = 4.5 V IO = 2.4 mA	-2		0.4	V
	Resistance (input-output)	Rs	R.H.≤ 60 %, Vs = 500 V (Note 5)	1×10 <sup>12</sup>	1014		Ω
	Capacitance (input-output)	Cs	Vs = 0 V, f = 1 MHz (Note 5)	$\mathcal{S}_{\prime}$	0.8	_	pF
	Isolation voltage	BVs	AC, 60 s (Note 5)	5000	_	_	Vrms

### Switching Characteristics (Ta = 25°C, VCC = 5V)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Propagation delay time $(H \rightarrow L)$	tpHL	ソ	$  I_F = 0 \rightarrow 16 \text{ mA}, \\ R_L = 1.9 \text{ k}\Omega $	Ι	0.2	0.8	μS
Propagation delay time $(L \rightarrow H)$	tpLH		$I_F = 16 \rightarrow 0 \text{ mA},$ RL = 1.9 kΩ	Ι	0.3	0.8	μS
Common mode transient immunity at logic high output (Note 1)	GMH	2	$I_{F} = 0 \text{ mA}, V_{CM} = 400 \text{ V}_{p-p}$ $R_{L} = 4.1 \text{ k}\Omega$	5000	10000	Ι	V / μs
Common mode transient immunity at logic low output (Note 1)			$F = 16 \text{ mA}, V_{CM} = 400 V_{p-p}$ $R_L = 4.1 \text{ k}\Omega$	-5000	-10000	_	V / μs

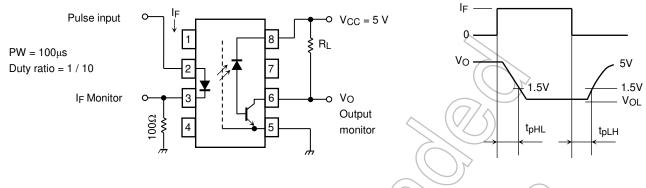
(Note 1) CML is the maximum rate of fall of the common mode voltage that can be sustained with the output voltage in the logic low state (VO < 0.8 V).

CMH is the maximum rate of rise of the common mode voltage that can be sustained with the output voltage in the logic high state (VO > 2.0 V).

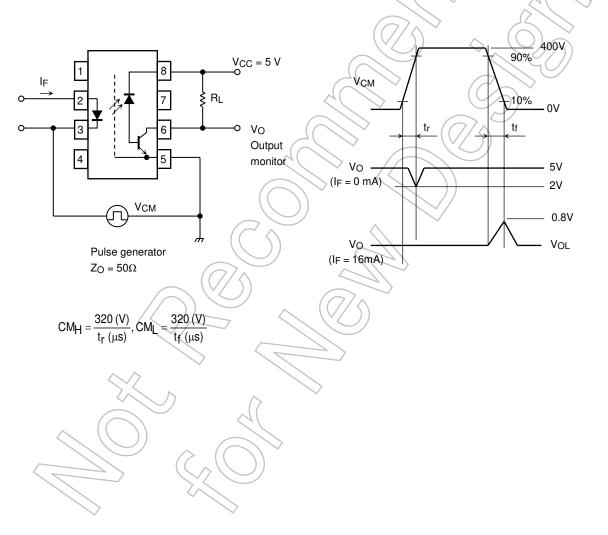


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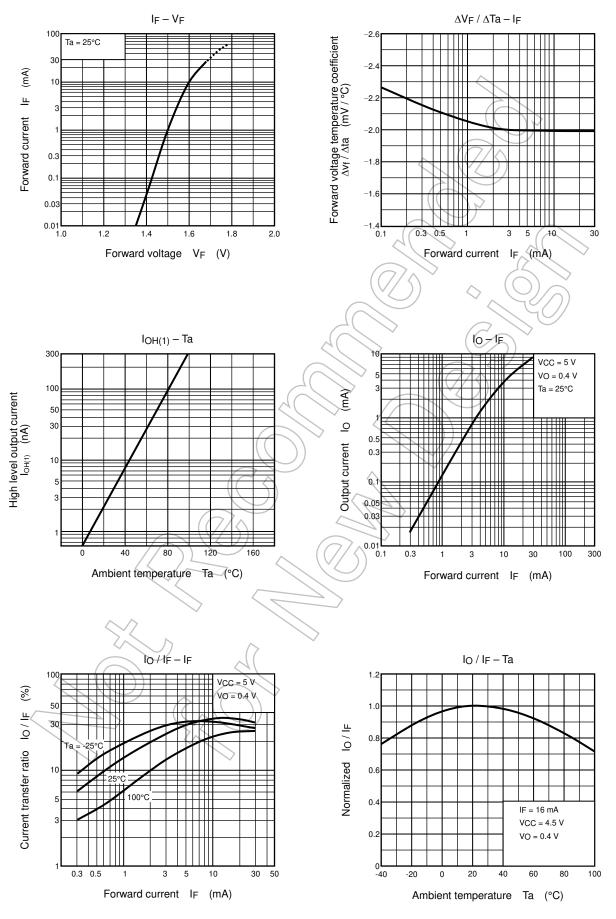
#### Test Circuit 1: Switching Time Test Circuit

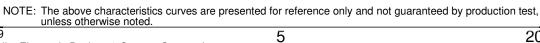




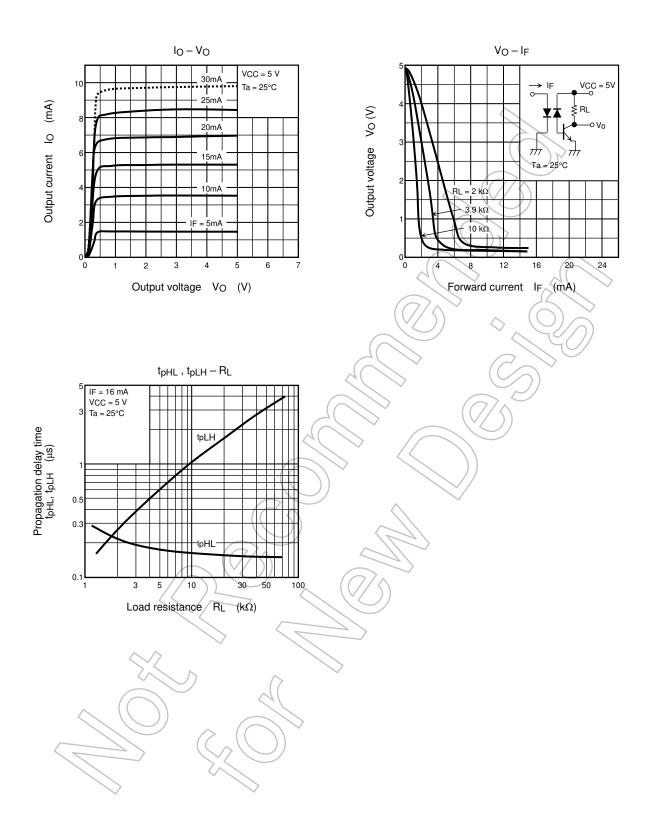


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NOTE: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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