

Features

- High Dense Cell Design for Extremely Low R_{DS(ON)}
- · Voltage Controlled Small Signal Switch
- · Surface Mount Package
- Epoxy Meets UL 94 V-0 Flammability Rating
- · Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

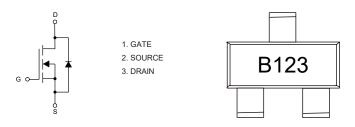
Maximum Ratings

- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature: -55°C to +150°C
- Thermal Resistance: 625°C/W Junction to Ambient

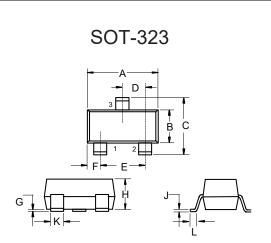
Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	100	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	0.17	Α
Power Dissipation	P _D	0.20	W

Note: 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Internal Structure and Marking Code

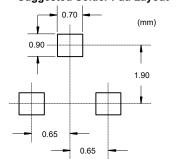


N-Channel MOSFET



DIMENSIONS						
DIM	INCHES		MM		NOTE	
DIIVI	MIN	MAX	MIN	MAX	NOTE	
Α	0.071	0.087	1.80	2.20		
В	0.045	0.053	1.15	1.35		
С	0.083	0.096	2.10	2.45		
D	0.026		0.65		TYP.	
E	0.047	0.055	1.20	1.40		
F	0.012	0.016	0.30	0.40		
G	0.000	0.004	0.00	0.10		
Н	0.035	0.044	0.90	1.10		
J	0.002	0.010	0.05	0.25		
K	0.006	0.016	0.15	0.40		
L	0.010	0.018	0.26	0.46		

Suggested Solder Pad Layout





ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

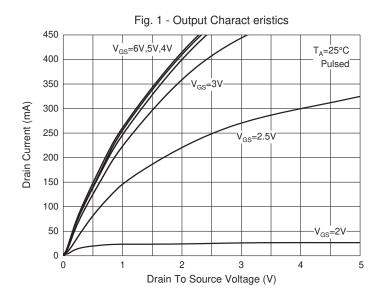
Parameter	Symbol	Test conditions	Min	Тур	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	100			V
Gate-Threshold Voltage ^(Note2)	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_D=250\mu A$	1.0		2.8	V
Gate-Body Leakage Current	I _{GSS}	V _{GS} =± 20V, V _{DS} =0V			±50	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V V _{DS} =20V, V _{GS} =0V			1 10	μA nA
Drain-Source On-Resistance ^(Note2)	R _{DS(on)}	V _{GS} =10V, I _D =0.17A V _{GS} =4.5V, I _D =0.17A			6	Ω
Forward Transconductance ^(Note2)	g _{FS}	V _{DS} =10V, I _D =0.17A	80			mS
Diode Forward Voltage ^(Note2)	V _{SD}	V _{GS} =0V, I _S =0.34A			1.3	V
Dynamic Characteristics(Note4)	•					
Input Capacitance	C _{iss}			29	60	pF
Output Capacitance	C _{oss}	V _{DS} =25V,V _{GS} =0V, f=1MHz		10	15	
Reverse Transfer Capacitance	C _{rss}			2	6	
Switching Characteristics	1			-	· · · · · · · · · · · · · · · · · · ·	
Total Gate Charge	Qg			1.4	2	
Gate-Source Charge	Q _{gs}	V _{DS} =10V,V _{GS} =10V,I _D =0.22A		0.15	0.25	nC
Gate-Drain Charge	Q _{gd}			0.2	0.4	
Turn-On Delay Time ^(Note3,4)	t _{d(on)}				8	
Turn-On Rise Time ^(Note3,4)	t _r	V_{DD} =30V, V_{GS} =10V, R_{G} =50 Ω ,			8	
Turn-Off Delay Time ^(Note3,4)	t _{d(off)}	I _D =0.28A			13	ns
Turn-Off Fall Time ^(Note3,4)	t _f				16	

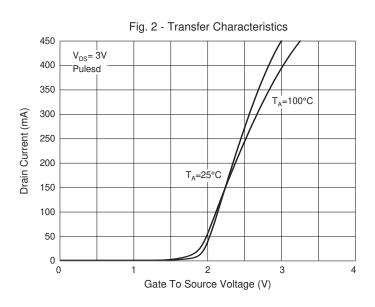
Note:

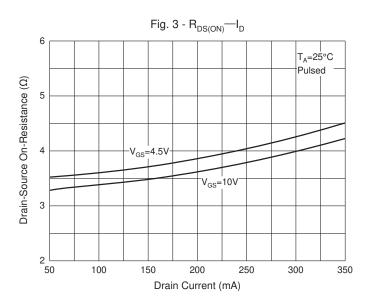
- 2. Surface Mounted on FR4 Board Using the Minimum Recommended Pad Size.
- 3. Pulse Test : Pulse Width=300µs, Duty Cycle≤2%.
- 4. Switching Characteristics are Independent of Operating Junction Temperature.
- 5. Graranted by Design, Not Subject to Producting.

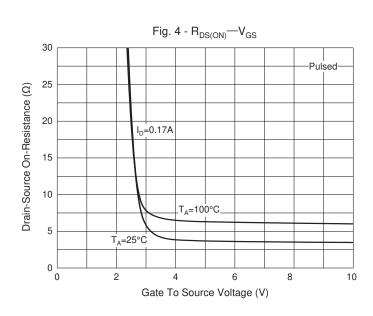


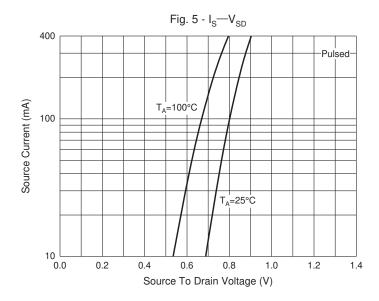
Curve Characteristics

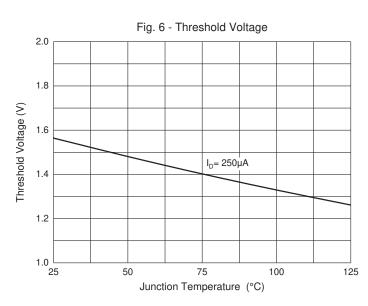














Ordering Information

Device	Packing
Part Number-TP	Tape&Reel:3Kpcs/Reel

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