



SURFACE MOUNT SCHOTTKY BARRIER DIODE

Product Summary (@TA = +25°C)

VRRM (V)	lo (mA)	V _{Fmax} (V)	I _{Rmax} (μΑ)
30	200	0.8	2

Description

200mA surface mount Schottky barrier diode in SOT23 (Standard) package, offers low turn-on voltage and fast switching capability, designed with PN junction guard ring for transient and ESD protection, totally lead-free finish and RoHS compliant, "Green" device.

Features and Benefits

- Low Turn-On Voltage
- Fast Switching
- PN Junction Guard Ring for Transient and ESD Protection
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part.
 A listing can be found at

https://www.diodes.com/products/automotive/automotive-products/.

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

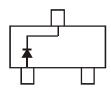
Mechanical Data

- Package: SOT23
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 (€3)
- Polarity: See Diagrams Below
- Weight: 0.008 grams (Approximate)

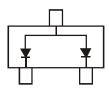
SOT23 (Standard)



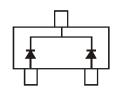
Top View



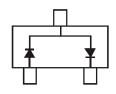
BAT54TA



BAT54ATA



BAT54CTA



BAT54STA

Ordering Information (Note 4)

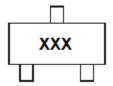
Part Number	Package	Packing		
	Package	Qty.	Carrier	
BAT54TA	SOT23 (Standard)	3,000	Tape & Reel	
BAT54ATA	SOT23 (Standard)	3,000	Tape & Reel	
BAT54CTA	SOT23 (Standard)	3,000	Tape & Reel	
BAT54STA	SOT23 (Standard)	3,000	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



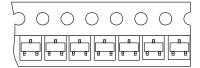
Marking Information



xxx = Product Type Marking Code L4Z = BAT54TA

L42 = BAT54ATA

L43 = BAT54CTA L44 = BAT54STA



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	VRRM VRWM VR	30	V
Average Rectified Output Current (Note 5)	lo	200	mA
Repetitive Peak Forward Current	IFRM	300	mA
Non-Repetitive Forward Surge Current @ t < 1.0s	IFSM	600	mA

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	PD	200	mW
Typical Thermal Resistance Junction to Ambient Air (Note 5)	Reja	500	°C/W
Typical Thermal Resistance Junction to Case (Note 8)	Rejc	180	°C/W
Operating and Storage Temperature Range (Note 6)	TJ, TSTG	-65 to +150	°C

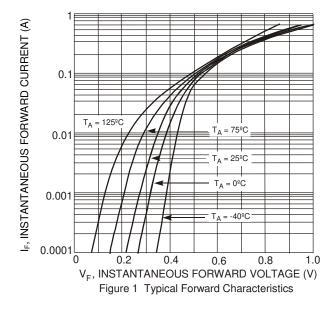
Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

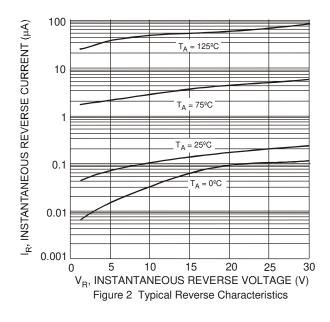
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 7)	V _{(BR)R}	30	_	_	V	IRS = 100μA
Forward Voltage	V _F	_	_	240 320 400 500 800	mV	IF = 0.1mA IF = 1mA IF = 10mA IF = 30mA IF = 100mA
Reverse Leakage Current (Note 7)	I _R			2.0	μΑ	V _R = 25V
Total Capacitance	Ст	_	_	10	pF	V _R = 1.0V, f = 1.0MHz
Reverse Recovery Time	trr	_	_	5.0	ns	I_F = 10mA through I_R = 10mA to I_R = 1.0mA, R_L = 100 Ω

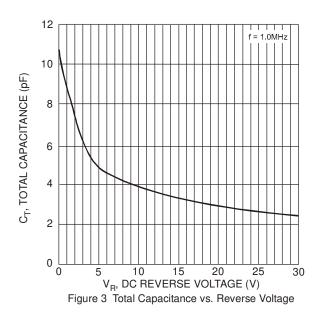
Notes:

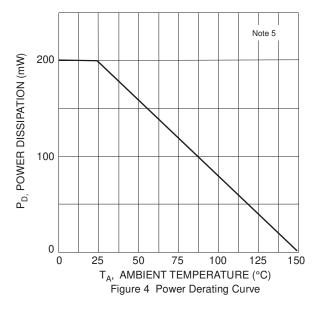
- 5. Part mounted on FR-4 board with recommended pad layout, which can be found on our website at http://www.diodes.com/package-outlines.html.
- 6. The heat generated must be less than the thermal conductivity from Junction-to-Ambient: $dP_D/dT_J < 1/R_{\theta JA}$.
- 7. Short duration test pulse used to minimize self-heating effect.
- 8. Device mounted on Polymide substrate PC board. FR-4 2oz 1*MRP layout.











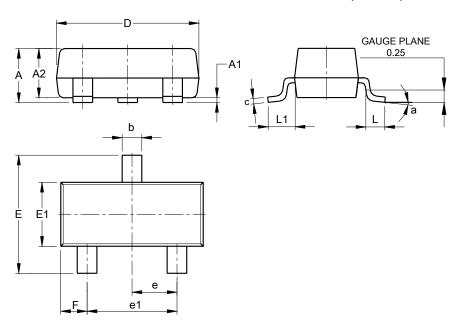
Note: 5. Part mounted on FR-4 board with recommended pad layout, which can be found on our website at http://www.diodes.com/package-outlines.html.



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23 (Standard)

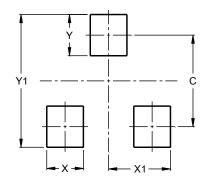


SOT23 (Standard)					
Dim	Min	Max	Тур		
Α	0.90	1.15	1.025		
A1	0.00	0.10	0.05		
A2	0.85	1.10	0.975		
b	0.30	0.51	0.40		
С	0.080	0.202	0.11		
D	2.80	3.00	2.90		
Е	2.25	2.55	2.40		
E1	1.20	1.40	1.30		
е	0.89	1.03	0.915		
e1	1.78	2.05	1.83		
F	0.40	0.60	0.535		
L1	0.45	0.61	0.55		
L	0.25	0.55	0.40		
а	0°	8°			
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23 (Standard)



Dimensions	Value (in mm)		
С	2.0		
Х	0.8		
X1	1.35		
Υ	0.9		
V1	2.9		



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June 2022