



30V COMPLEMENTARY ENHANCEMENT MODE MOSFET

Product Summary

Device	V _{(BR)DSS}	R _{DS(ON) max}	Package	I _{D MAX} T _A = +25℃
N-Channel	30V	20mΩ @ V _{GS} = 10V		8.5A
		$32m\Omega$ @ $V_{GS} = 4.5V$	SO-8	7.0A
P-Channel	-30V	45mΩ @ V _{GS} = -10V	3U-8	-5.5A
		$85mO @ V_{GS} = -4.5V$		-4.1A

Description

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- DC Motor Control
- DC-AC Inverters

Features

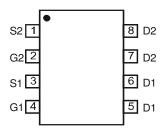
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

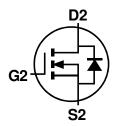
- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed Over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.008 grams (Approximate)



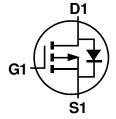
Top View



Pin Configuration



Q2 N-CHANNEL MOSFET



Q1 P-CHANNEL MOSFET

Equivalent Circuit

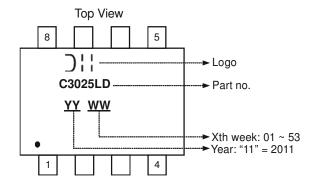
Ordering Information (Note 4)

Part Number	Case	Packaging	
DMC3025LSD-13	SO-8	2,500/Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html

Marking Information





Maximum Ratings N-CHANNEL— Q2 (@T_A = +25 ℃, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage			V_{DSS}	30	V
Gate-Source Voltage			V_{GSS}	±20	V
		$T_A = +25$ °C $T_A = +70$ °C	I _D	6.5 5.1	Α
Continuous Drain Current (Note 5) V _{GS} = 10V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	l _D	8.5 6.8	Α
Continuous Drain Current (Note 5) // 45V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	5.3 4.1	Α
Continuous Drain Current (Note 5) V _{GS} = 4.5V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	l _D	7.0 5.5	Α
Maximum Continuous Body Diode Forward Current	(Note 5)		Is	2	Α
Pulsed Drain Current (10μs pulse, duty cycle = 1%)	I _{DM}	60	Α		
Pulsed Body Diode Current (10µs pulse, duty cycle = 1%)			I _{SM}	60	Α
Avalanche Current (Note 7) L = 0.1mH			I _{AS}	14	Α
Avalanche Energy (Note 7) L = 0.1mH			E _{AS}	10	mJ

Maximum Ratings P-CHANNEL— Q1 (@T_A = +25 ℃, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V_{DSS}	-30	V		
Gate-Source Voltage			V_{GSS}	±20	V
		$T_A = +25$ °C $T_A = +70$ °C	I _D	-4.2 -3.2	Α
Continuous Drain Current (Note 5) V _{GS} = -10V	t<10s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	-5.5 -4.3	А
Continuous Drain Current (Note 5) V 4 5V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	-3.5 -2.3	А
Continuous Drain Current (Note 5) V _{GS} = -4.5V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	I _D	-4.1 -3.2	Α
Maximum Continuous Body Diode Forward Current	(Note 5)		Is	-2	Α
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I _{DM}	-30	Α
Pulsed Body Diode Current (10µs pulse, duty cycle = 1%)			I _{SM}	-30	Α
Avalanche Current (Note 7) L = 0.1mH			I _{AS}	-14	Α
Avalanche Energy (Note 7) L = 0.1mH			E _{AS}	10	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 6)	T _A = +25 °C	В	1.2	W
Total Fower Dissipation (Note 6)	T _A = +70 °C	P_D	0.77	VV
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Б	104	°C/W
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	$R_{\theta JA}$	62	G/ VV
Total Dower Dissinction (Note 5)	T _A = +25 ℃	В	1.5	W
Total Power Dissipation (Note 5)	T _A = +70 °C	P_D	0.95	VV
Thermal Desistance, Junction to Ambient (Note 5)	Steady State	Б	83	
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	$R_{\theta JA}$	49	°C/W
Thermal Resistance, Junction to Case (Note 5)	$R_{ heta JC}$	15		
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	∞	

Notes:

^{5.} Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.



Electrical Characteristics N-CHANNEL— Q2 (@T_A = +25 °C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	30	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μA	V _{DS} = 30V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	_	_	±1	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						•
Gate Threshold Voltage	V _{GS(th)}	1.0	_	2.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
Static Drain-Source On-Resistance	П	_	15	20	mΩ	V _{GS} = 10V, I _D = 7.4A
Static Drain-Source On-Resistance	R _{DS (ON)}	_	23	32	11177	$V_{GS} = 4.5V, I_D = 6A$
Forward Transfer Admittance	Y _{fs}	_	8	_	S	$V_{DS} = 5V, I_{D} = 10A$
Diode Forward Voltage	V_{SD}	_	0.70	1.2	V	V _{GS} = 0V, I _S = 1A
DYNAMIC CHARACTERISTICS (Note 9)	•					•
Input Capacitance	C _{iss}	_	501	_		$V_{DS} = 15V, V_{GS} = 0V,$ f = 1.0MHz
Output Capacitance	Coss	_	72	_	pF	
Reverse Transfer Capacitance	C _{rss}	_	57	_		
Gate Resistance	R_g	_	1.84	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	4.6	_		
Total Gate Charge (V _{GS} = 10V)	Qg	_	9.8	_	nC	Vns = 15V. In = 10A
Gate-Source Charge	Qgs	_	1.6	_	IIC	V _{DS} = 15V, I _D = 10A
Gate-Drain Charge	Q_{gd}	_	2.0	_		
Turn-On Delay Time	t _{D(on)}	_	3.9	_		
Turn-On Rise Time	t _r	_	4.2	_		$\begin{split} V_{DD} &= 15 V, \ V_{GS} = 10 V, \\ R_G &= 6 \Omega, \ I_D = 1 A \end{split}$
Turn-Off Delay Time	t _{D(off)}	_	16.6	_	ns	
Turn-Off Fall Time	t _f	_	5.8	_		
Reverse Recovery Time	t _{rr}	_	5.5	_	ns	1 104 4:/44 5004/
Reverse Recovery Charge	Q _{rr}	_	2.6	_	nC	- I _F = 12A, di/dt = 500A/μs



Electrical Characteristics P-CHANNEL — Q1 (@T_A = +25 °C, unless otherwise specified.)

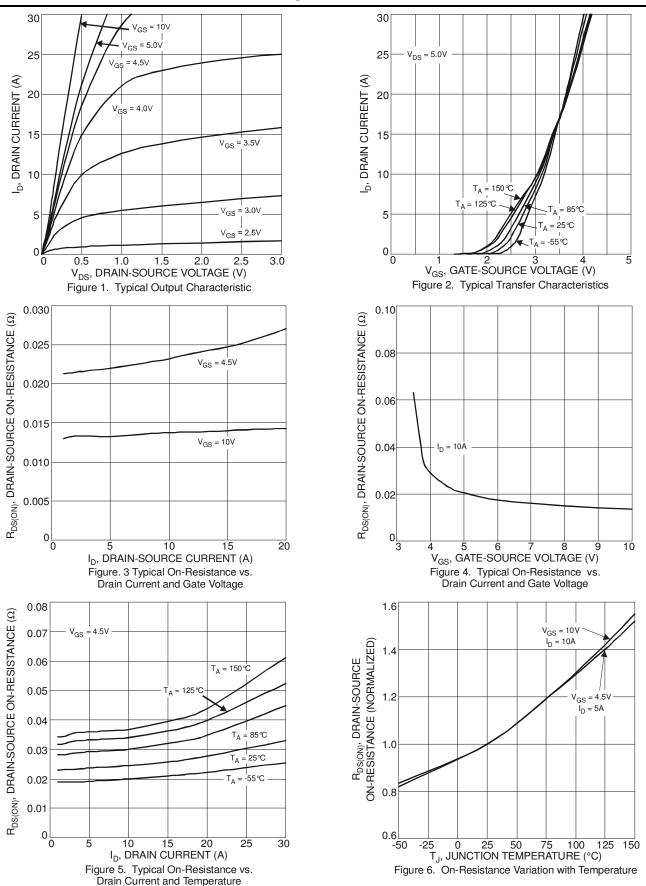
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	-30	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_		-1	μA	$V_{DS} = -30V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}			±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						•
Gate Threshold Voltage	V _{GS(th)}	-1.0	_	-2.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
Static Drain-Source On-Resistance		_	38	45	mΩ	$V_{GS} = -10V, I_D = -5.2A$
Static Drain-Source On-nesistance	R _{DS} (ON)	_	65	85	11177	$V_{GS} = -4.5V, I_D = -4A$
Forward Transfer Admittance	Y _{fs}	_	5	_	S	$V_{DS} = -5V, I_D = -5.2A$
Diode Forward Voltage	V_{SD}	_	-0.7	-1.2	V	$V_{GS} = 0V, I_{S} = -1A$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	_	590	_	pF	
Output Capacitance	Coss	_	69	_	pF	$V_{DS} = -25V, V_{GS} = 0V,$ - f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	53	_	pF	-1 = 1.0lvii iz
Gate Resistance	Rg	_	11	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge (V _{GS} = 4.5V)	Qg	_	5.1	_	nC	
Total Gate Charge (V _{GS} = 10V)	Qg	_	10.5	_	nC	V _{DS} = -15V, I _D = -6A
Gate-Source Charge	Q _{gs}	_	1.8	_	nC	
Gate-Drain Charge	Q _{gd}	_	1.9	_	nC	1
Turn-On Delay Time	t _{D(on)}	_	6.8	_	ns	
Turn-On Rise Time	t _r	_	4.9	_	ns	$V_{DD} = -15V, V_{GS} = -10V,$
Turn-Off Delay Time	t _{D(off)}	_	28.4	_	ns	$R_G = 6\Omega$, $I_D = -1A$
Turn-Off Fall Time	tf	_	12.4	_	ns	7
Reverse Recovery Time	t _{rr}	_	14	_	ns	1 100 4:/44 5000//
Reverse Recovery Charge	Q _{rr}	_	11	_	nC	I _F = 12A, di/dt = 500A/μs

Notes:

- 7. IAS and EAS rating are based on low frequency and duty cycles to keep $T_J = +25\,^{\circ}\text{C}$. 8. Short duration pulse test used to minimize self-heating effect. 9. Guaranteed by design. Not subject to product testing.



N-CHANNEL





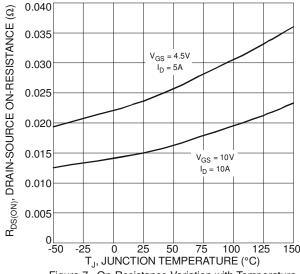
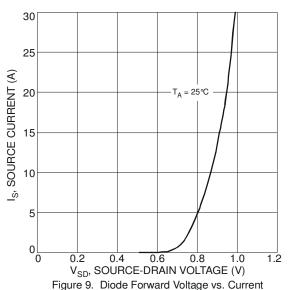
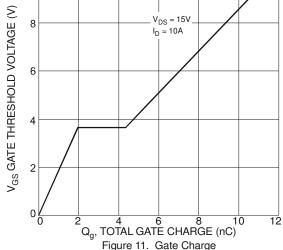


Figure 7. On-Resistance Variation with Temperature



V_{DS} = 15V



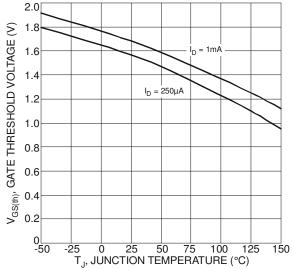
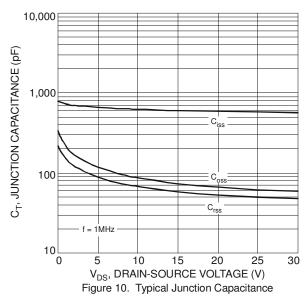
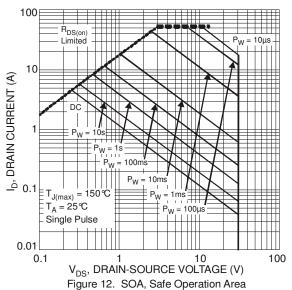


Figure 8 Gate Threshold Variation vs. Ambient Temperature

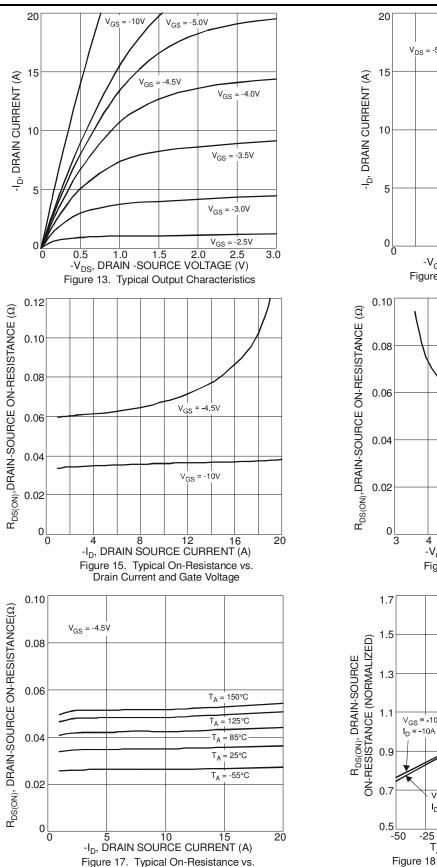


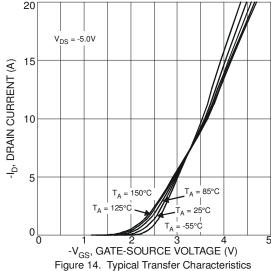


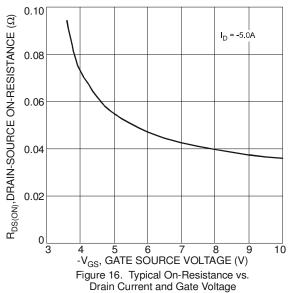
10



P-CHANNEL







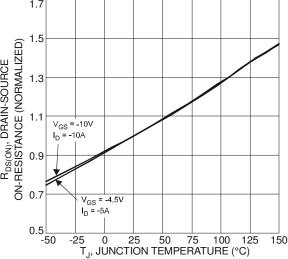
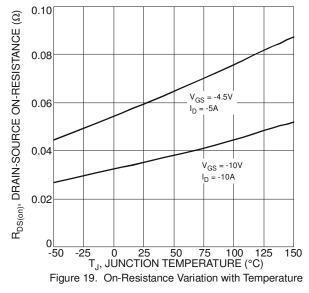
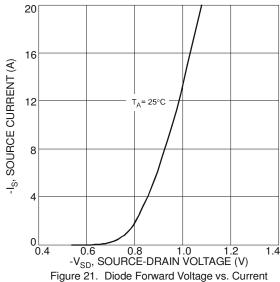


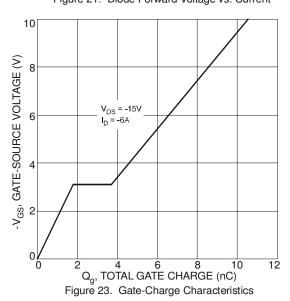
Figure 18. On-Resistance Variation with Temperature

Drain Current and Temperature









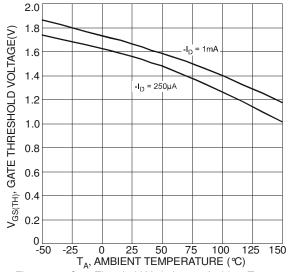
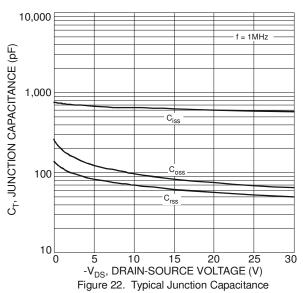


Figure 20. Gate Threshold Variation vs. Ambient Temperature



100

RDS(on)
Limited

P_W = 10µS

P_W = 10µS

P_W = 100mS

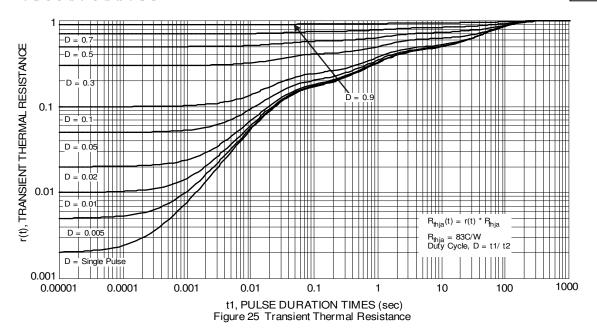
O.1

T_A = +25 °C
Single Pulse

0.01

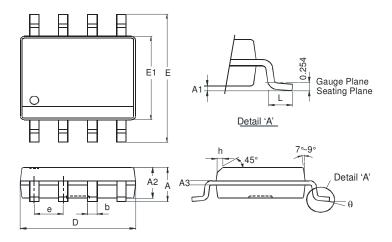
-V_{DS}, DRAIN-SOURCE VOLTAGE (V)
Figure 24. SOA, Safe Operation Area





Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

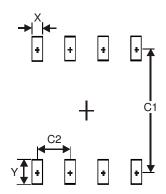


SO-8					
Dim	Min	Max			
Α	1	1.75			
A1	0.10	0.20			
A2	1.30	1.50			
A3	0.15	0.25			
b	0.3	0.5			
D	4.85	4.95			
Е	5.90	6.10			
E1	3.85 3.95				
е	1.27	Тур			
h	1	0.35			
L	0.62	0.82			
θ	0°	8°			
All Dimensions in mm					

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

SO-8



Dimensions	Value (in mm)
Х	0.60
Υ	1.55
C1	5.4
C2	1.27



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