



#### SURFACE MOUNT SCHOTTKY BARRIER

Voltage 40 V Current 1 A

#### **Features**

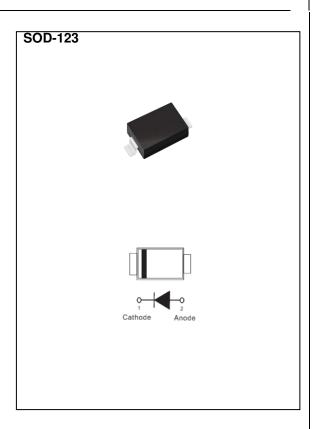
- Low forward voltage drop
- Deal for automated placement
- Low power loss, high efficienc
- High surge current capability
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard
- AEC-Q101 qualified

#### **Mechanical Data**

• Case: SOD-123 Package

• Terminals: Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.00037 ounces, 0.01 grams



### **Maximum Ratings and Thermal Characteristics** ( $T_A = 25^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Maximum Repetitive Peak Reverse Voltage	$V_{RRM}$	40	٧
Maximum Rms Voltage	$V_{RMS}$	28	V
Maximum Dc Blocking Voltage	$V_{DC}$	40	V
Maximum Average Forward Current	I <sub>F(AV)</sub>	1	Α
Peak Forward Surge Current: 8.3 ms single half sine-wave superimposed on rated load per diode	I <sub>FSM</sub>	30	А
Typical Junction Capacitance  Measured at 1 MHz And Applied VR = 4V	CJ	45	pF
Typical Thermal Resistance	$R_{\theta JA}^{(1)}$ $R_{\theta JC}^{(2)}$ $R_{\theta JL}^{(2)}$	380 100 200	°C/W
Operating Junction Temperature Range	TJ	-55~125	°C
Storage Temperature Range	T <sub>STG</sub>	-55~125	°C





# **Electrical Characteristics** (T<sub>A</sub> = 25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Forward Voltage	V <sub>F</sub>	$I_F = 0.5 \text{ A}, T_J = 25 ^{\circ}\text{C}$	-	0.44	-	· V	
		$I_F = 1 \text{ A}, T_J = 25 ^{\circ}\text{C}$	-	1	0.55		
		I <sub>F</sub> = 0.5 A, T <sub>J</sub> = 125 °C	-	0.36	-		
		I <sub>F</sub> = 1 A, T <sub>J</sub> = 125 °C	-	0.47	-		
Reverse Current	I <sub>R</sub> <sup>(3)</sup>	$V_R = 32 \text{ V}, T_J = 25 ^{\circ}\text{C}$	-	1.5	-		
		$V_R = 40 \text{ V}, T_J = 25 ^{\circ}\text{C}$	-	ı	60	uA	
		V <sub>R</sub> = 40 V, T <sub>J</sub> = 125 °C	-	2.3	-	mA	

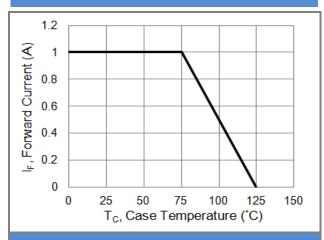
#### NOTES:

- 1. Mounted on a FR4 PCB, single-sided copper, mini pad.
- 2. Mounted on a FR4 PCB, single-sided copper, with 100 cm<sup>2</sup> copper pad area
- 3. Short duration pulse test used to minimize self-heating effect





#### **TYPICAL CHARACTERISTIC CURVES**



**Fig.1 Forward Current Derating Curve** 

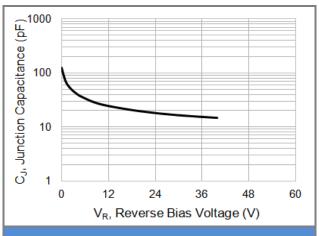


Fig.2 Typical Junction Capacitance

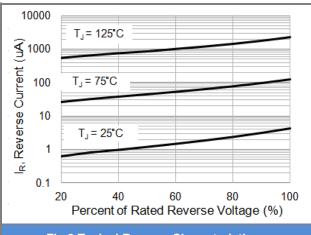


Fig.3 Typical Reverse Characteristics

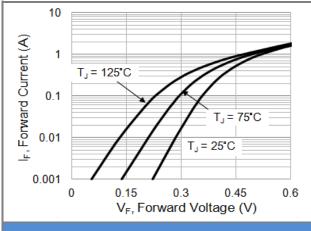


Fig.4 Typical Forward Characteristics

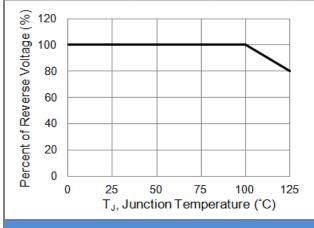


Fig.5 Operating Temperature Derating Curve

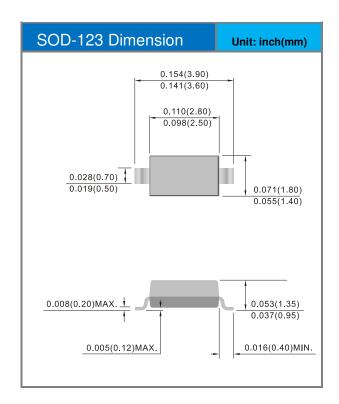


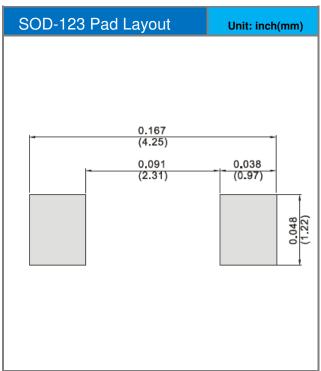


### **Part No Packing Code Version**

Part No Packing Code	Package Type	Packing Type	Marking	Version
SS1040-AU_R1_000A1	SOD-123	3K / 7" Reel	G5	Halogen free

### **Packaging Information & Mounting Pad Layout**









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