VS-UFB201FA40

Vishay Semiconductors

Insulated Ultrafast Rectifier Module, 200 A



www.vishay.com

PRIMARY CHARACTERISTICS						
V _R	400 V					
$I_{F(AV)}$ per module at $T_C = 86 \ ^{\circ}C$	200 A					
t _{rr}	40 ns					
Туре	Modules - diode FRED Pt®					
Package	SOT-227					

FEATURES

- Two fully independent diodes
- · Fully insulated package
- Ultrafast, soft reverse recovery, with high junction RoHS temperature (T_{.1} max. = $175 \degree$ C)
- Low forward voltage drop
- Optimized for power conversion: welding and industrial SMPS applications
- · Easy to use and parallel
- Industry standard outline
- UL approved file E78996
- · Designed and qualified for industrial level
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

The VS-UFB201FA40 insulated modules integrate two state of the art ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The diodes structure, and its life time control, provide an ultrasoft recovery current shape, together with the best overall performance, ruggedness and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, DC/DC converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and EMI/RFI.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Cathode to anode voltage	V _R		400	V		
Continuous forward current per diode	١ _F	T _C = 88 °C	120	^		
Single pulse forward current per diode	I _{FSM}	T _C = 25 °C	600	A		
Maximum power dissipation per module	PD	T _C = 88 °C	311	W		
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V		
Maximum junction temperature	TJ		-55 to +175			
Maximum case temperature	T _C		150	°C		
Storage temperature	T _{STG}		-55 to +150			



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ELECTRICAL SPECIFICATIONS PER DIODE (T_J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	OL TEST CONDITIONS		TYP.	MAX.	UNITS	
Cathode to anode breakdown voltage	V_{BR}	I _R = 100 μA	400	-	-		
		I _F = 100 A	-	1.33	1.59		
Forward voltage	V _{FM}	$I_F = 100 \text{ A}, \text{T}_\text{J} = 125 \ ^\circ\text{C}$	-	1.19	1.28	V	
Forward voltage		I _F = 200 A	-	1.56	1.91		
		$I_F = 200 \text{ A}, \text{ T}_J = 125 \text{ °C}$	-	1.49	1.64		
		$V_{R} = V_{R}$ rated	-	0.20	50	μA	
Reverse leakage current	I _{RM}	$T_J = 175 \text{ °C}, V_R = V_R \text{ rated}$ -		0.40	2	mA	
Junction capacitance	CT	V _R = 400 V	-	76	-	pF	

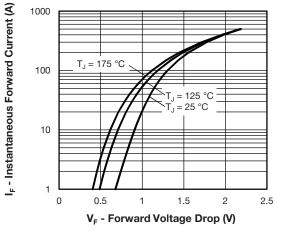
DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST C	ONDITIONS	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 200 \text{ A}/\mu\text{s}, V_R = 30$		-	40	-		
Reverse recovery time	t _{rr}	T _J = 25 °C		-	80	-	ns - A - nC	
		T _J = 125 °C		-	160	-		
Peak recovery current		T _J = 25 °C	I _F = 50 A dI _F /dt = 200 A/μs V _R = 200 V	-	7	-		
Peak recovery current	I _{RRM}	T _J = 125 °C		-	16	-		
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	310	-		
		T _J = 125 °C		-	1300	-		

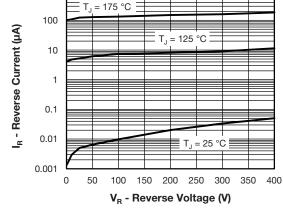
THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Junction to case, single leg conducting	Р		-	-	0.56		
Junction to case, both leg conducting	R _{thJC}		-	-	0.28	°C/W	
Case to heatsink	R _{thCS}	Flat, greased surface	-	0.075	-		
Weight			-	30	-	g	
		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)	
Mounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)	
Case style					SOT-227		



VS-UFB201FA40

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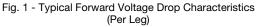


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

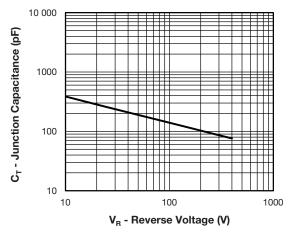


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

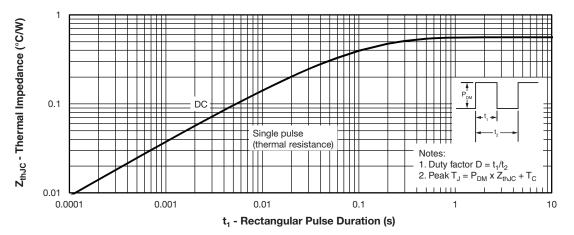


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)

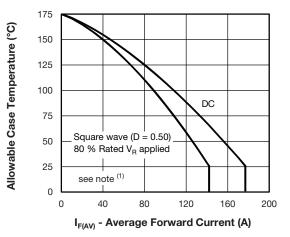
 Revision: 10-Sep-2019
 Document Number: 93793

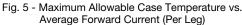
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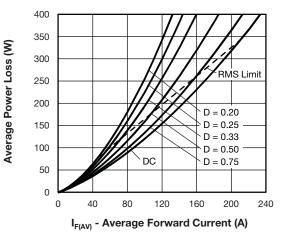


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

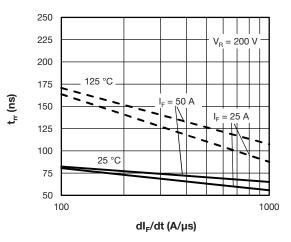
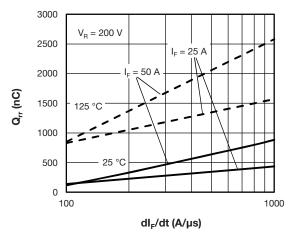


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt





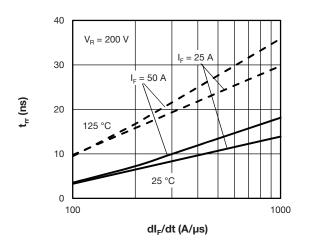


Fig. 9 - Typical Reverse Recovery vs. dl_F/dt

Note

 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;



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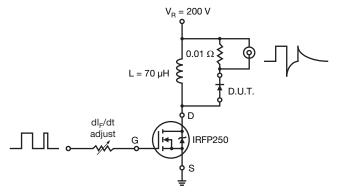
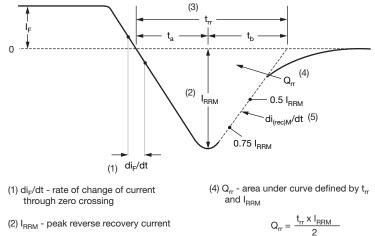


Fig. 10 - Reverse Recovery Parameter Test Circuit



(3) ${\rm t}_{\rm rr}$ - reverse recovery time measured from zero crossing point of negative going ${\rm I}_{\rm F}$ to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.

(5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 11 - Reverse Recovery Waveform and Definitions





ORDERING INFORMATION TABLE

Device code

е	VS-	UF	В	201	F	Α	40			
	1	2	3	4	5	6	(7)			
	 Vishay Semiconductors product Ultrafast rectifier Ultrafast Pt diffused 									
	4 · 5 ·	- Cur	Current rating (201 = 200 A) Circuit configuration (two separate diodes, parallel pin-out)							
	6 -	- Pac	Package indicator (SOT-227 standard insulated base)							

7 Voltage rating (40 = 400 V)

CIRCUIT CONFIGURATION						
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING				
Two separate diodes, parallel pin-out	F	Lead Assignment				

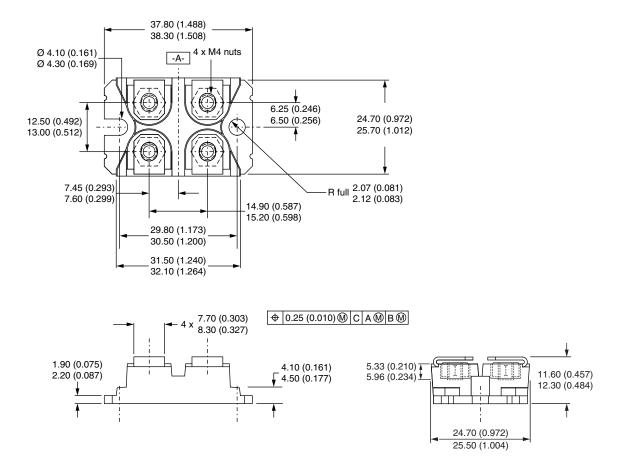
LINKS TO RELATED DOCUMENTS						
Dimensions www.vishay.com/doc?95423						
Packaging information	www.vishay.com/doc?95425					

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SOT-227 Generation 2

DIMENSIONS in millimeters (inches)



Note

• Controlling dimension: millimeter



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