### **30V N-CHANNEL ENHANCEMENT MODE MOSFET**

### **SUMMARY**

 $V_{(BR)DSS}$ =30V;  $R_{DS(ON)}$ =0.025 $\Omega$   $I_D$ =6.7A

#### **DESCRIPTION**

This new generation of TRENCH MOSFETs from Zetex utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage, power management applications.



#### **FEATURES**

- Low on-resistance
- · Fast switching speed
- Low threshold
- Low gate drive
- Low profile SOIC package

### **APPLICATIONS**

- DC DC Converters
- Power Management Functions
- Disconnect switches
- Motor control

# ORDERING INFORMATION

| DEVICE       | REEL<br>SIZE | TAPE<br>WIDTH | QUANTITY<br>PER REEL |
|--------------|--------------|---------------|----------------------|
| ZXMN3A02X8TA | 7"           | 12mm          | 1000 units           |
| ZXMN3A02X8TC | 13"          | 12mm          | 4000 units           |

# 

### Top View

#### **DEVICE MARKING**

 ZXMN 3A02



#### **ABSOLUTE MAXIMUM RATINGS.**

| PARAMETER   | SYMBOL           | LIMIT             | UNIT       |
|---|------------------|-------------------|------------|
| Drain-Source Voltage  | V <sub>DSS</sub> | 30                | V          |
| Gate Source Voltage   | VGS              | ±20               | V          |
| Continuous Drain Current $V_{GS}=10V$ ; $T_A=25^{\circ}C$ (b) $V_{GS}=10V$ ; $T_A=70^{\circ}C$ (b) $V_{GS}=10V$ ; $T_A=25^{\circ}C$ (a) | ID               | 6.7<br>5.4<br>5.3 | А          |
| Pulsed Drain Current (c)  | IDM              | 24                | А          |
| Continuous Source Current (Body Diode) (b)  | IS               | 3.2               | А          |
| Pulsed Source Current (Body Diode) (c)  | ISM              | 24                | А          |
| Power Dissipation at TA=25°C (a)<br>Linear Derating Factor  | PD               | 1.1<br>8.8        | W<br>mW/°C |
| Power Dissipation at T <sub>A</sub> =25°C (b)<br>Linear Derating Factor   | PD               | 1.8<br>14.4       | W<br>mW/°C |
| Operating and Storage Temperature Range   | Tj:Tstg          | -55 to +150       | °C         |

#### THERMAL RESISTANCE

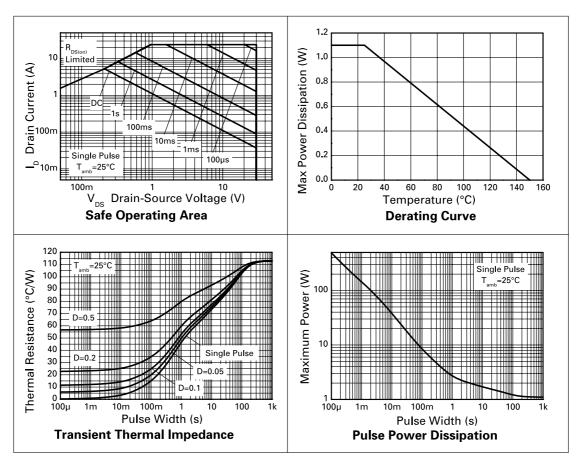
| PARAMETER               | SYMBOL            | VALUE | UNIT |
|-------------------------|-------------------|-------|------|
| Junction to Ambient (a) | R <sub>θ</sub> JA | 113   | °C/W |
| Junction to Ambient (b) | R <sub>θ</sub> JA | 70    | °C/W |

#### NOTES

- (a) For a device surface mounted on  $25 \text{mm} \times 25 \text{mm}$  FR4 PCB with high coverage of single sided 1oz copper, in still air conditions
- (b) For a device surface mounted on FR4 PCB measured at t  $\!\!\!<\!\!10$  secs.
- (c) Repetitive rating 25mm x 25mm FR4 PCB, D = 0.05, pulse width  $10\mu s$  pulse width limited by maximum junction temperature.



#### **CHARACTERISTICS**



<sup>\*</sup> For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.



# **ELECTRICAL CHARACTERISTICS** (at $T_A = 25^{\circ}C$ unless otherwise stated).

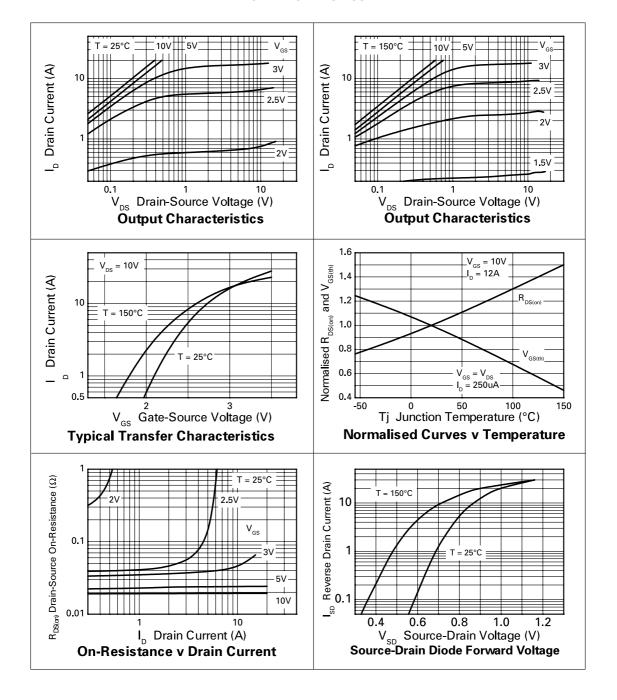
| PARAMETER                                   | SYMBOL              | MIN. | TYP. | MAX.           | UNIT   | CONDITIONS.   |  |
|---|---------------------|------|------|----------------|--------|---|--|
| STATIC                                      | '                   | ı    |      |                |        |   |  |
| Drain-Source Breakdown Voltage              | V(BR)DSS            | 30   |      |                | V      | I <sub>D</sub> =250μA, V <sub>GS</sub> =0V  |  |
| Zero Gate Voltage Drain Current             | IDSS                |      |      | 1              | μА     | V <sub>DS</sub> =30V, V <sub>GS</sub> =0V   |  |
| Gate-Body Leakage                           | IGSS                |      |      | 100            | nA     | V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V  |  |
| Gate-Source Threshold Voltage               | VGS(th)             | 1    |      |                | V      | I <sub>D</sub> =250μA, V <sub>DS</sub> = V <sub>GS</sub>                                    |  |
| Static Drain-Source On-State Resistance (1) | R <sub>DS(on)</sub> |      |      | 0.025<br>0.035 | Ω<br>Ω | V <sub>GS</sub> =10V, I <sub>D</sub> =12A<br>V <sub>GS</sub> =4.5V, I <sub>D</sub> =10.2A   |  |
| Forward Transconductance (1)(3)             | 9fs                 |      | 22   |                | S      | V <sub>DS</sub> =10V,I <sub>D</sub> =12A  |  |
| DYNAMIC (3)                                 |                     |      |      |                | •      |   |  |
| Input Capacitance                           | C <sub>iss</sub>    |      | 1400 |                | pF     | \/ 25 \/ \/ 0\/   |  |
| Output Capacitance                          | Coss                |      | 209  |                | pF     | V <sub>DS</sub> =25 V, V <sub>GS</sub> =0V,<br>f=1MHz                                       |  |
| Reverse Transfer Capacitance                | C <sub>rss</sub>    |      | 120  |                | pF     |   |  |
| SWITCHING(2) (3)                            |                     |      | •    | •              |        |   |  |
| Turn-On Delay Time                          | <sup>t</sup> d(on)  |      | 3.9  |                | ns     |   |  |
| Rise Time                                   | t <sub>r</sub>      |      | 5.5  |                | ns     | V <sub>DD</sub> =15V, I <sub>D</sub> =5.5A  |  |
| Turn-Off Delay Time                         | td(off)             |      | 35.0 |                | ns     | RG=6.2Ω, VGS=10V<br>(refer to test circuit)   |  |
| Fall Time                                   | tf                  |      | 7.6  |                | ns     |   |  |
| Gate Charge                                 | Ωg                  |      | 14.5 |                | nC     | V <sub>DS</sub> =15V,V <sub>GS</sub> =5V,<br>I <sub>D=5.5A</sub><br>(refer to test circuit) |  |
| Total Gate Charge                           | Ωg                  |      | 26.8 |                | nC     |   |  |
| Gate-Source Charge                          | Qgs                 |      | 4.7  |                | nC     | V <sub>DS</sub> =15V,V <sub>GS</sub> =10V,<br>I <sub>D</sub> =5.5A                          |  |
| Gate-Drain Charge                           | Qgd                 |      | 4.7  |                | nC     | (refer to test circuit)   |  |
| SOURCE-DRAIN DIODE                          |                     | •    | •    | •              |        |   |  |
| Diode Forward Voltage (1)                   | V <sub>SD</sub>     |      |      | 0.95           | V      | T <sub>J</sub> =25°C, I <sub>S</sub> =9A,<br>V <sub>GS</sub> =0V                            |  |
| Reverse Recovery Time (3)                   | t <sub>rr</sub>     |      | 17   |                | ns     | TJ=25°C, IF=5.5A,<br>di/dt= 100A/μs   |  |
| Reverse Recovery Charge (3)                 | Orr                 |      | 8.3  |                | nC     |   |  |

#### NOTES

- (1) Measured under pulsed conditions. Width=300 $\mu s.$  Duty cycle  $\leq~2\%$  .
- (2) Switching characteristics are independent of operating junction temperature.
- (3) For design aid only, not subject to production testing.

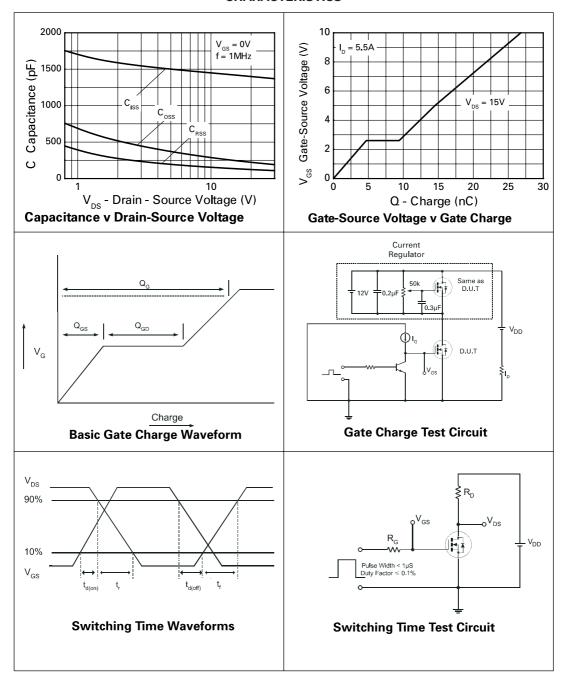


#### **CHARACTERISTICS**



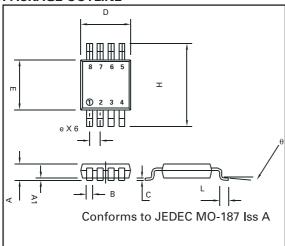


#### **CHARACTERISTICS**





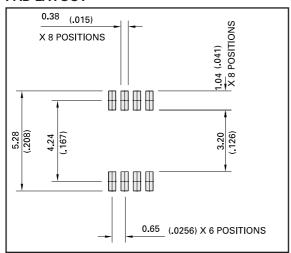
#### **PACKAGE OUTLINE**



#### **PACKAGE DIMENSION**

| DIM | Millimetres |      | Millimetres |       | Inch | es |
|-----|-------------|------|-------------|-------|------|----|
|     | MIN         | MAX  | MIN         | MAX   |      |    |
| А   |             | 1.10 |             | 0.043 |      |    |
| A1  | 0.05        | 0.15 | 0.002       | 0.006 |      |    |
| В   | 0.25        | 0.40 | 0.010       | 0.016 |      |    |
| С   | 0.13        | 0.23 | 0.005       | 0.009 |      |    |
| D   | 2.90        | 3.10 | 0.114       | 0.122 |      |    |
| е   | 0.65        | BSC  | 0.0256      | BSC   |      |    |
| Е   | 2.90        | 3.10 | 0.114       | 0.122 |      |    |
| Н   | 4.90        | BSC  | 0.193       | BSC   |      |    |
| L   | 0.40        | 0.70 | 0.016       | 0.028 |      |    |
| θ°  | 0°          | 6°   | 0°          | 6°    |      |    |

#### **PAD LAYOUT**



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