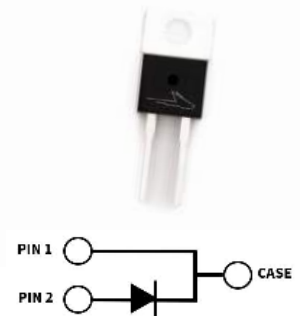


C6D10065A

6th Generation 650 V, 10 A Silicon Carbide Schottky Diode

Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.



Package Types: TO-220-2
Marking: C6D10065A

Features

- Low Forward Voltage (V_F) Drop with Positive Temperature Coefficient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior

Applications

- Industrial Switched Mode Power Supplies
- Uninterruptible & AUX Power Supplies
- Boost for PFC & DC-DC Stages
- Solar Inverters

Maximum Ratings ($T_C = 25^\circ\text{C}$ Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Notes
Repetitive Peak Reverse Voltage	V_{RRM}	650	V		
DC Blocking Voltage	V_{DC}	650			
Continuous Forward Current	I_F	37	A	$T_J = 25^\circ\text{C}$	Fig. 3
		19		$T_J = 125^\circ\text{C}$	
		10		$T_J = 155^\circ\text{C}$	
Repetitive Peak Forward Surge Current	I_{FRM}	45	A	$T_C = 25^\circ\text{C}, t_p = 10 \text{ ms}, \text{Half Sine Wave}$	Fig. 8
		27		$T_C = 110^\circ\text{C}, t_p = 10 \text{ ms}, \text{Half Sine Wave}$	
Non-Repetitive Forward Surge Current	I_{FSM}	86	A	$T_C = 25^\circ\text{C}, t_p = 10 \text{ ms}, \text{Half Sine Wave}$	Fig. 8
		75		$T_C = 110^\circ\text{C}, t_p = 10 \text{ ms}, \text{Half Sine Wave}$	
Non-Repetitive Peak Forward Surge Current	$I_{F,Max}$	1250	A	$T_C = 25^\circ\text{C}, t_p = 10 \mu\text{s}, \text{Pulse}$	Fig. 8
		1100		$T_C = 110^\circ\text{C}, t_p = 10 \mu\text{s}, \text{Pulse}$	
Power Dissipation	P_{tot}	109	W	$T_J = 25^\circ\text{C}$	Fig. 4
		47		$T_J = 110^\circ\text{C}$	

Electrical Characteristics

Parameter	Symbol	Typ.	Max.	Unit	Test Conditions	Notes
Forward Voltage	V_F	1.27	1.50	V	$I_F = 10 \text{ A}, T_j = 25 \text{ }^\circ\text{C}$	Fig. 1
		1.37	1.60		$I_F = 10 \text{ A}, T_j = 175 \text{ }^\circ\text{C}$	
Reverse Current	I_R	2	50	μA	$V_R = 650 \text{ V}, T_j = 25 \text{ }^\circ\text{C}$	Fig. 2
		15	200		$V_R = 650 \text{ V}, T_j = 175 \text{ }^\circ\text{C}$	
Total Capacitive Charge	Q_C	34		nC	$V_R = 400 \text{ V}, T_j = 25 \text{ }^\circ\text{C}$	Fig. 5
Total Capacitance	C	611		pF	$V_R = 0 \text{ V}, T_j = 25 \text{ }^\circ\text{C}$	

Typical Performance

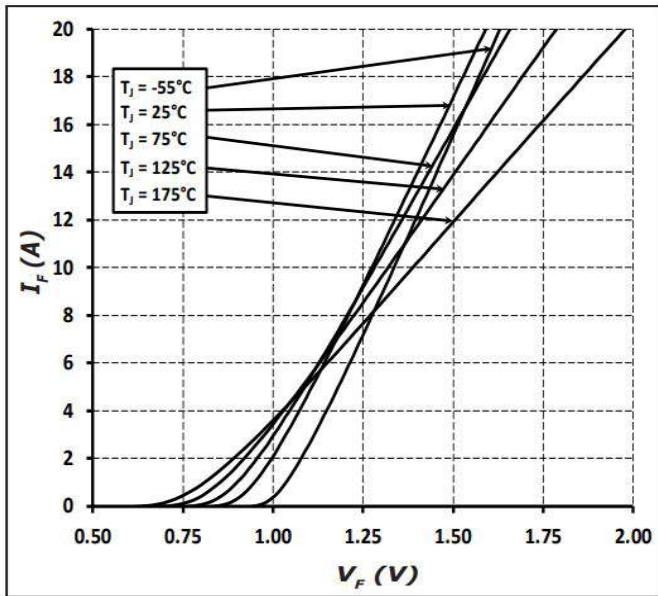


Figure 1
Forward Characteristics

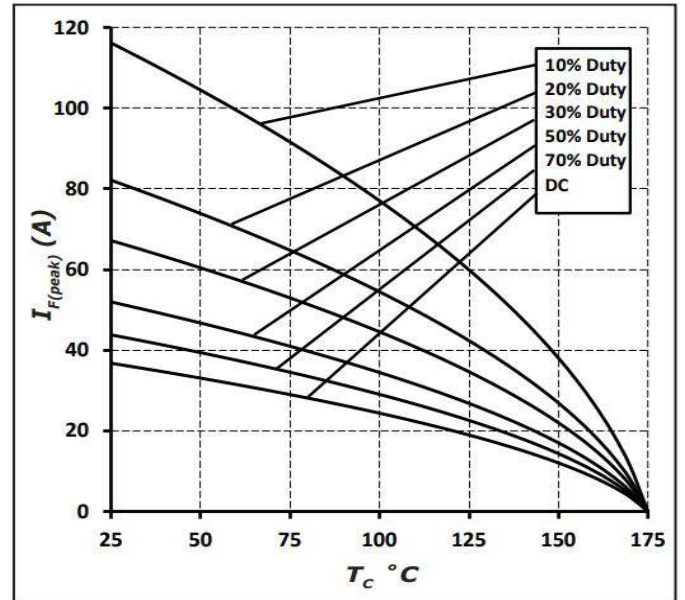


Figure 2
Reverse Characteristics

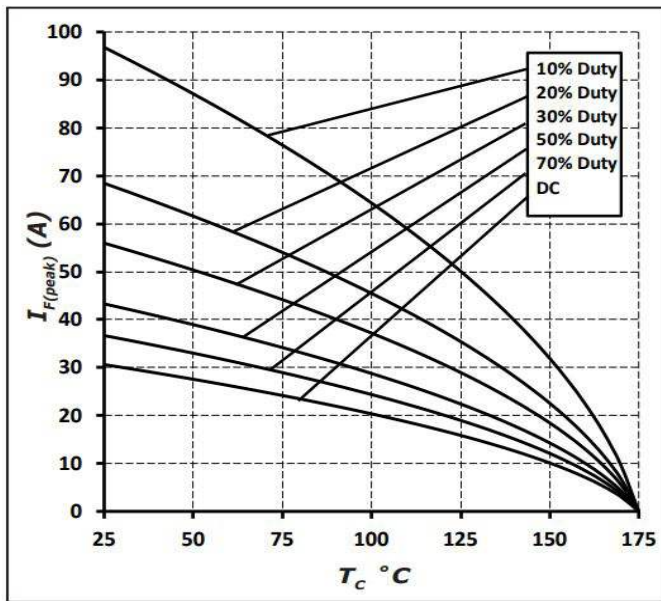


Figure 3
Current Derating

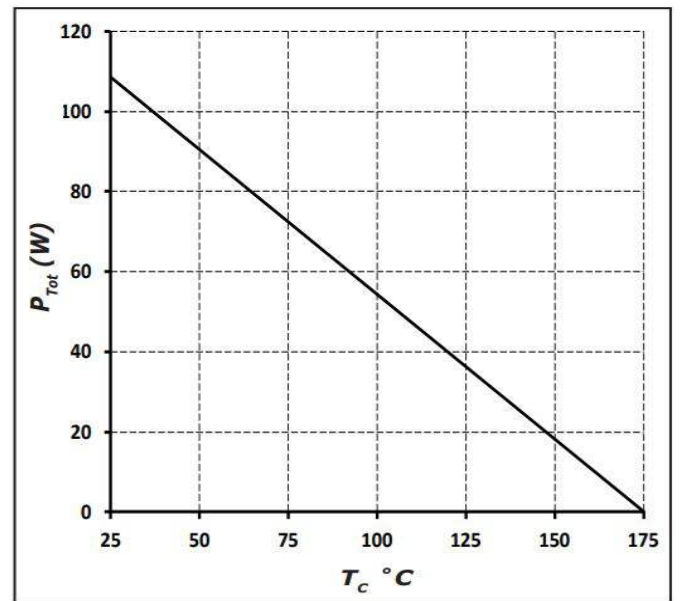


Figure 4
Power Derating

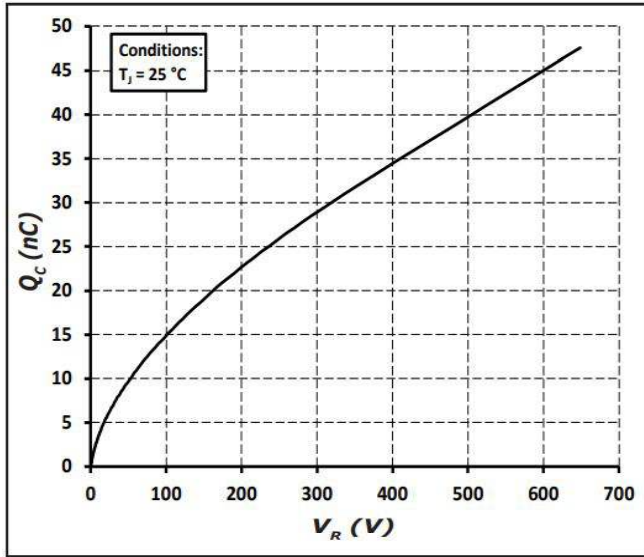


Figure 5

Total Capacitance vs. Reverse Voltage

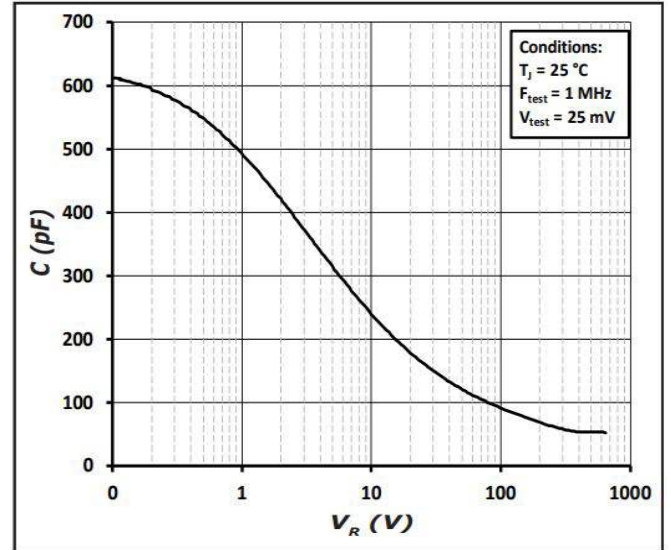


Figure 6

Capacitance vs. Reverse Voltage

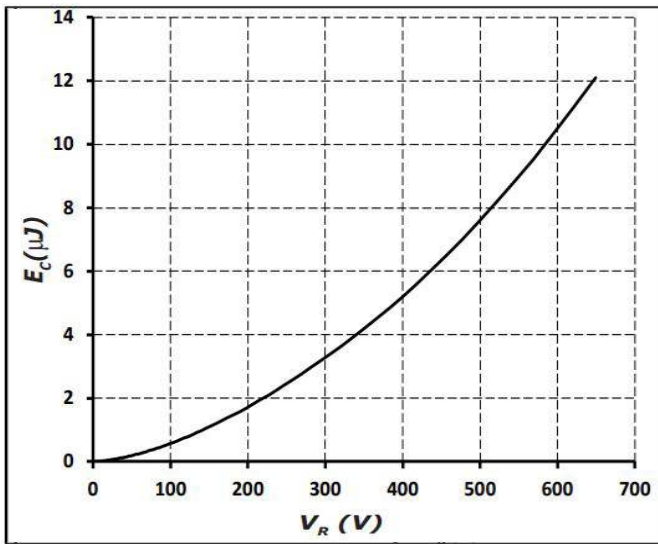


Figure 7

Capacitance Stored Energy

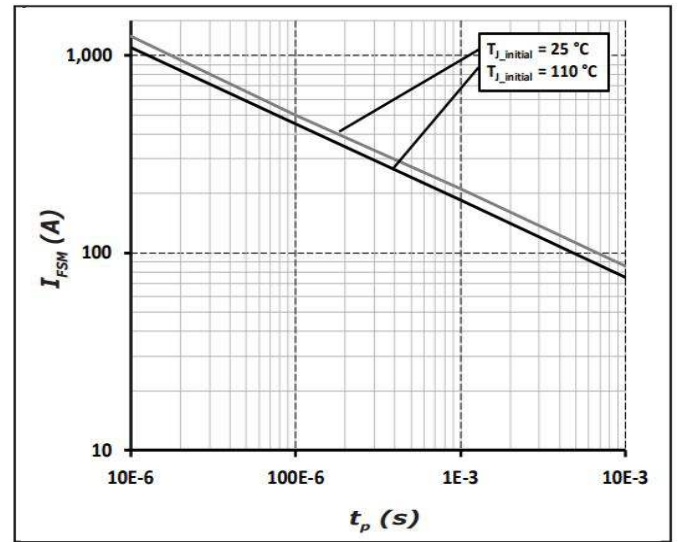


Figure 8

Non-Repetitive Peak Forward Surge Current versus Pulse Duration (sinusoidal waveform)

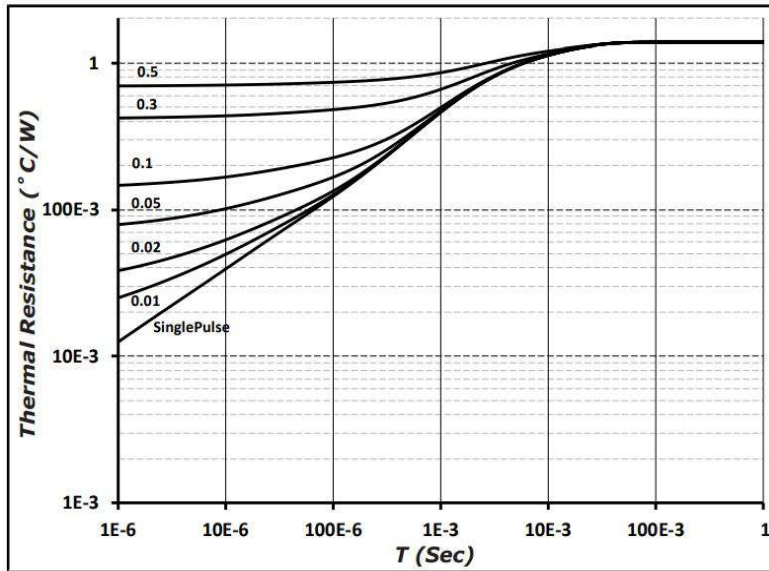
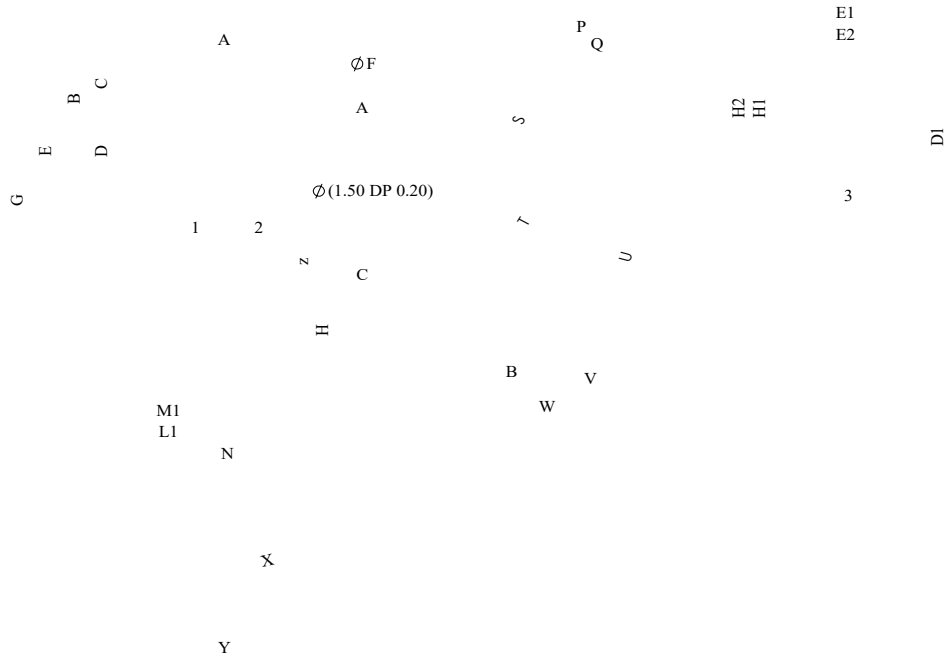


Figure 9
Transient Thermal Impedance

Package Dimensions & Pin-Out

Package: TO-220-2



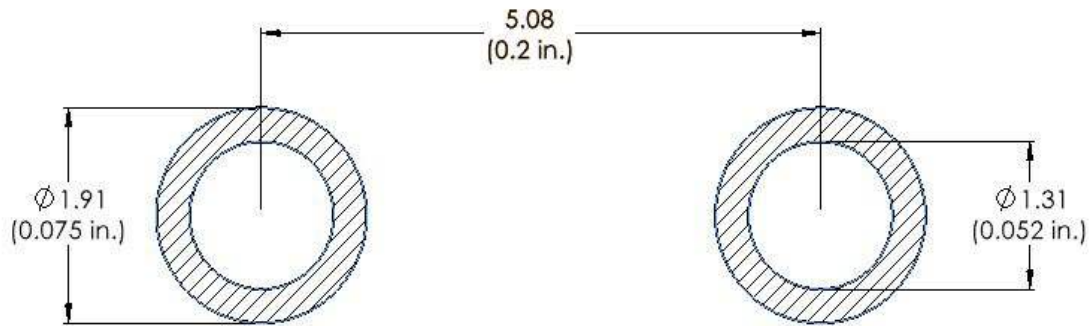
NOTE

1. ALL METAL SURFACES ARE TIN PLATED (MATTE), EXCEPT AREA OF CUT.
2. DIMENSIONING & TOLERANCING CONFORM TO ASME Y14.5M-1994.
3. ALL DIMENSIONS ARE LISTED IN MILLIMETERS. ANGLES ARE IN DEGREES.
4. PACKAGE BURR FLASH SIZE (0.5 mm) IS NOT INCLUDED IN THE DIMENSIONS



Recommended Solder Pad Layout

Primary dimensions shown in mm.



Product Ordering Information

Order Number	Packing Type
C6D10065A	Tube



Revision History

Document Version	Date of Release	Description of Changes
0	April-2019	Initial Release
1	March-2023	Update Package Drawing Update Landing Pad



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Contact info:

4600 Silicon Drive
Durham, NC 27703 USA
Tel: +1.919.313.5300
www.wolfspeed.com/power