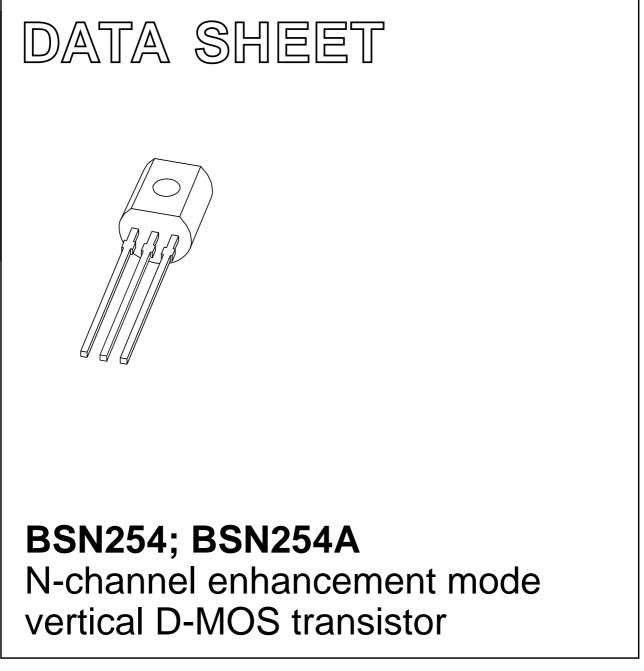
DISCRETE SEMICONDUCTORS



Product specification Supersedes data of 1997 Jun 23 2002 Feb 19



BSN254; BSN254A

FEATURES

- Direct interface to C-MOS, TTL, etc.
- High-speed switching
- No secondary breakdown
- Low R_{DSon}.

APPLICATIONS

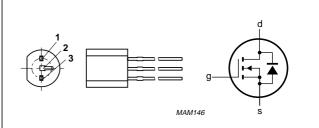
- Line current interruptor in telephone sets
- Relay, high-speed and line transformer drivers.

DESCRIPTION

N-channel enhancement mode vertical D-MOS transistor in a SOT54 (TO-92) variant package.

PINNING - SOT54 variant

| PIN | DESCRIPTION | | |
|-----|-------------|---------|--|
| FIN | BSN254 | BSN254A | |
| 1 | gate | source | |
| 2 | drain | gate | |
| 3 | source | drain | |



note: various pinnings are available on request

Fig.1 Simplified outline and symbol.

QUICK REFERENCE DATA

| SYMBOL | PARAMETER | CONDITIONS | TYP. | MAX. | UNIT |
|-------------------|----------------------------------|---|------|------|------|
| V _{DS} | drain-source voltage (DC) | | _ | 250 | V |
| I _D | drain current (DC) | | _ | 310 | mA |
| P _{tot} | total power dissipation | T _{amb} ≤ 25 °C | - | 1 | W |
| R _{DSon} | drain-source on-state resistance | $I_D = 300 \text{ mA}; V_{GS} = 10 \text{ V}$ | 2.8 | 5 | Ω |
| V _{GSth} | gate-source threshold voltage | $I_D = 1 \text{ mA}; V_{DS} = V_{GS}$ | _ | 2 | V |

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

| SYMBOL | PARAMETER | CONDITIONS | MIN. | MAX. | UNIT |
|------------------|---------------------------|--------------------------------------|------|------|------|
| V _{DS} | drain-source voltage (DC) | | - | 250 | V |
| V _{GSO} | gate-source voltage (DC) | open drain | - | ±20 | V |
| I _D | drain current (DC) | | - | 310 | mA |
| I _{DM} | peak drain current | | - | 1.25 | А |
| P _{tot} | total power dissipation | $T_{amb} \le 25 \ ^{\circ}C;$ note 1 | - | 1 | W |
| T _{stg} | storage temperature | | -55 | +150 | °C |
| Tj | junction temperature | | - | 150 | °C |

Note

1. Device mounted on a printed-circuit board; maximum lead length 4 mm; mounting pad for drain lead minimum 10×10 mm.

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THERMAL CHARACTERISTICS

| SYMBOL | PARAMETER | VALUE | UNIT |
|---------------------|---|-------|------|
| R _{th j-a} | thermal resistance from junction to ambient; note 1 | 125 | K/W |

Note

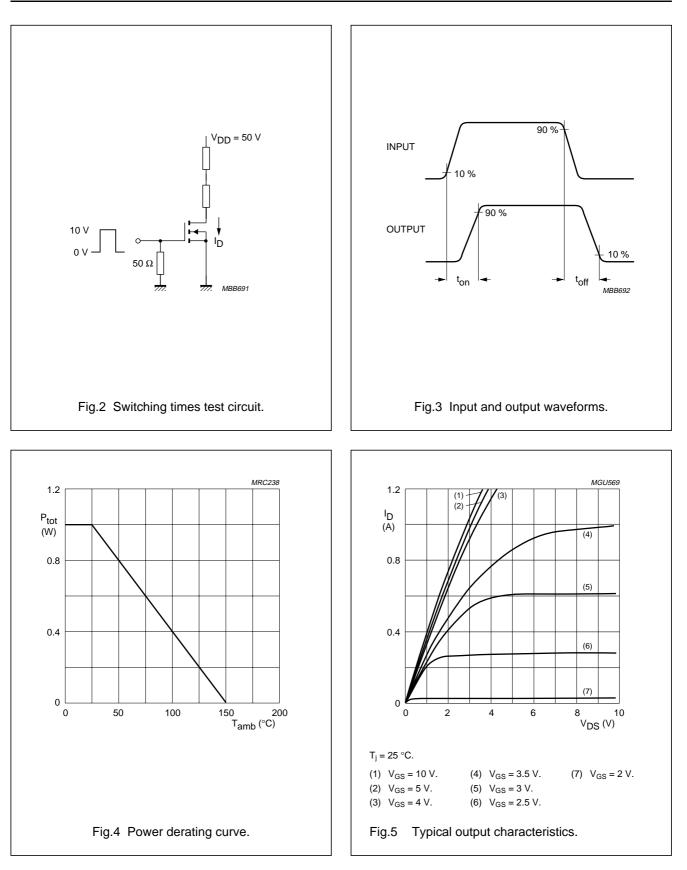
1. Device mounted on a printed-circuit board; maximum lead length 4 mm; mounting pad for drain lead minimum 10×10 mm.

CHARACTERISTICS

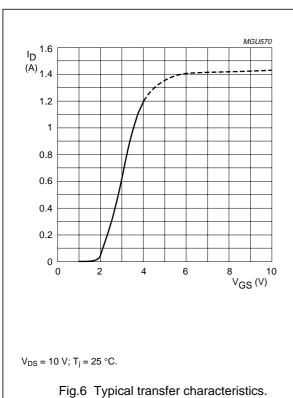
 $T_i = 25 \ ^{\circ}C$ unless otherwise specified.

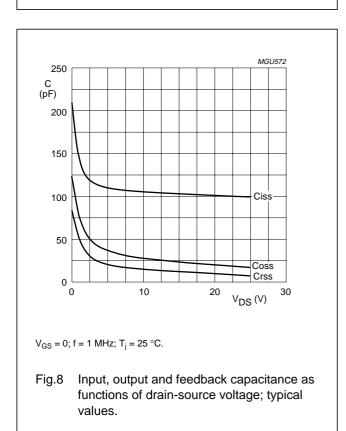
| SYMBOL | PARAMETER | CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|----------------------|----------------------------------|---|------|------|------|----------|
| V _{(BR)DSS} | drain-source breakdown voltage | $I_D = 10 \ \mu A; \ V_{GS} = 0$ | 250 | _ | _ | V |
| I _{GSS} | gate-source leakage current | $V_{GS} = \pm 20 \text{ V}; V_{DS} = 0$ | - | _ | ±100 | nA |
| V _{GSth} | gate-source threshold voltage | $I_D = 1 \text{ mA}; V_{DS} = V_{GS}$ | 0.8 | _ | 2 | V |
| R _{DSon} | drain-source on-state resistance | $I_D = 20 \text{ mA}; V_{GS} = 2.4 \text{ V}$ | - | _ | 7.5 | Ω |
| | | $I_D = 300 \text{ mA}; \text{ V}_{GS} = 10 \text{ V}$ | _ | 2.8 | 5 | Ω |
| I _{DSS} | drain-source leakage current | $V_{DS} = 200 \text{ V}; V_{GS} = 0$ | _ | _ | 1 | μA |
| Y _{fs} | transfer admittance | I _D = 300 mA; V _{DS} = 25 V | 200 | 600 | _ | mS |
| C _{iss} | input capacitance | V _{DS} = 25 V; V _{GS} = 0; f = 1 MHz | _ | 100 | 120 | pF |
| C _{oss} | output capacitance | V _{DS} = 25 V; V _{GS} = 0; f = 1 MHz | _ | 21 | 30 | pF |
| C _{rss} | feedback capacitance | V _{DS} = 25 V; V _{GS} = 0; f = 1 MHz | _ | 10 | 15 | pF |
| Switching ti | mes (see Figs 2 and 3) | | | | | <u> </u> |
| t _{on} | turn-on time | $I_D = 250 \text{ mA}; V_{DD} = 50 \text{ V};$ $V_{GS} = 0 \text{ to } 10 \text{ V}$ | - | 6 | 10 | ns |
| t _{off} | turn-off time | $I_D = 250 \text{ mA}; V_{DD} = 50 \text{ V};$ $V_{GS} = 10 \text{ to } 0 \text{ V}$ | - | 47 | 60 | ns |

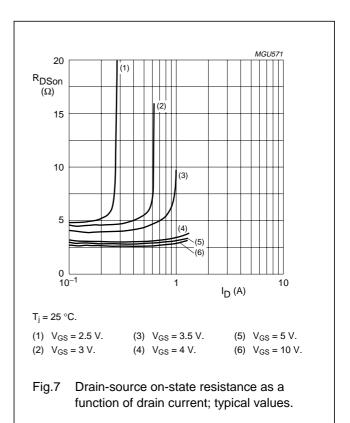
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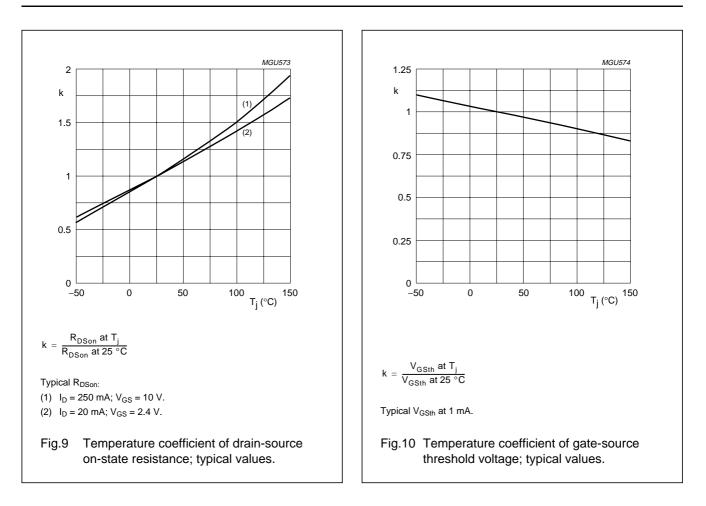
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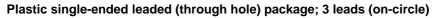


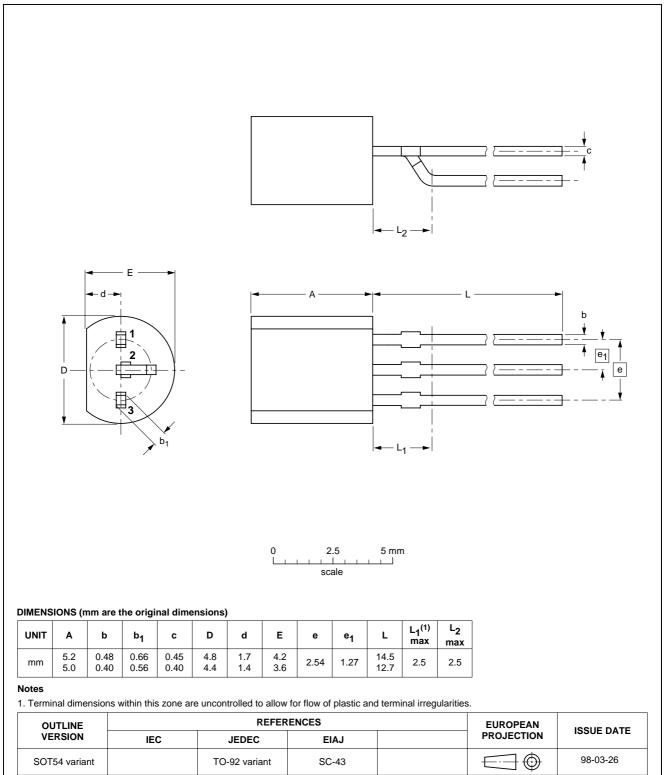
SOT54 variant

N-channel enhancement mode vertical D-MOS transistor

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PACKAGE OUTLINE





BSN254; BSN254A

DATA SHEET STATUS

| DATA SHEET STATUS ⁽¹⁾ | PRODUCT STATUS ⁽²⁾ | DEFINITIONS |
|----------------------------------|----------------------------------|--|
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NOTES

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