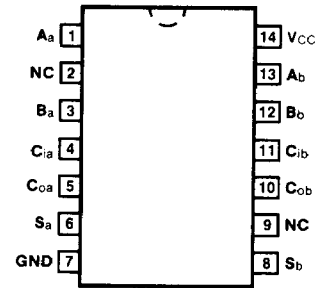
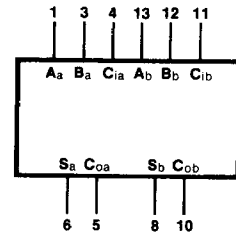


✓ **54H/74H183** 610012  
**DUAL HIGH SPEED ADDER**

**CONNECTION DIAGRAM**  
**PINOUT A**



**LOGIC SYMBOL**



Vcc = Pin 14  
 GND = Pin 7

**DESCRIPTION** — The '183 contains two independent full adders. Each adder has an individual carry output for use in multiple-input, carry-save techniques to produce the true sum and true carry outputs with no more than two gate delays. Typical propagation delay is 12 ns.

**ORDERING CODE:** See Section 9

PKGS	PIN OUT	COMMERCIAL GRADE	MILITARY GRADE	PKG TYPE
		Vcc = +5.0 V ±5%, TA = 0°C to +70°C	Vcc = +5.0 V ±10%, TA = -55°C to +125°C	
Plastic DIP (P)	A	74H183PC		9A
Ceramic DIP (D)	A	74H183DC	54H183DM	6A
Flatpak (F)	A	74H183FC	54H183FM	3I

**TRUTH TABLE**  
 (Each Half)

INPUTS			OUTPUTS	
A	B	C <sub>i</sub>	S	C <sub>o</sub>
L	L	L	L	L
H	L	L	H	L
L	H	L	H	L
L	L	H	H	L
H	H	L	L	H
H	L	H	L	H
L	H	H	L	H
H	H	H	H	H

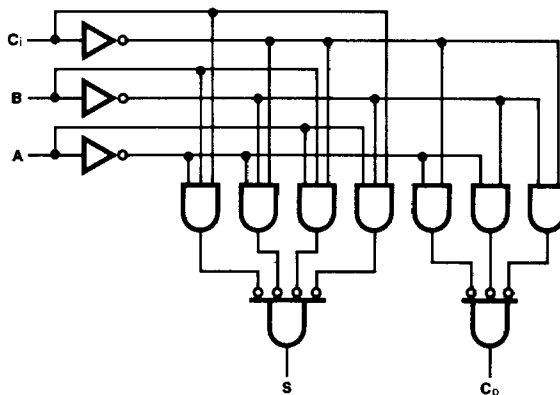
H = HIGH Voltage Level  
 L = LOW Voltage Level

**INPUT LOADING/FAN-OUT:** See Section 3 for U.L. definitions

PIN NAMES	DESCRIPTION	54/74H (U.L.) HIGH/LOW
Aa, Ba	Side a Operand Inputs	3.75/3.75
Ab, Bb	Side b Operand Inputs	3.75/3.75
Cia, Cib	Carry Inputs	3.75/3.75
Sa, Sb	Sum Outputs	25/12.5
Coa, Cob	Carry Outputs	25/12.5

4

## LOGIC DIAGRAM



## DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

SYMBOL	PARAMETER	54/74H		UNITS	CONDITIONS
		Min	Max		
I <sub>CC</sub>	Power Supply Current	X <sub>M</sub>	69	mA	V <sub>CC</sub> = Max All Inputs = Gnd
		X <sub>C</sub>	75		

AC CHARACTERISTICS: V<sub>CC</sub> = +5.0 V, T<sub>A</sub> = +25° C (See Section 3 for waveforms and load configurations)

SYMBOL	PARAMETER	54/74H		UNITS	CONDITIONS
		C <sub>L</sub> = 25 pF R <sub>L</sub> = 280 Ω			
		Min	Max		
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay A <sub>x</sub> , B <sub>x</sub> or C <sub>ix</sub> to S <sub>x</sub>		15 18	ns	Figs. 3-1, 3-20
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay A <sub>x</sub> , B <sub>x</sub> or C <sub>ix</sub> to C <sub>ox</sub>		15 18		