

N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

V _{DSS}	R _{DS(ON)} Max	I _D Max T _C = +25°C
30V	6.5mΩ @ V _{GS} = 10V	46.2A
30 V	10mΩ @ V _{GS} = 4.5V	37.0A

Description

This MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

- Backlighting
- Power Management Functions
- DC-DC Converters

V-DFN3030-8 (Type Q)

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- · Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

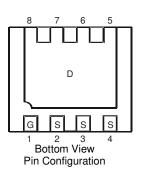
Mechanical Data

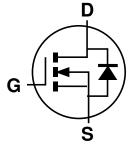
- Case: V-DFN3030-8 (Type Q)
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.0172 grams (Approximate)





Bottom View





Equivalent Circuit

Ordering Information (Note 4)

Top View

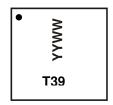
-			
Part Number		Case Packaging	
	DMT3006LDK-7	V-DFN3030-8 (Type Q)	3,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information

V-DFN3030-8 (Type Q)



T39 = Product Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 17 for 2017) WW = Week Code (01 to 53)



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	$V_{ m DSS}$	30	V	
Gate-Source Voltage	V _{GSS}	±20	V	
		I _D	17.1 13.7	А
	$T_C = +25$ °C $T_C = +70$ °C	I _D	46.2 37.0	А
Maximum Continuous Body Diode Forward Current (Note	Is	2	Α	
Pulsed Drain Current (10µS Pulse, Duty Cycle = 1%)	I _{DM}	80	Α	
Avalanche Current (Note 7) L = 0.1mH	I _{AS}	25	Α	
Avalanche Energy (Note 7) L = 0.1mH	Eas	31	mJ	

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 5)	P_D	1.1	W	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{ hetaJA}$	116	°C/W
Total Power Dissipation (Note 6)		P_{D}	2.8	W
Thermal Resistance, Junction to Ambient (Note 6) Steady State		$R_{ hetaJA}$	44	°C/W
Thermal Resistance, Junction to Case	$R_{ heta JC}$	6	C/VV	
Operating and Storage Temperature Range		$T_{J_{I}}T_{STG}$	-55 to +150	°C

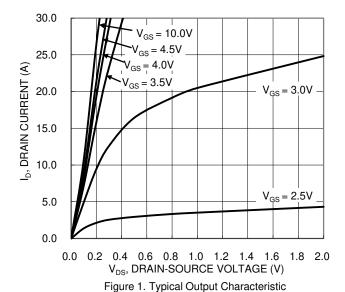
Electrical Characteristics (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	30		_	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}		l	1	μA	$V_{DS} = 24V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	-		±100	nA	$V_{GS} = +20V, V_{DS} = 0V$ $V_{GS} = -16V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(TH)}	1.0	-	3.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance		_	5.5	6.5	mΩ	$V_{GS} = 10V, I_D = 12A$	
Static Dialif-Source Off-Nesistance	RDS(ON)	_	7.5	10	11122	$V_{GS} = 4.5V, I_D = 12A$	
Diode Forward Voltage	V_{SD}	_	_	1.0	V	$V_{GS} = 0V$, $I_S = 2A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}		1,155			V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz	
Output Capacitance	Coss	l	456	_	рF		
Reverse Transfer Capacitance	Crss		72	_			
Gate Resistance	R_g		1.6		Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = 10V)	Qg		16.7	_		V _{DD} = 15V, I _D = 9A	
Total Gate Charge (V _{GS} = 4.5V)	Q_{g}		8.4		nC		
Gate-Source Charge	Q _{gs}	_	2.2	_	IIC		
Gate-Drain Charge	Q_{gd}	1	3.5	_			
Turn-On Delay Time	t _{D(ON)}		3.5	_			
Turn-On Rise Time	t _R	_	5.5	_		$\begin{split} V_{DD} &= 15 V, \ V_{GS} = 10 V, \\ R_g &= 3 \Omega, \ I_D = 9 A \end{split}$	
Turn-Off Delay Time	t _{D(OFF)}	_	13.5	_	ns		
Turn-Off Fall Time	t _F	_	4.6	_			
Body Diode Reverse Recovery Time	t _{RR}	_	19.3	_	ns	1 1 5 A di/dt 100 A /:	
Body Diode Reverse Recovery Charge	Q _{RR}	_	8.6	_	nC	$I_F = 1.5A$, di/dt = 100A/ μ s	

5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.

- 6. Device mounted on 4.75 inches by 4.5 inches FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
- 7. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
- 8. Short duration pulse test used to minimize self-heating effect.
 9. Guaranteed by design. Not subject to product testing.





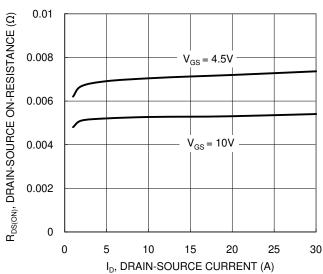


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

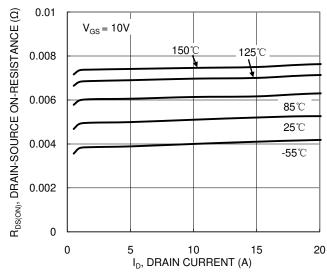


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

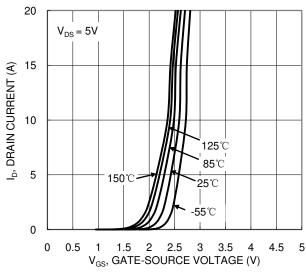


Figure 2. Typical Transfer Characteristic

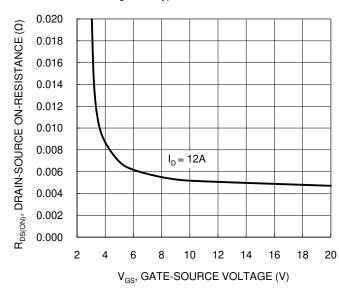
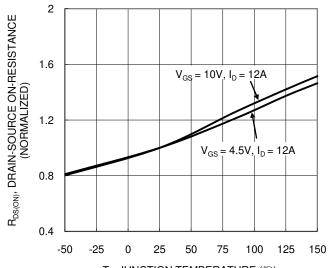


Figure 4. Typical Transfer Characteristic



 $T_{J},$ JUNCTION TEMPERATURE $(^{\circlearrowright})$ Figure 6. On-Resistance Variation with Temperature





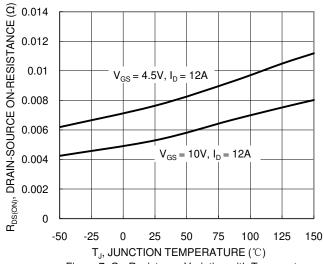


Figure 7. On-Resistance Variation with Temperature

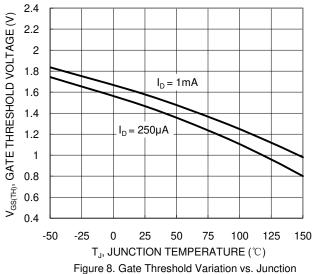
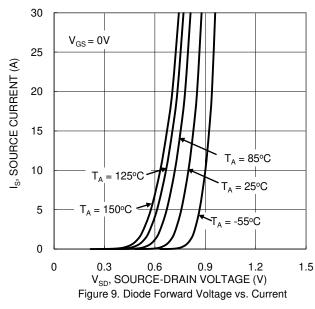
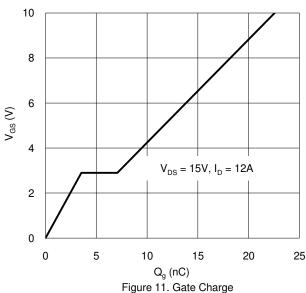
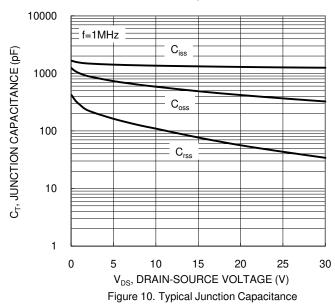
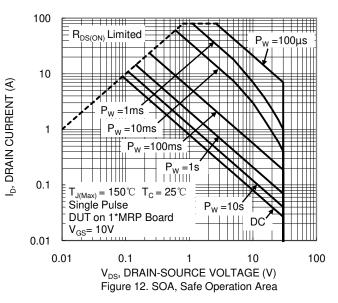


Figure 8. Gate Threshold Variation vs. Junctio Temperature











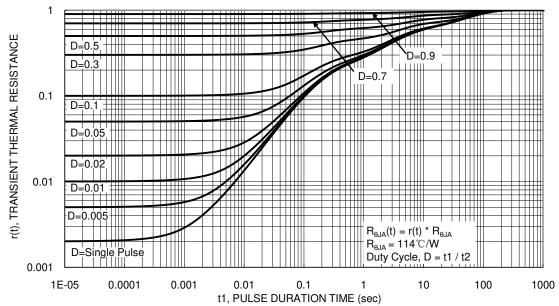


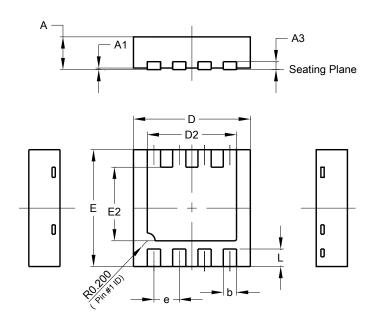
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

V-DFN3030-8 (Type Q)

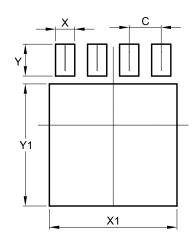


V-DFN3030-8 (Type Q)					
Dim	Min	Max	Тур		
Α	0.77	0.83	0.80		
A1	0.00	0.05	0.02		
А3			0.203		
b	0.29	0.39	0.34		
D	2.95	3.05	3.00		
D2	2.19	2.39	2.29		
Е	2.95	3.05	3.00		
E2	1.64	1.84	1.74		
е			0.65		
L	0.40	0.50	0.45		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

V-DFN3030-8 (Type Q)



Dimensions	Value (in mm)		
С	0.650		
Х	0.390		
X1	2.590		
Υ	0.650		
Y1	2.490		



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